



SPEC® CFP2006 Result

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Dell Inc.

SPECfp®2006 = 25.5

PowerEdge R610 (Intel Xeon E5503, 2.00 GHz)

SPECfp_base2006 = 23.7

CPU2006 license: 55

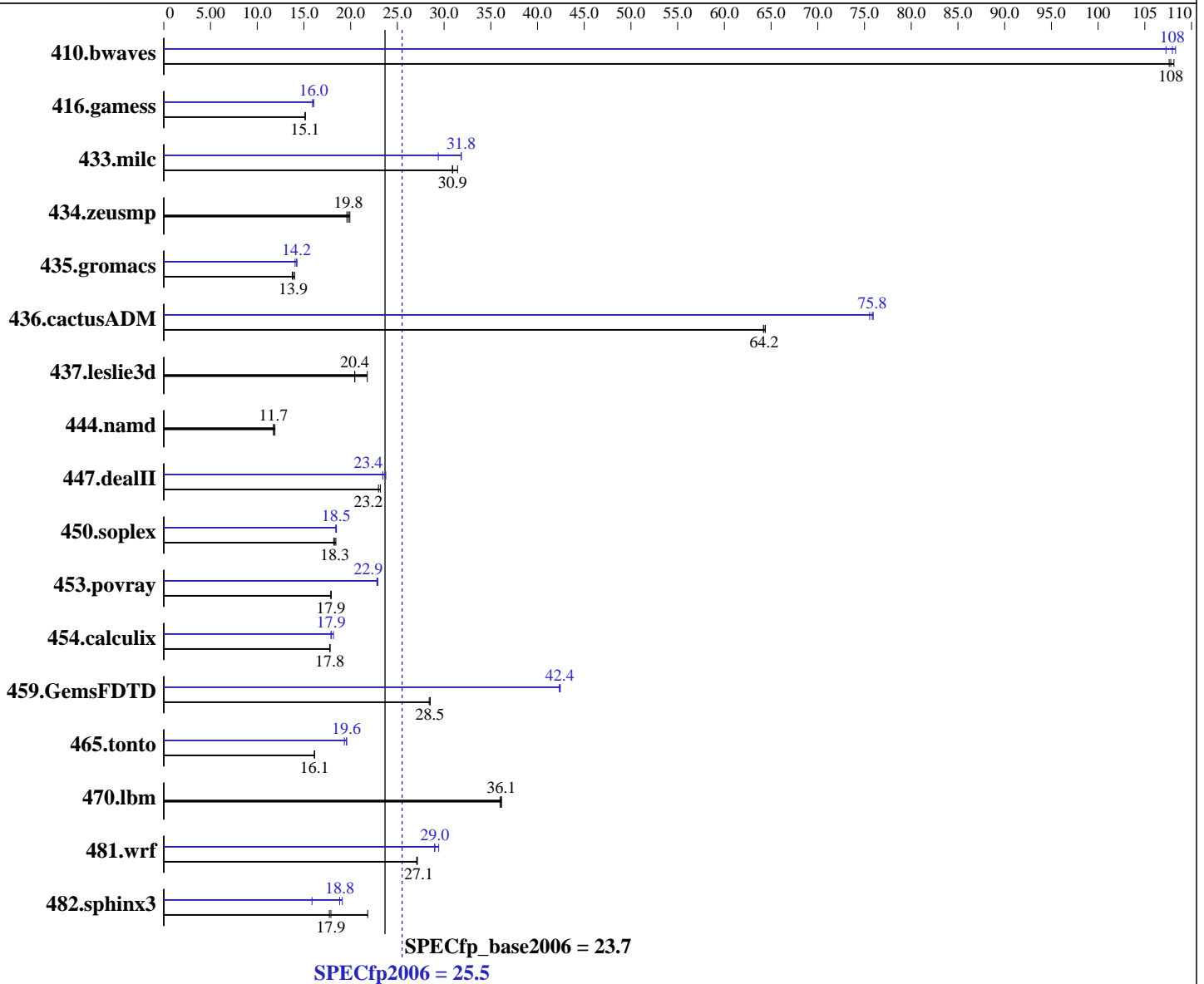
Test date: May-2010

Test sponsor: Dell Inc.

Hardware Availability: Mar-2010

Tested by: Dell Inc.

Software Availability: Dec-2009



Hardware

CPU Name: Intel Xeon E5503
 CPU Characteristics:
 CPU MHz: 2000
 FPU: Integrated
 CPU(s) enabled: 4 cores, 2 chips, 2 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

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Software

Operating System: SUSE Linux Enterprise Server 11 (x86_64), Kernel 2.6.27.19-5-smpp
 Compiler: Intel C++ and Fortran Professional Compiler for IA32 and Intel 64, Version 11.1 Build 20091130 Package ID: l_cproc_p_11.1.064, l_cprof_p_11.1.064
 Auto Parallel: Yes
 File System: ext3
 System State: Run level 3 (multi-user)

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L3 Cache: 4 MB I+D on chip per chip
 Other Cache: None
 Memory: 48 GB (12 x 4 GB DDR3-1333 DR RDIMM, CL9, ECC, downclocked to 800 MHz)
 Disk Subsystem: 1 x 146 GB 15000 RPM SAS
 Other Hardware: None

Base Pointers: 64-bit
 Peak Pointers: 32/64-bit
 Other Software: None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	126	108	<u>126</u>	<u>108</u>	126	108	<u>126</u>	<u>108</u>	127	107	125	108
416.gamess	<u>1294</u>	<u>15.1</u>	1295	15.1	1293	15.1	1230	15.9	1220	16.0	<u>1221</u>	<u>16.0</u>
433.milc	292	31.4	297	30.9	<u>297</u>	<u>30.9</u>	<u>288</u>	<u>31.8</u>	288	31.8	312	29.4
434.zeusmp	<u>460</u>	<u>19.8</u>	457	19.9	464	19.6	<u>460</u>	<u>19.8</u>	457	19.9	464	19.6
435.gromacs	<u>515</u>	<u>13.9</u>	519	13.8	510	14.0	501	14.2	<u>502</u>	<u>14.2</u>	508	14.1
436.cactusADM	186	64.4	186	64.2	<u>186</u>	<u>64.2</u>	158	75.5	<u>158</u>	<u>75.8</u>	157	75.9
437.leslie3d	460	20.4	432	21.8	<u>460</u>	<u>20.4</u>	460	20.4	432	21.8	<u>460</u>	<u>20.4</u>
444.namd	<u>683</u>	<u>11.7</u>	674	11.9	683	11.7	<u>683</u>	<u>11.7</u>	674	11.9	683	11.7
447.dealII	498	23.0	<u>494</u>	<u>23.2</u>	493	23.2	481	23.8	<u>488</u>	<u>23.4</u>	488	23.4
450.soplex	453	18.4	459	18.2	<u>456</u>	<u>18.3</u>	453	18.4	<u>452</u>	<u>18.5</u>	451	18.5
453.povray	297	17.9	<u>297</u>	<u>17.9</u>	298	17.9	232	22.9	<u>233</u>	<u>22.9</u>	233	22.8
454.calculix	463	17.8	465	17.8	<u>465</u>	<u>17.8</u>	461	17.9	454	18.2	<u>460</u>	<u>17.9</u>
459.GemsFDTD	374	28.4	<u>372</u>	<u>28.5</u>	372	28.5	251	42.3	<u>250</u>	<u>42.4</u>	250	42.4
465.tonto	609	16.2	<u>610</u>	<u>16.1</u>	611	16.1	509	19.3	503	19.6	<u>503</u>	<u>19.6</u>
470.lbm	382	36.0	380	36.1	<u>380</u>	<u>36.1</u>	382	36.0	380	36.1	<u>380</u>	<u>36.1</u>
481.wrf	413	27.1	<u>412</u>	<u>27.1</u>	412	27.1	<u>385</u>	<u>29.0</u>	380	29.4	385	29.0
482.sphinx3	<u>1089</u>	<u>17.9</u>	1100	17.7	893	21.8	<u>1035</u>	<u>18.8</u>	1228	15.9	1020	19.1

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

Platform Notes

BIOS Settings:
Power Management = Maximum Performance (Default = Active Power Controller)

General Notes

OMP_NUM_THREADS set to number of cores
KMP_AFFINITY set to granularity=fine,scatter
KMP_STACKSIZE set to 200M

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General Notes (Continued)

Binaries were compiled on SLES 10 with Binutils 2.18.50.0.7.20080502
The Dell PowerEdge R610 and
the Bull NovaScale R440 F2 models are electronically equivalent.
The results have been measured on a Dell PowerEdge R610 model.

Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

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Base Optimization Flags (Continued)

Fortran benchmarks:

`-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch`

Benchmarks using both Fortran and C:

`-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch`

Peak Compiler Invocation

C benchmarks:

`icc -m64`

C++ benchmarks:

`icpc -m64`

Fortran benchmarks:

`ifort -m64`

Benchmarks using both Fortran and C:

`icc -m64 ifort -m64`

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-ansi-alias`

470.lbm: `basepeak = yes`

482.sphinx3: `-xSSE4.2 -ipo -O3 -no-prec-div -static -auto-ilp32
-unroll2`

C++ benchmarks:

444.namd: `basepeak = yes`

447.dealII: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll2 -ansi-alias -scalar-rep- -auto-ilp32`

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Peak Optimization Flags (Continued)

450.soplex: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-opt-malloc-options=3 -auto-ilp32

453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch
-parallel

416.gamess: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll2 -Ob0 -ansi-alias -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll2 -Ob0 -opt-prefetch -parallel

465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-inline-calloc -opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-opt-prefetch -auto-ilp32

436.cactusADM: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll2 -opt-prefetch -parallel -auto-ilp32

454.calculix: -xSSE4.2 -ipo -O3 -no-prec-div -static -auto-ilp32

481.wrf: Same as 454.calculix

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100330.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100330.xml>



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Tested with SPEC CPU2006 v1.1.
Report generated on Wed Jul 23 09:06:44 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 9 June 2010.