



SPEC[®] CFP2006 Result

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Itaotec

SPECfp[®]_rate2006 = 225

Servidor Itaotec LX213 (Intel Xeon X5670)

SPECfp_rate_base2006 = 217

CPU2006 license: 9001

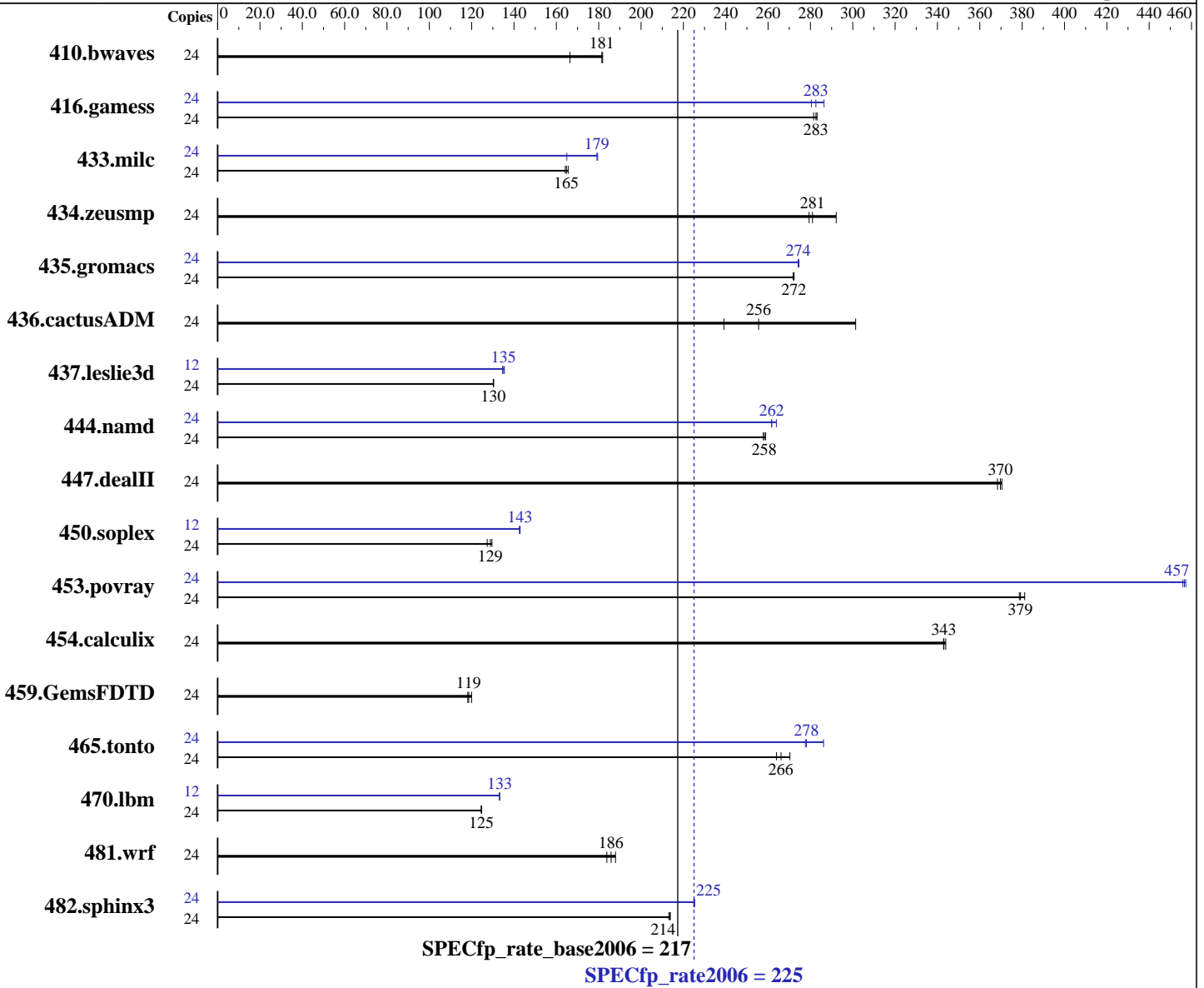
Test sponsor: Itaotec

Tested by: Itaotec

Test date: May-2010

Hardware Availability: Apr-2010

Software Availability: Apr-2010



Hardware

CPU Name: Intel Xeon X5670
 CPU Characteristics: Intel Turbo Boost Technology up to 3.33 GHz
 CPU MHz: 2933
 FPU: Integrated
 CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip, 2 threads/core
 CPU(s) orderable: 1, 2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

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Software

Operating System: SUSE Linux Enterprise Server 11 (x86_64), Kernel 2.6.27.19-5-smp
 Compiler: Intel C++ and Fortran Professional Compiler 11.1 for Linux
 Build 20100414 Package ID: l_cproc_p_11.1.072, l_cprof_p_11.1.072
 Auto Parallel: No
 File System: ReiserFS
 System State: Run level 3 (multi-user)

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L3 Cache: 12 MB I+D on chip per chip
Other Cache: None
Memory: 24 GB (6 x 4GB, DDR3-1333, Dual Rank, CL 9, ECC)
Disk Subsystem: 1 x 160 GB SATA-2, 7200 RPM
Other Hardware: None

Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: Binutils 2.18.50.0.7.20080502

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	24	1960	166	1793	182	<u>1797</u>	<u>181</u>	24	1960	166	1793	182	<u>1797</u>	<u>181</u>
416.gamess	24	1660	283	<u>1663</u>	<u>283</u>	1669	282	24	1641	286	1676	280	<u>1663</u>	<u>283</u>
433.milc	24	1342	164	<u>1337</u>	<u>165</u>	1331	166	24	1336	165	1227	179	<u>1230</u>	<u>179</u>
434.zeusmp	24	747	292	782	279	<u>777</u>	<u>281</u>	24	747	292	782	279	<u>777</u>	<u>281</u>
435.gromacs	24	630	272	<u>630</u>	<u>272</u>	629	272	24	<u>625</u>	<u>274</u>	624	275	625	274
436.cactusADM	24	1199	239	<u>1122</u>	<u>256</u>	952	301	24	1199	239	<u>1122</u>	<u>256</u>	952	301
437.leslie3d	24	1730	130	<u>1732</u>	<u>130</u>	1733	130	12	838	135	<u>835</u>	<u>135</u>	833	135
444.namd	24	744	259	<u>745</u>	<u>258</u>	747	258	24	736	262	<u>735</u>	<u>262</u>	729	264
447.dealII	24	745	368	741	370	<u>743</u>	<u>370</u>	24	745	368	741	370	<u>743</u>	<u>370</u>
450.soplex	24	1545	130	<u>1554</u>	<u>129</u>	1572	127	12	702	143	<u>701</u>	<u>143</u>	700	143
453.povray	24	<u>337</u>	<u>379</u>	335	381	337	379	24	279	457	280	456	<u>280</u>	<u>457</u>
454.calculix	24	576	344	577	343	<u>577</u>	<u>343</u>	24	576	344	577	343	<u>577</u>	<u>343</u>
459.GemsFDTD	24	2123	120	2155	118	<u>2147</u>	<u>119</u>	24	2123	120	2155	118	<u>2147</u>	<u>119</u>
465.tonto	24	<u>887</u>	<u>266</u>	874	270	895	264	24	851	278	825	286	<u>849</u>	<u>278</u>
470.lbm	24	2644	125	2649	124	<u>2646</u>	<u>125</u>	12	1238	133	<u>1238</u>	<u>133</u>	1239	133
481.wrf	24	1458	184	1426	188	<u>1442</u>	<u>186</u>	24	1458	184	1426	188	<u>1442</u>	<u>186</u>
482.sphinx3	24	<u>2189</u>	<u>214</u>	2188	214	2195	213	24	2076	225	2080	225	<u>2078</u>	<u>225</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
numactl was used to bind copies to the cores

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run.

General Notes

This result was measured on the Servidor Itaotec LX203.
The Servidor Itaotec LX203, the Servidor Itaotec LX204 and the Servidor Itaotec LX213 are electronically equivalent.



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Test date: May-2010
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Base Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static

C++ benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static

Fortran benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static

Benchmarks using both Fortran and C:
-xSSE4.2 -ipo -O3 -no-prec-div -static



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Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

482.sphinx3: icc -m32

C++ benchmarks (except as noted below):

icpc -m64

450.soplex: icpc -m32

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

433.milc: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-fno-alias -opt-prefetch

470.lbm: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-opt-malloc-options=3 -ansi-alias -auto-ilp32

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Peak Optimization Flags (Continued)

482.sphinx3: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2

C++ benchmarks:

444.namd: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-fno-alias -auto-ilp32

447.deallI: basepeak = yes

450.soplex: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-opt-malloc-options=3

453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll2 -Ob0 -ansi-alias -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: -xSSE4.2 -ipo -O3 -no-prec-div -static

459.GemsFDTD: basepeak = yes

465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll4 -auto -inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-opt-prefetch -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes



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Software Availability: Apr-2010

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Itautec-Intel-ic11.1-linux64-revE.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Itautec-Intel-ic11.1-linux64-revE.xml>

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