



SPEC® CINT2006 Result

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IBM Corporation

SPECint®_rate2006 = 508

IBM System x3850 X5 (Intel Xeon L7545)

SPECint_rate_base2006 = 472

CPU2006 license: 11

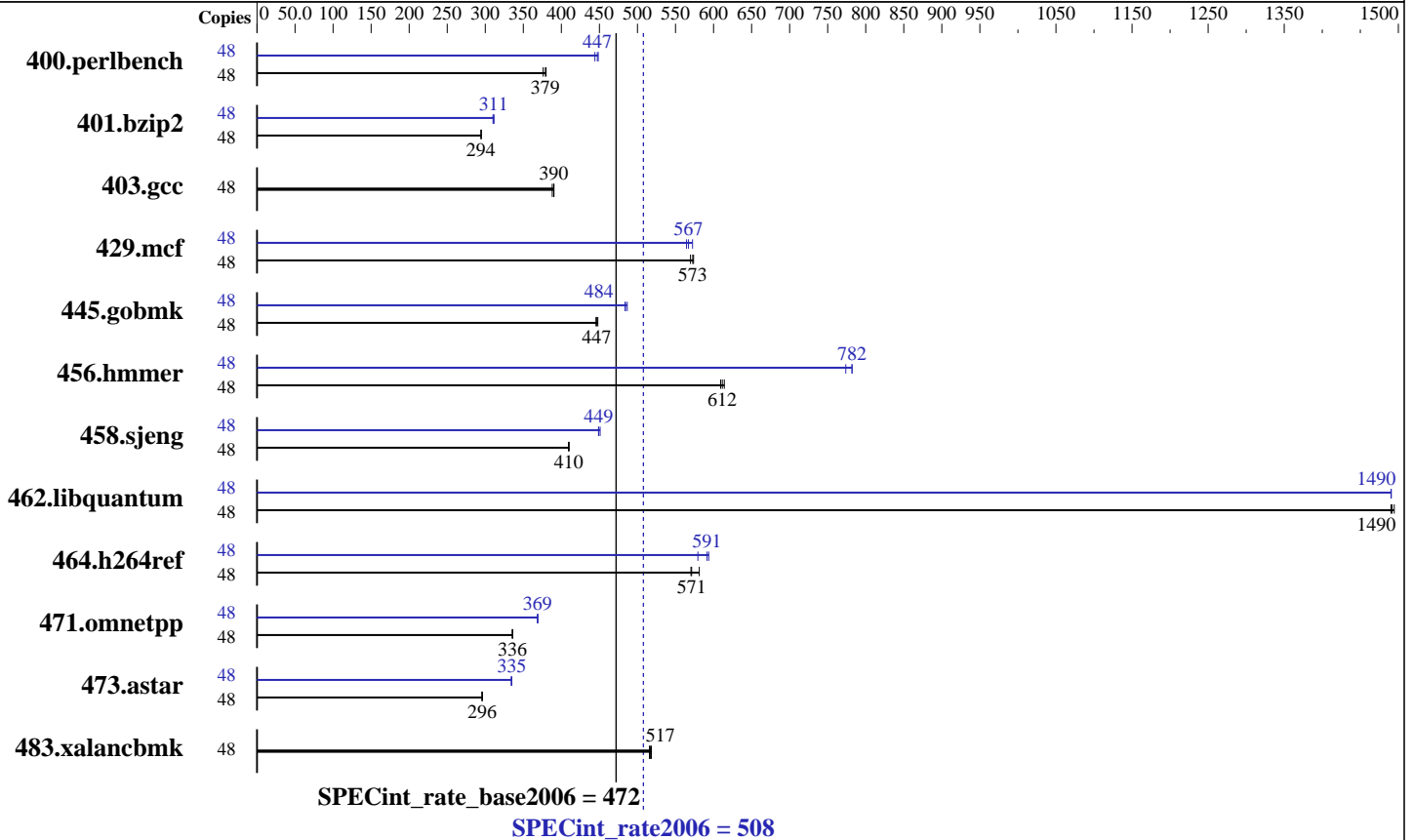
Test sponsor: IBM Corporation

Tested by: IBM Corporation

Test date: Apr-2010

Hardware Availability: Mar-2010

Software Availability: Jan-2010



Hardware

CPU Name: Intel Xeon L7545
 CPU Characteristics: Intel Turbo Boost Technology up to 2.53 GHz
 CPU MHz: 1867
 FPU: Integrated
 CPU(s) enabled: 24 cores, 4 chips, 6 cores/chip, 2 threads/core
 CPU(s) orderable: 2,4 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 18 MB I+D on chip per chip
 Other Cache: None
 Memory: 256 GB (64 x 4 GB PC3-8500R CL7, Quad Rank, running at 978 MHz)
 Disk Subsystem: 3 x 50 GB SATA, SSD
 Other Hardware: None

Software

Operating System: SuSE Linux Enterprise Server 11 (x86_64), Kernel 2.6.27.19-5-default
 Compiler: Intel C++ Professional Compiler for IA32 and Intel 64, Version 11.1
 Build 20091130 Package ID: l_cproc_p_11.1.064
 Auto Parallel: No
 File System: ext3
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V8.1



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	48	<u>1238</u>	<u>379</u>	1247	376	1234	380	48	<u>1048</u>	<u>447</u>	1046	448	1056	444
401.bzip2	48	<u>1573</u>	<u>294</u>	1571	295	1573	294	48	1487	311	<u>1490</u>	<u>311</u>	1493	310
403.gcc	48	<u>991</u>	<u>390</u>	990	390	996	388	48	<u>991</u>	<u>390</u>	990	390	996	388
429.mcf	48	768	570	<u>764</u>	<u>573</u>	763	574	48	765	572	776	564	<u>772</u>	<u>567</u>
445.gobmk	48	1130	445	1124	448	<u>1127</u>	<u>447</u>	48	1035	487	<u>1040</u>	<u>484</u>	1041	484
456.hammer	48	<u>732</u>	<u>612</u>	735	609	729	614	48	573	782	<u>573</u>	<u>782</u>	579	774
458.sjeng	48	1419	409	1416	410	<u>1417</u>	<u>410</u>	48	1288	451	1294	449	<u>1293</u>	<u>449</u>
462.libquantum	48	667	1490	<u>667</u>	<u>1490</u>	665	1490	48	<u>667</u>	<u>1490</u>	667	1490	667	1490
464.h264ref	48	<u>1860</u>	<u>571</u>	1828	581	1863	570	48	1789	594	<u>1796</u>	<u>591</u>	1833	579
471.omnetpp	48	<u>894</u>	<u>336</u>	893	336	894	336	48	813	369	814	369	<u>813</u>	<u>369</u>
473.astar	48	<u>1139</u>	<u>296</u>	1137	296	1140	296	48	<u>1007</u>	<u>335</u>	1009	334	1007	335
483.xalancbmk	48	642	516	<u>641</u>	<u>517</u>	639	518	48	642	516	<u>641</u>	<u>517</u>	639	518

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
numactl was used to bind copies to the cores

Platform Notes

Turbo Boost set to Traditional
Demand Scrub disabled

General Notes

'ulimit -s unlimited' was used to set the stack size to unlimited prior to run
Binaries were compiled on SLES 10 with Binutils 2.18.50.0.7.20080502

Base Compiler Invocation

C benchmarks:
icc -m32

C++ benchmarks:
icpc -m32



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Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmarheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m32

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

462.libquantum: icc -m64

C++ benchmarks (except as noted below):

icpc -m32

473.astar: icpc -m64

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64

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Peak Portability Flags (Continued)

456.hmmcr: -DSPEC_CPU_LP64
 458.sjeng: -DSPEC_CPU_LP64
 462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
 473.astar: -DSPEC_CPU_LP64
 483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
 -prof-use(pass 2) -ansi-alias

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
 -prof-use(pass 2) -opt-prefetch -ansi-alias -auto-ilp32

403.gcc: basepeak = yes

429.mcf: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2
 -ipo -no-prec-div -ansi-alias

456.hmmcr: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2
 -ansi-alias -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
 -prof-use(pass 2) -unroll4 -auto-ilp32

462.libquantum: -xSSE4.2 -ipo -O3 -no-prec-div -static -auto-ilp32
 -opt-prefetch

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
 -prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
 -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
 -L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmartheap

473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
 -ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs

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Peak Optimization Flags (Continued)

473.astar (continued):

-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-64bit -lsmartheap64

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100603.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100603.xml>

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For other inquiries, please contact webmaster@spec.org.

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