SPEC® CINT2006 Result
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NEC Corporation

Express5800/GT110b-S
(Intel Core i3-540)

SPECint®_rate2006 = 61.9
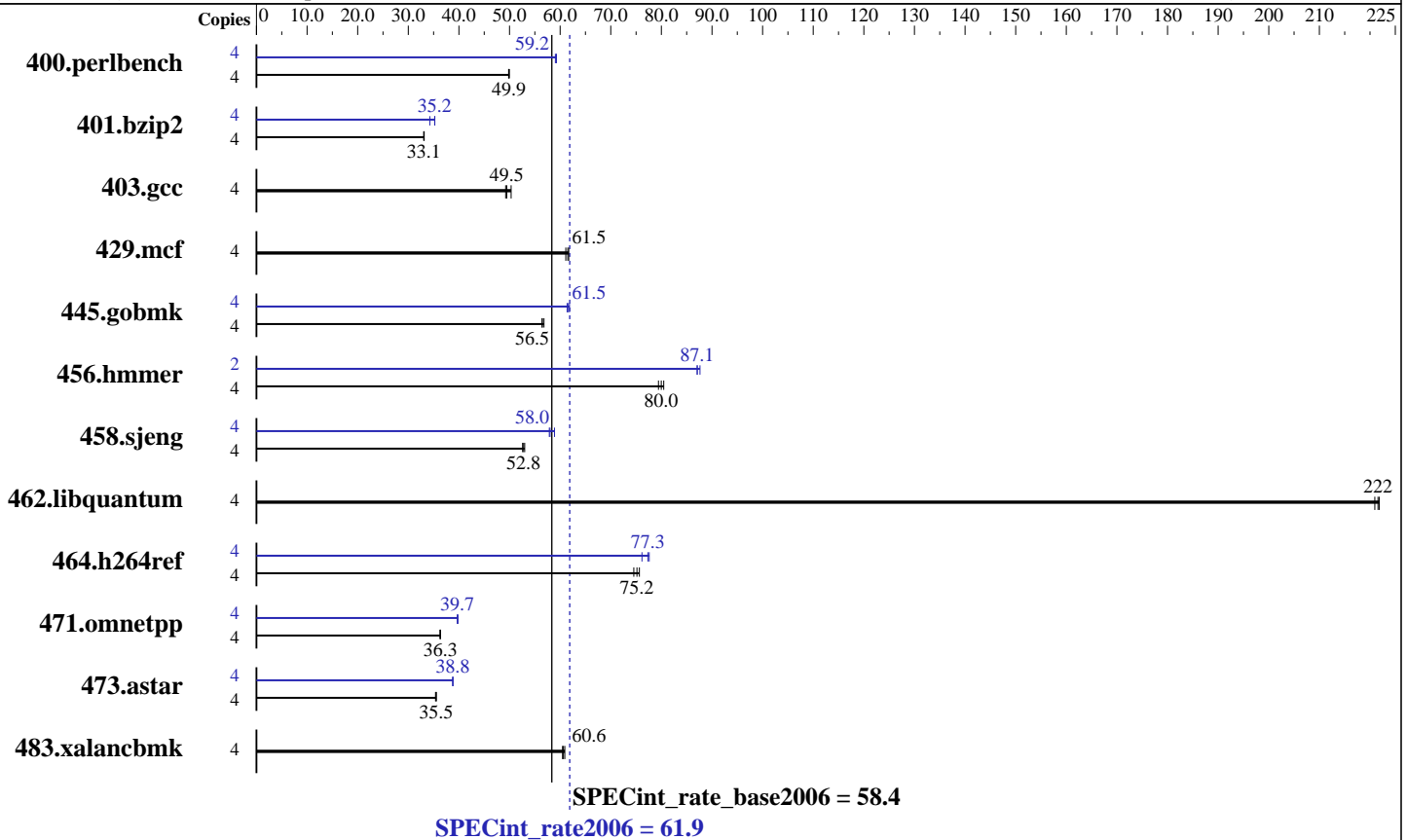
SPECint_rate_base2006 = 58.4

CPU2006 license: 9006
Test sponsor: NEC Corporation
Tested by: NEC Corporation

Test date: Feb-2010
Hardware Availability: Apr-2010
Software Availability: Nov-2009

SPECint_rate_base2006 = 58.4

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Hardware

CPU Name:	Intel Core i3-540
CPU Characteristics:	
CPU MHz:	3067
FPU:	Integrated
CPU(s) enabled:	2 cores, 1 chip, 2 cores/chip, 2 threads/core
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	4 MB I+D on chip per chip
Other Cache:	None
Memory:	8 GB (4 x 2 GB PC3-10600E, 2 rank, CL9, ECC)
Disk Subsystem:	1x160 GB SATA, 7200 RPM
Other Hardware:	None

Software

Operating System:	SUSE Linux Enterprise Server 11 (x86_64), Kernel 2.6.27.19-5-default
Compiler:	Intel C++ Professional Compiler for IA32 and Intel 64, Version 11.1 Build 20091012 Package ID: l_cproc_p_11.1.059
Auto Parallel:	No
File System:	ext3
System State:	Run level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V8.1

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Results Table

Benchmark	Base						Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	782	50.0	784	49.8	**784**	**49.9**	4	662	59.1	**660**	**59.2**	660	59.2
401.bzip2	4	**1167**	**33.1**	1165	33.1	1168	33.1	4	1095	35.2	1127	34.3	**1096**	**35.2**
403.gcc	4	654	49.2	640	50.3	**651**	**49.5**	4	654	49.2	640	50.3	**651**	**49.5**
429.mcf	4	591	61.7	597	61.1	**593**	**61.5**	4	591	61.7	597	61.1	**593**	**61.5**
445.gobmk	4	743	56.5	739	56.8	**743**	**56.5**	4	684	61.4	678	61.8	**682**	**61.5**
456.hmmer	4	**467**	**80.0**	470	79.4	464	80.5	2	214	87.1	213	87.6	**214**	**87.1**
458.sjeng	4	913	53.0	**917**	**52.8**	921	52.6	4	822	58.9	836	57.9	**834**	**58.0**
462.libquantum	4	**374**	**222**	374	222	375	221	4	**374**	**222**	374	222	375	221
464.h264ref	4	1170	75.7	**1178**	**75.2**	1187	74.6	4	1141	77.6	**1145**	**77.3**	1162	76.2
471.omnetpp	4	688	36.3	689	36.3	**688**	**36.3**	4	628	39.8	630	39.7	**630**	**39.7**
473.astar	4	794	35.4	**791**	**35.5**	790	35.5	4	724	38.8	**724**	**38.8**	724	38.8
483.xalancbmk	4	453	60.9	456	60.5	**455**	**60.6**	4	453	60.9	456	60.5	**455**	**60.6**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
    The config file option 'submit' was used.
     numactl was used to bind copies to the cores
```

Operating System Notes

```
     'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
```

Platform Notes

```
    Default BIOS settings were used.
```

Base Compiler Invocation

```
C benchmarks:
     icc -m32

C++ benchmarks:
     icpc -m32
```

Base Portability Flags

```
    400.perlbench: -DSPEC_CPU_LINUX_IA32
```

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Base Portability Flags (Continued)

462.libquantum: `-DSPEC_CPU_LINUX`
483.xalancbmk: `-DSPEC_CPU_LINUX`

Base Optimization Flags

C benchmarks:
    `-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch`

C++ benchmarks:
    `-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs`
    `-L/opt/SmartHeap_8.1/lib -lsmartheap`

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks (except as noted below):
    `icc   -m32`

401.bzip2: `icc -m64`

456.hmmer: `icc -m64`

458.sjeng: `icc -m64`

C++ benchmarks (except as noted below):
    `icpc -m32`

473.astar: `icpc -m64`

Peak Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
401.bzip2: `-DSPEC_CPU_LP64`
456.hmmer: `-DSPEC_CPU_LP64`
458.sjeng: `-DSPEC_CPU_LP64`
462.libquantum: `-DSPEC_CPU_LINUX`

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Peak Portability Flags (Continued)

473.astar: `-DSPEC_CPU_LP64`
483.xalancbmk: `-DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

400.perlbench: `-xSSE4.2(pass 2)` `-prof-gen(pass 1)` `-ipo(pass 2)`
`-O3(pass 2)` `-no-prec-div(pass 2)` `-static(pass 2)`
`-prof-use(pass 2)` `-ansi-alias`

401.bzip2: `-xSSE4.2(pass 2)` `-prof-gen(pass 1)` `-ipo(pass 2)`
`-O3(pass 2)` `-no-prec-div(pass 2)` `-static(pass 2)`
`-prof-use(pass 2)` `-opt-prefetch` `-ansi-alias` `-auto-ilp32`

403.gcc: `basepeak = yes`

429.mcf: `basepeak = yes`

445.gobmk: `-xSSE4.2(pass 2)` `-prof-gen(pass 1)` `-prof-use(pass 2)` `-O2`
`-ipo` `-no-prec-div` `-ansi-alias`

456.hmmer: `-xSSE4.2` `-ipo` `-O3` `-no-prec-div` `-static` `-unroll2`
`-ansi-alias` `-auto-ilp32`

458.sjeng: `-xSSE4.2(pass 2)` `-prof-gen(pass 1)` `-ipo(pass 2)`
`-O3(pass 2)` `-no-prec-div(pass 2)` `-static(pass 2)`
`-prof-use(pass 2)` `-unroll4` `-auto-ilp32`

462.libquantum: `basepeak = yes`

464.h264ref: `-xSSE4.2(pass 2)` `-prof-gen(pass 1)` `-ipo(pass 2)`
`-O3(pass 2)` `-no-prec-div(pass 2)` `-static(pass 2)`
`-prof-use(pass 2)` `-unroll2` `-ansi-alias`

C++ benchmarks:

471.omnetpp: `-xSSE4.2(pass 2)` `-prof-gen(pass 1)` `-ipo(pass 2)`
`-O3(pass 2)` `-no-prec-div(pass 2)` `-prof-use(pass 2)`
`-ansi-alias` `-opt-ra-region-strategy=block` `-Wl,-z,muldefs`
`-L/opt/SmartHeap_8.1/lib` `-lsmartheap`

473.astar: `-xSSE4.2(pass 2)` `-prof-gen(pass 1)` `-ipo(pass 2)`
`-O3(pass 2)` `-no-prec-div(pass 2)` `-prof-use(pass 2)`
`-ansi-alias` `-opt-ra-region-strategy=routine` `-Wl,-z,muldefs`
`-L/opt/SmartHeap_8.1/lib64` `-lsmartheap64`

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Peak Optimization Flags (Continued)

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic11.1-int-linux64-revE.20100302.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic11.1-int-linux64-revE.20100302.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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