SPEC® CFP2006 Result

HITACHI

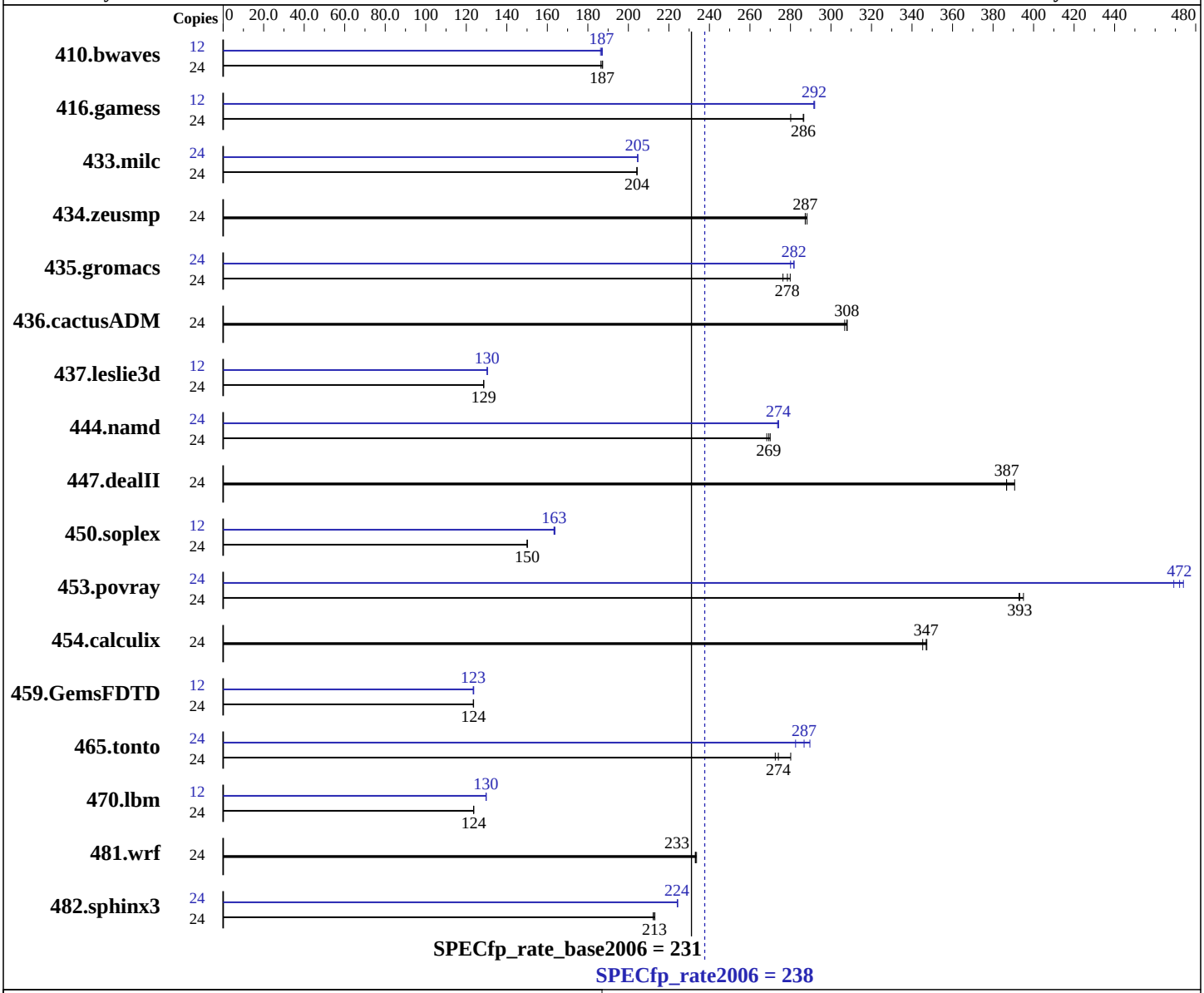
BladeSymphony BS320 (Intel Xeon X5670)

SPECfp®_rate2006 = 238

SPECfp_rate_base2006 = 231

CPU2006 license: 872
Test sponsor: HITACHI
Tested by: HITACHI

Test date: Mar-2010
Hardware Availability: Mar-2010
Software Availability: Dec-2009



Hardware

CPU Name:	Intel Xeon X5670
CPU Characteristics:	Intel Turbo Boost Technology up to 3.33 GHz
CPU MHz:	2933
FPU:	Integrated
CPU(s) enabled:	12 cores, 2 chips, 6 cores/chip, 2 threads/core
CPU(s) orderable:	1, 2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core

Continued on next page

Software

Operating System:	Red Hat Enterprise Linux Server release 5.4.3, Advanced Platform, Kernel 2.6.18-164.9.1.el5 on an x86_64
Compiler:	Intel C++ Compiler 11.1 for Linux Build 20091012 Package ID: l_cproc_p_11.1.059 Intel Fortran Compiler 11.1 for Linux Build 20091012 Package ID: l_cprof_p_11.1.059
Auto Parallel:	No
File System:	ext3

Continued on next page

SPEC CFP2006 Result

HITACHI

BladeSymphony BS320 (Intel Xeon X5670)

SPECfp_rate2006 = 238

SPECfp_rate_base2006 = 231

CPU2006 license: 872
Test sponsor: HITACHI
Tested by: HITACHI

Test date: Mar-2010
Hardware Availability: Mar-2010
Software Availability: Dec-2009

L3 Cache: 12 MB I+D on chip per chip
Other Cache: None
Memory: 48 GB(6 x 8 GB PC3-10600R running at 1333 MHz, 2 rank)
Disk Subsystem: 1 x 147 GB 10000 rpm SAS
Other Hardware: None

System State: Multi-user run level 3
Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	24	1749	186	**1744**	**187**	1741	187	12	876	186	871	187	**873**	**187**
416.gamess	24	**1641**	**286**	1678	280	1640	287	12	805	292	**806**	**292**	806	292
433.milc	24	1078	204	1079	204	**1079**	**204**	24	1077	205	1077	205	**1077**	**205**
434.zeusmp	24	758	288	760	287	**760**	**287**	24	758	288	760	287	**760**	**287**
435.gromacs	24	612	280	**615**	**278**	620	276	24	**608**	**282**	608	282	612	280
436.cactusADM	24	935	307	931	308	**932**	**308**	24	935	307	931	308	**932**	**308**
437.leslie3d	24	1754	129	1755	129	**1755**	**129**	12	865	130	866	130	**866**	**130**
444.namd	24	713	270	**715**	**269**	717	268	24	**702**	**274**	702	274	703	274
447.dealII	24	710	387	703	391	**710**	**387**	24	710	387	703	391	**710**	**387**
450.soplex	24	1333	150	**1334**	**150**	1334	150	12	**613**	**163**	613	163	611	164
453.povray	24	**325**	**393**	323	395	325	393	24	272	469	**270**	**472**	269	474
454.calculix	24	**571**	**347**	573	345	570	347	24	**571**	**347**	573	345	570	347
459.GemsFDTD	24	2062	123	2060	124	**2062**	**124**	12	**1031**	**123**	1031	123	1029	124
465.tonto	24	867	272	843	280	**862**	**274**	24	836	283	**824**	**287**	816	290
470.lbm	24	**2666**	**124**	2666	124	2669	124	12	1269	130	1271	130	**1271**	**130**
481.wrf	24	1150	233	1147	234	**1150**	**233**	24	1150	233	1147	234	**1150**	**233**
482.sphinx3	24	2195	213	**2198**	**213**	2204	212	24	**2086**	**224**	2087	224	2085	224

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The config file option 'submit' was used.
'/usr/bin/numactl' used to bind processes to CPUs
```

Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
```

Base Compiler Invocation

C benchmarks:

```
icc -m64
```

Continued on next page

Base Compiler Invocation (Continued)

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static

C++ benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static

Fortran benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static

Benchmarks using both Fortran and C:
-xSSE4.2 -ipo -O3 -no-prec-div -static

Peak Compiler Invocation

C benchmarks (except as noted below):
`icc -m64`

482.sphinx3: `icc -m32`

C++ benchmarks (except as noted below):
`icpc -m64`

450.soplex: `icpc -m32`

Fortran benchmarks:
`ifort -m64`

Benchmarks using both Fortran and C:
`icc -m64 ifort -m64`

Peak Portability Flags

410.bwaves: `-DSPEC_CPU_LP64`
416.gamess: `-DSPEC_CPU_LP64`
433.milc: `-DSPEC_CPU_LP64`
434.zeusmp: `-DSPEC_CPU_LP64`
435.gromacs: `-DSPEC_CPU_LP64 -nofor_main`
436.cactusADM: `-DSPEC_CPU_LP64 -nofor_main`
437.leslie3d: `-DSPEC_CPU_LP64`
444.namd: `-DSPEC_CPU_LP64`
447.dealII: `-DSPEC_CPU_LP64`
453.povray: `-DSPEC_CPU_LP64`
454.calculix: `-DSPEC_CPU_LP64 -nofor_main`
459.GemsFDTD: `-DSPEC_CPU_LP64`
465.tonto: `-DSPEC_CPU_LP64`
470.lbm: `-DSPEC_CPU_LP64`
481.wrf: `-DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

433.milc: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2)
`-no-prec-div`(pass 2) `-static`(pass 2) `-prof-use`(pass 2)
`-fno-alias -opt-prefetch`

470.lbm: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2)
`-no-prec-div`(pass 2) `-static`(pass 2) `-prof-use`(pass 2)
`-opt-malloc-options=3 -ansi-alias -auto-ilp32`

Continued on next page

Peak Optimization Flags (Continued)

482.sphinx3: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2

C++ benchmarks:

444.namd: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -fno-alias -auto-ilp32

447.dealII: basepeak = yes

450.soplex: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -opt-malloc-options=3

453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

416.gamess: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -unroll2 -Ob0 -ansi-alias -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -opt-malloc-options=3 -opt-prefetch

459.GemsFDTD: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -unroll2 -Ob0

465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -unroll4 -auto -inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -opt-prefetch -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

Continued on next page

Peak Optimization Flags (Continued)

481.wrf: `basepeak = yes`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.xml`

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.
Report generated on Wed Jul 23 07:26:44 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 27 April 2010.