



SPEC® CINT2006 Result

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Hewlett-Packard Company

SPECint®_rate2006 = 356

ProLiant DL360 G7
(2.93 GHz, Intel Xeon X5670)

SPECint_rate_base2006 = 333

CPU2006 license: 3

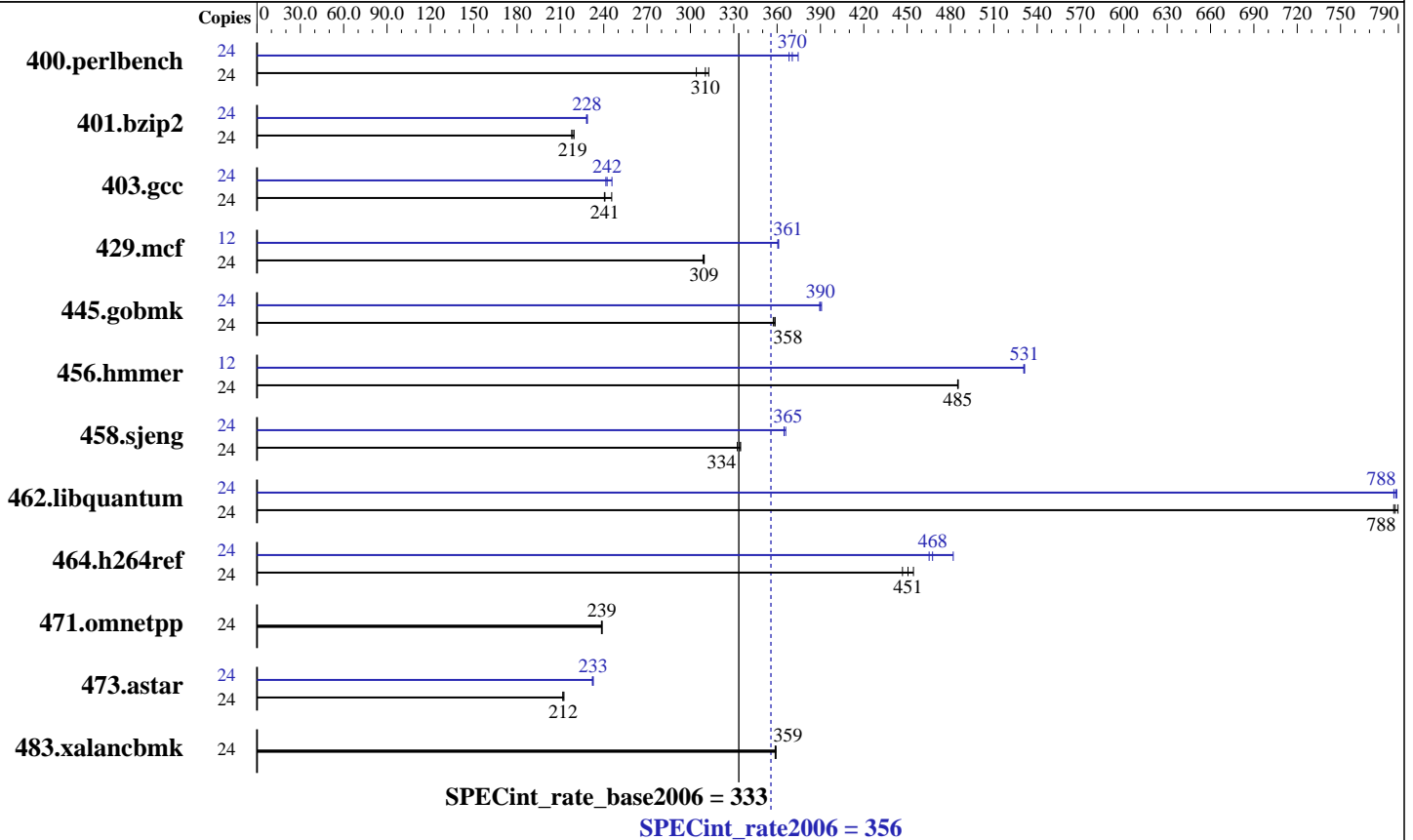
Test date: Apr-2010

Test sponsor: Hewlett-Packard Company

Hardware Availability: Jun-2010

Tested by: Hewlett-Packard Company

Software Availability: Sep-2009



Hardware

CPU Name: Intel Xeon X5670
 CPU Characteristics: Intel Turbo Boost Technology up to 3.33 GHz
 CPU MHz: 2933
 FPU: Integrated
 CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip, 2 threads/core
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 12 MB I+D on chip per chip
 Other Cache: None
 Memory: 48 GB (12x4 GB DDR3-10600R CL9)
 Disk Subsystem: 1x146 GB 10K 2.5" SAS
 Other Hardware: None

Software

Operating System: Red Hat Enterprise Linux Server release 5.4
 Advanced Platform, Kernel 2.6.18-164.el5
 Compiler: Intel C++ Compiler 11.1 for Linux
 Build 20090827 Package ID: l_cproc_p_11.1.056
 Auto Parallel: No
 File System: ext3
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V8.1
 Binutils 2.17.50.0.18



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	24	750	313	771	304	756	310	24	637	368	626	375	633	370
401.bzip2	24	1060	219	1063	218	1055	219	24	1014	228	1015	228	1013	229
403.gcc	24	804	240	787	246	802	241	24	786	246	800	241	797	242
429.mcf	24	709	309	708	309	707	310	12	304	360	303	361	303	361
445.gobmk	24	702	359	703	358	705	357	24	646	389	644	391	646	390
456.hammer	24	462	485	462	485	461	485	12	211	531	211	531	211	531
458.sjeng	24	874	332	867	335	869	334	24	794	366	796	365	796	365
462.libquantum	24	632	787	631	788	630	790	24	632	787	630	789	631	788
464.h264ref	24	1189	447	1169	454	1179	451	24	1102	482	1142	465	1136	468
471.omnetpp	24	628	239	628	239	629	238	24	628	239	628	239	629	238
473.astar	24	793	212	795	212	796	212	24	724	233	724	233	726	232
483.xalancbmk	24	461	359	462	359	461	359	24	461	359	462	359	461	359

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
numactl was used to bind copies to the cores

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

Platform Notes

BIOS configuration:
HP Power Profile set to Maximum Performance
Thermal Configuration set to Increased Cooling
Memory Speed with 2 DIMMs per Channel set to 1333Mhz Maximum
Data Reuse set to Disabled

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc



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Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -inline-calloc
-opt-malloc-options=3 -opt-prefetch

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/cpu2006/SmartHeap_8.1/lib -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /opt/intel/Compiler/11.1/056/bin/intel64/icc

456.hmmer: /opt/intel/Compiler/11.1/056/bin/intel64/icc

458.sjeng: /opt/intel/Compiler/11.1/056/bin/intel64/icc

C++ benchmarks:

icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX

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Peak Portability Flags (Continued)

483.xalanbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -ansi-alias -opt-prefetch

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -opt-prefetch -ansi-alias -auto-ilp32

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -static -inline-calloc
-opt-malloc-options=3

429.mcf: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -opt-prefetch

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2
-ipo -no-prec-div -ansi-alias

456.hmmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2
-ansi-alias -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -unroll4 -auto-ilp32

462.libquantum: -xSSE4.2 -ipo -O3 -no-prec-div -static
-opt-malloc-options=3 -opt-prefetch

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: basepeak = yes

473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs
-L/cpu2006/SmartHeap_8.1/lib -lsmartheap

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Peak Optimization Flags (Continued)

483.xalanbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revF.html>

<http://www.spec.org/cpu2006/flags/HP-Intel-Linux-Settings-flags.20100511.01.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revF.xml>

<http://www.spec.org/cpu2006/flags/HP-Intel-Linux-Settings-flags.20100511.01.xml>

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.

Report generated on Wed Jul 23 06:58:12 2014 by SPEC CPU2006 PS/PDF formatter v6932.

Originally published on 11 May 2010.