



SPEC® CINT2006 Result

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Acer Incorporated

SPECint®_rate2006 = 267

Gateway GR360 F1 (Intel Xeon X5570)

SPECint_rate_base2006 = 250

CPU2006 license: 97

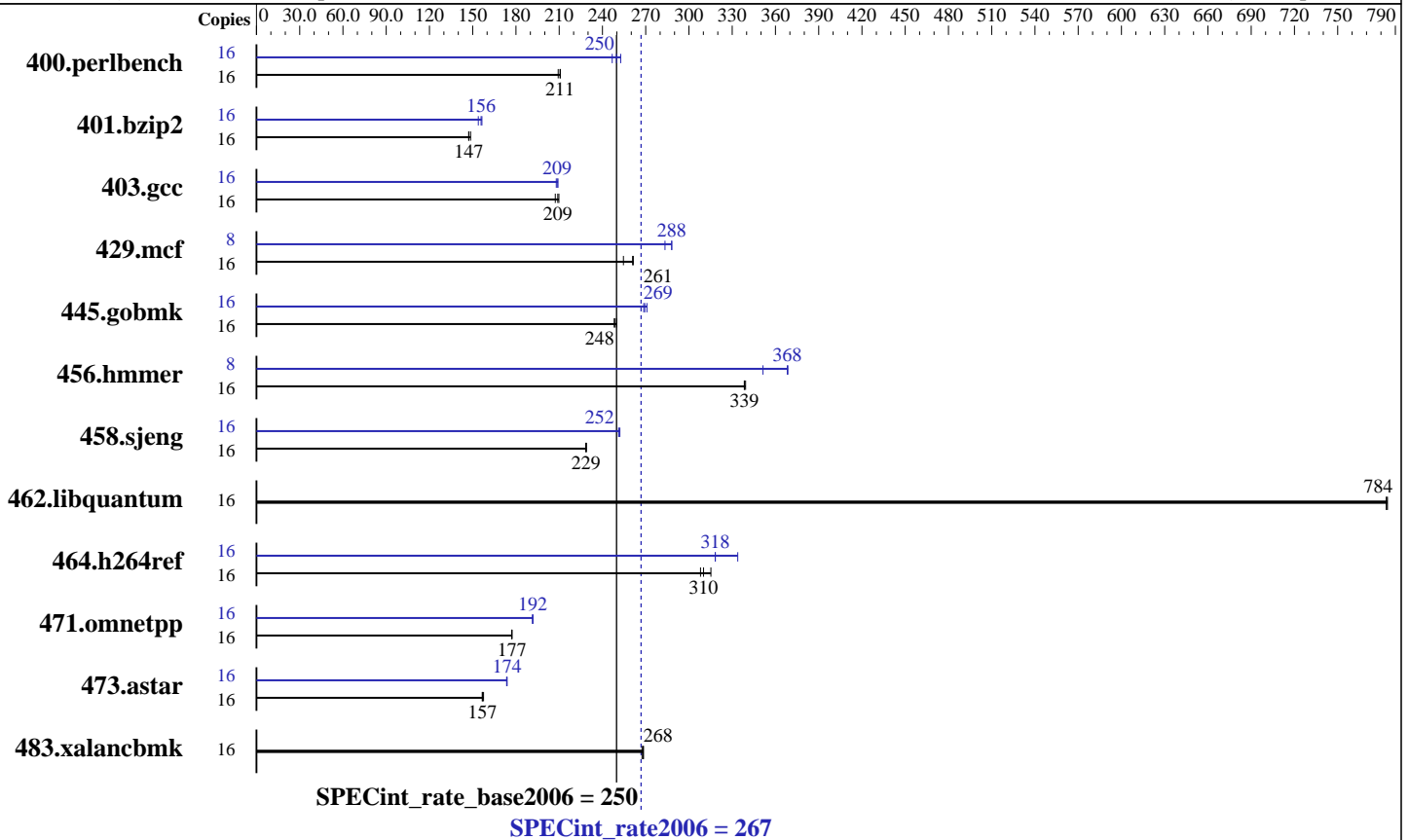
Test date: Jan-2010

Test sponsor: Acer Incorporated

Hardware Availability: Jan-2010

Tested by: Acer Incorporated

Software Availability: Sep-2009



Hardware

CPU Name: Intel Xeon X5570
 CPU Characteristics: Intel Turbo Boost Technology up to 3.33 GHz
 CPU MHz: 2933
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip, 2 threads/core
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 8 MB I+D on chip per chip
 Other Cache: None
 Memory: 24 GB (6 x 4GB DDR3-1333 RDIMM)
 Disk Subsystem: 1 x 500 GB SATA II, 7200 RPM
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 11 (x86_64)
 Kernel 2.6.27.19-5
 Compiler: Intel C++ Professional Compiler for IA32 and Intel 64, Version 11.1
 Build 20091012 Package ID: l_cproc_p_11.1.059
 Auto Parallel: No
 File System: ReiserFS
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V8.1
 Binutils 2.18.50.0.7.20080502



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	16	747	209	<u>742</u>	<u>211</u>	742	211	16	633	247	<u>626</u>	<u>250</u>	619	253
401.bzip2	16	<u>1048</u>	<u>147</u>	1040	149	1049	147	16	1003	154	<u>991</u>	<u>156</u>	987	156
403.gcc	16	621	207	614	210	<u>616</u>	<u>209</u>	16	616	209	619	208	<u>617</u>	<u>209</u>
429.mcf	16	573	255	558	261	<u>559</u>	<u>261</u>	8	257	283	253	288	<u>253</u>	<u>288</u>
445.gobmk	16	676	248	<u>676</u>	<u>248</u>	672	250	16	620	271	625	269	<u>623</u>	<u>269</u>
456.hammer	16	441	338	<u>441</u>	<u>339</u>	440	339	8	212	351	202	369	<u>203</u>	<u>368</u>
458.sjeng	16	848	228	<u>847</u>	<u>229</u>	846	229	16	<u>769</u>	<u>252</u>	768	252	771	251
462.libquantum	16	423	784	423	785	<u>423</u>	<u>784</u>	16	423	784	423	785	<u>423</u>	<u>784</u>
464.h264ref	16	<u>1142</u>	<u>310</u>	1150	308	1123	315	16	1061	334	1113	318	<u>1112</u>	<u>318</u>
471.omnetpp	16	565	177	<u>564</u>	<u>177</u>	564	177	16	<u>522</u>	<u>192</u>	522	192	522	192
473.astar	16	713	158	717	157	<u>716</u>	<u>157</u>	16	646	174	647	174	<u>646</u>	<u>174</u>
483.xalancbmk	16	413	267	<u>411</u>	<u>268</u>	411	268	16	413	267	<u>411</u>	<u>268</u>	411	268

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
numactl was used to bind copies to the cores

Operating System Notes

'ulimit -s unlimited' was set for stacksize unlimited

General Notes

The Acer AR360 F1, Gateway GR360 F1, the Acer AR380 F1 and the Gateway GR380 F1 are electronically equivalent.
This result was measured on the Gateway GR380 F1.

Base Compiler Invocation

C benchmarks:
icc -m32

C++ benchmarks:
icpc -m32



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Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m32

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks (except as noted below):

icpc -m32

473.astar: icpc -m64

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64

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Peak Portability Flags (Continued)

462.libquantum: -DSPEC_CPU_LINUX
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -ansi-alias

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -opt-prefetch -ansi-alias -auto-ilp32

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -static

429.mcf: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2
-ipo -no-prec-div -ansi-alias

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2
-ansi-alias -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -unroll4 -auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmartheap

473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-64bit -lsmartheap64

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Peak Optimization Flags (Continued)

483.xalanbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revD.20091208.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revD.20091208.xml>

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For other inquiries, please contact webmaster@spec.org.

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