



# SPEC® CFP2006 Result

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## Intel Corporation

Supermicro SuperServer 7046A-T (Intel Xeon X5677, 3.46 GHz)

SPECfp®\_rate2006 = 203

SPECfp\_rate\_base2006 = 195

CPU2006 license: 13

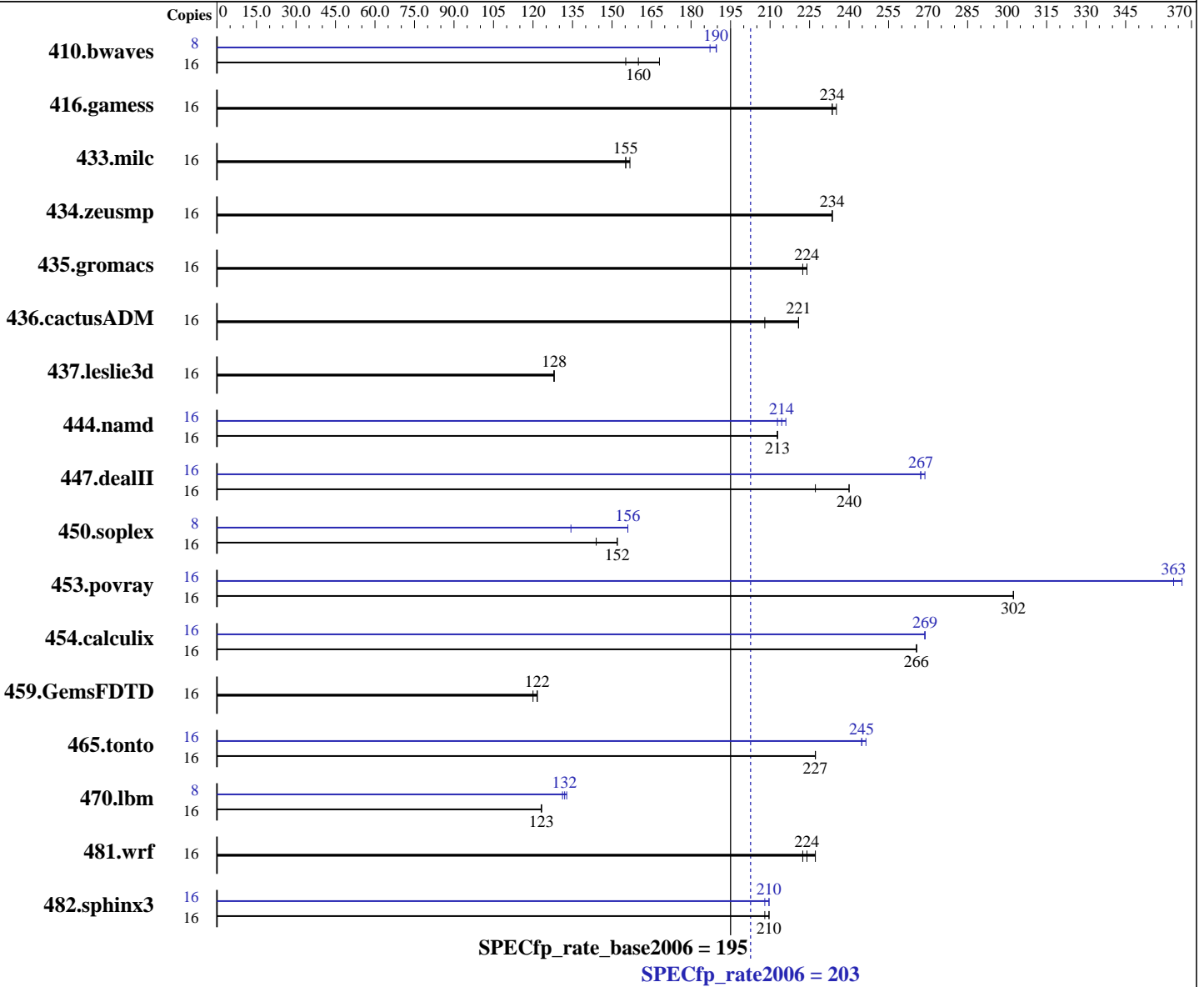
Test sponsor: Intel Corporation

Tested by: Intel Corporation

Test date: Mar-2010

Hardware Availability: Mar-2010

Software Availability: Nov-2009



### Hardware

CPU Name: Intel Xeon X5677  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.73 GHz  
 CPU MHz: 3467  
 FPU: Integrated  
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

Continued on next page

### Software

Operating System: Windows 7 Enterprise (64-bit)  
 Compiler: Intel C++ Compiler Professional 11.1 for Intel 64  
 Build 20090903 Package ID: w\_cproc\_p\_11.1.045  
 Intel Visual Fortran Compiler Professional 11.1 for Intel 64  
 Build 20090903 Package ID: w\_cproc\_p\_11.1.045, w\_cprof\_p\_11.1.045  
 Microsoft Visual Studio 2008 Professional SP1 (for libraries)  
 Auto Parallel: No

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L3 Cache: 12 MB I+D on chip per chip  
Other Cache: None  
Memory: 24 GB (6x 4GB DDR3-1333, ECC, CL9)  
Disk Subsystem: 1 x 300 GB SATA, 10000 RPM  
Other Hardware: None

File System: NTFS  
System State: Default  
Base Pointers: 64-bit  
Peak Pointers: 64-bit  
Other Software: SmartHeap Library Version 8.1 from <http://www.microquill.com/>

## Results Table

Benchmark	Base								Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio		
410.bwaves	16	1397	155	<b><u>1354</u></b>	<b><u>160</u></b>	1295	168	8	<b><u>574</u></b>	<b><u>190</u></b>	574	190	581	187		
416.gamess	16	1342	234	1333	235	<b><u>1341</u></b>	<b><u>234</u></b>	16	1342	234	1333	235	<b><u>1341</u></b>	<b><u>234</u></b>		
433.milc	16	<b><u>943</u></b>	<b><u>155</u></b>	941	157	947	155	16	<b><u>943</u></b>	<b><u>155</u></b>	941	157	947	155		
434.zeusmp	16	621	234	623	234	<b><u>623</u></b>	<b><u>234</u></b>	16	621	234	623	234	<b><u>623</u></b>	<b><u>234</u></b>		
435.gromacs	16	<b><u>512</u></b>	<b><u>224</u></b>	511	224	515	222	16	<b><u>512</u></b>	<b><u>224</u></b>	511	224	515	222		
436.cactusADM	16	<b><u>865</u></b>	<b><u>221</u></b>	863	221	919	208	16	<b><u>865</u></b>	<b><u>221</u></b>	863	221	919	208		
437.leslie3d	16	1182	128	1177	128	<b><u>1179</u></b>	<b><u>128</u></b>	16	1182	128	1177	128	<b><u>1179</u></b>	<b><u>128</u></b>		
444.namd	16	601	213	605	213	<b><u>604</u></b>	<b><u>213</u></b>	16	593	216	602	213	<b><u>599</u></b>	<b><u>214</u></b>		
447.dealII	16	804	227	761	240	<b><u>765</u></b>	<b><u>240</u></b>	16	681	269	685	267	<b><u>685</u></b>	<b><u>267</u></b>		
450.soplex	16	929	144	874	152	<b><u>874</u></b>	<b><u>152</u></b>	8	495	134	427	156	<b><u>427</u></b>	<b><u>156</u></b>		
453.povray	16	282	302	281	302	<b><u>281</u></b>	<b><u>302</u></b>	16	235	363	232	366	<b><u>234</u></b>	<b><u>363</u></b>		
454.calculix	16	<b><u>497</u></b>	<b><u>266</u></b>	497	266	496	266	16	490	269	492	269	<b><u>491</u></b>	<b><u>269</u></b>		
459.GemsFDTD	16	1407	120	<b><u>1397</u></b>	<b><u>122</u></b>	1392	122	16	1407	120	<b><u>1397</u></b>	<b><u>122</u></b>	1392	122		
465.tonto	16	<b><u>691</u></b>	<b><u>227</u></b>	691	227	692	227	16	642	245	<b><u>641</u></b>	<b><u>245</u></b>	641	246		
470.lbm	16	1776	123	1784	123	<b><u>1777</u></b>	<b><u>123</u></b>	8	<b><u>831</u></b>	<b><u>132</u></b>	830	133	837	131		
481.wrf	16	785	227	<b><u>800</u></b>	<b><u>224</u></b>	803	222	16	785	227	<b><u>800</u></b>	<b><u>224</u></b>	803	222		
482.sphinx3	16	1496	208	1484	210	<b><u>1487</u></b>	<b><u>210</u></b>	16	1500	208	<b><u>1490</u></b>	<b><u>210</u></b>	1490	210		

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.

## General Notes

Tested systems is used with tower case,  
PC Power and Cooling dual 865W power supplys  
System was configured with nVidia Quadro FX 370 discrete graphics card



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**Test date:** Mar-2010

**Hardware Availability:** Mar-2010

**Software Availability:** Nov-2009

## Base Compiler Invocation

C benchmarks:

icl -Qvc9 -Qstd=c99

C++ benchmarks:

icl -Qvc9

Fortran benchmarks:

ifort

Benchmarks using both Fortran and C:

icl -Qvc9 -Qstd=c99 ifort

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_P64 /Qlowercase  
 416.gamess: -DSPEC\_CPU\_P64  
 433.milc: -DSPEC\_CPU\_P64  
 434.zeusmp: -DSPEC\_CPU\_P64  
 435.gromacs: -DSPEC\_CPU\_P64  
 436.cactusADM: -DSPEC\_CPU\_P64 -Qlowercase /assume:underscore  
 437.leslie3d: -DSPEC\_CPU\_P64  
 444.namd: -DSPEC\_CPU\_P64 /TP  
 447.dealII: -DSPEC\_CPU\_P64 -DDEAL\_II\_MEMBER\_VAR\_SPECIALIZATION\_BUG  
 450.soplex: -DSPEC\_CPU\_P64  
 453.povray: -DSPEC\_CPU\_P64 -DSPEC\_CPU\_WINDOWS\_ICL  
 454.calculix: -DSPEC\_CPU\_P64 -DSPEC\_CPU\_NOZMODIFIER -Qlowercase  
 459.GemsFDTD: -DSPEC\_CPU\_P64  
 465.tonto: -DSPEC\_CPU\_P64  
 470.lbm: -DSPEC\_CPU\_P64  
 481.wrf: -DSPEC\_CPU\_P64 -DSPEC\_CPU\_WINDOWS\_ICL  
 482.sphinx3: -DSPEC\_CPU\_P64

## Base Optimization Flags

C benchmarks:

-QxSSE4.2 -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qauto-ilp32  
/F1000000000 -link /FORCE:MULTIPLE

C++ benchmarks:

-QxSSE4.2 -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qcxx-features  
-Qauto-ilp32 /F1000000000 shlw64M.lib -link /FORCE:MULTIPLE

Fortran benchmarks:

-QxSSE4.2 -Qipo -O3 -Qprec-div- -Qopt-prefetch /F1000000000  
-link /FORCE:MULTIPLE

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## Base Optimization Flags (Continued)

Benchmarks using both Fortran and C:

```
-QxSSE4.2 -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qauto-ilp32
/F1000000000 -link /FORCE:MULTIPLE
```

## Peak Compiler Invocation

C benchmarks:

```
icl -Qvc9 -Qstd=c99
```

C++ benchmarks:

```
icl -Qvc9
```

Fortran benchmarks:

```
ifort
```

Benchmarks using both Fortran and C:

```
icl -Qvc9 -Qstd=c99 ifort
```

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

```
433.milc: basepeak = yes
```

```
470.lbm: -QxSSE4.2 -Qipo -O3 -Qprec-div- -Qopt-prefetch
-Qauto-ilp32 /F1000000000 -link /FORCE:MULTIPLE
```

```
482.sphinx3: -QxSSE4.2 -Qipo -O3 -Qprec-div- -Qunroll2 -Qauto-ilp32
/F1000000000 -link /FORCE:MULTIPLE
```

C++ benchmarks:

```
444.namd: -QxSSE4.2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Oa -Qauto-ilp32 /F1000000000
shlW64M.lib -link /FORCE:MULTIPLE
```

```
447.dealII: -QxSSE4.2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Qunroll2 -Qansi-alias
-Qscalar-rep- -Qauto-ilp32 /F1000000000 shlW64M.lib
-link /FORCE:MULTIPLE
```

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## Peak Optimization Flags (Continued)

450.soplex: -QxSSE4.2(pass 2) -Qprof\_gen(pass 1) -Qprof\_use(pass 2)  
-Qipo -O3 -Qauto-ilp32 /F1000000000 sh1W64M.lib  
-link /FORCE:MULTIPLE

453.povray: -QxSSE4.2(pass 2) -Qprof\_gen(pass 1) -Qprof\_use(pass 2)  
-Qipo -O3 -Qprec-div- -Qunroll4 -Qansi-alias -Qauto-ilp32  
/F1000000000 sh1W64M.lib -link /FORCE:MULTIPLE

### Fortran benchmarks:

410.bwaves: -QxSSE4.2 -Qipo -O3 -Qprec-div- -Qopt-prefetch  
/F1000000000 -link /FORCE:MULTIPLE

416.gamess: basepeak = yes

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: basepeak = yes

465.tonto: -QxSSE4.2(pass 2) -Qprof\_gen(pass 1) -Qprof\_use(pass 2)  
-Qipo -O3 -Qprec-div- -Qunroll4 -Qauto /F1000000000  
-link /FORCE:MULTIPLE

### Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -QxSSE4.2 -Qipo -O3 -Qprec-div- -Qauto-ilp32 /F1000000000  
-link /FORCE:MULTIPLE

481.wrf: basepeak = yes

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-winx64-revA.20100302.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-winx64-revA.20100302.xml>



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