



SPEC[®] CFP2006 Result

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Fujitsu

SPECfp[®]_rate2006 = 124

CELSIUS M470, Intel Xeon W3680

SPECfp_rate_base2006 = 120

CPU2006 license: 19

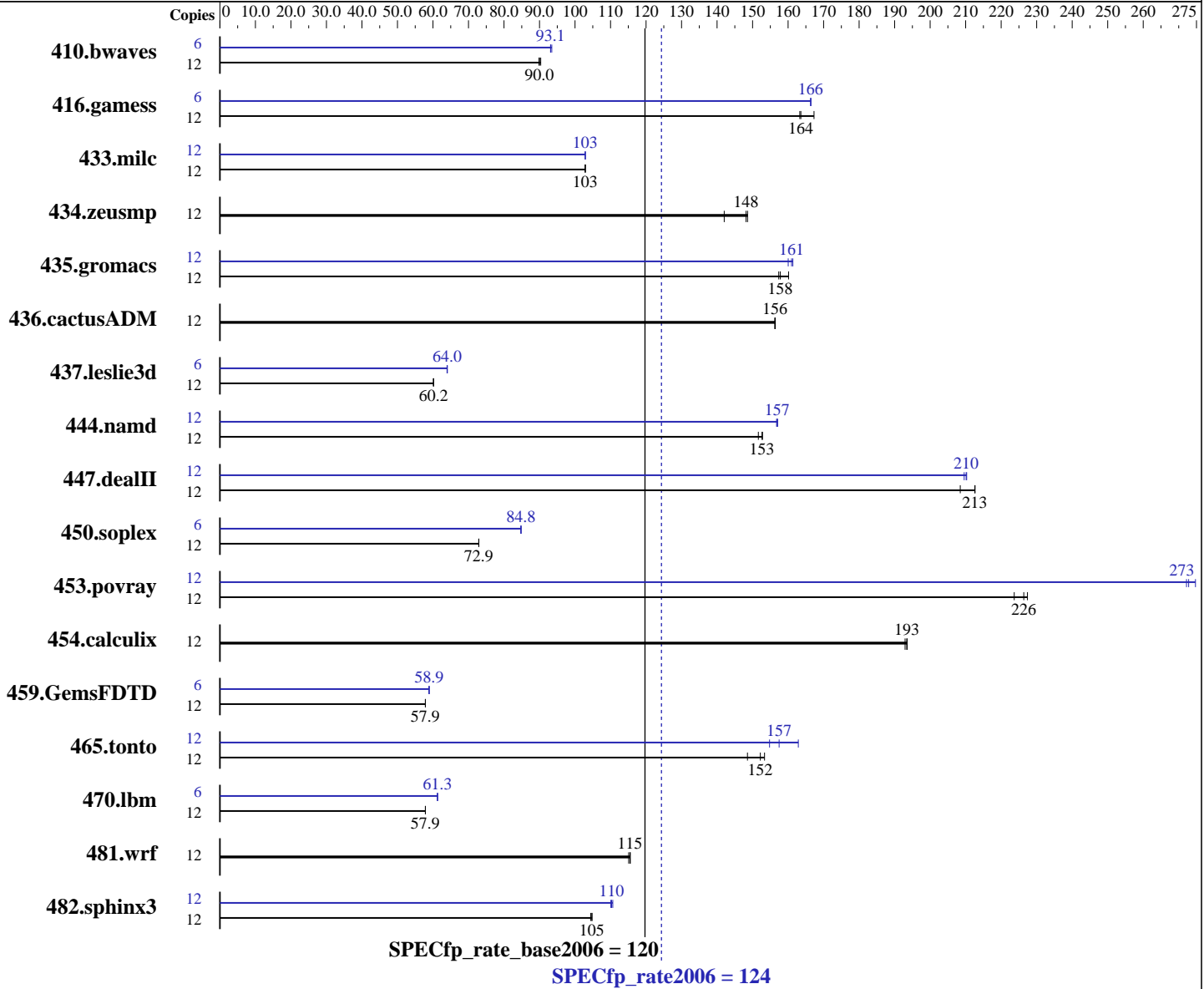
Test sponsor: Fujitsu

Tested by: Fujitsu

Test date: Mar-2010

Hardware Availability: Mar-2010

Software Availability: Dec-2009



Hardware

CPU Name: Intel Xeon W3680
 CPU Characteristics: Intel Turbo Boost Technology up to 3.6 GHz
 CPU MHz: 3333
 FPU: Integrated
 CPU(s) enabled: 6 cores, 1 chip, 6 cores/chip, 2 threads/core
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

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Software

Operating System: SUSE Linux Enterprise Server 11 (x86_64), kernel 2.6.27.19-5-default
 Compiler: Intel C++ and Fortran Professional Compiler for IA32 and Intel 64, Version 11.1 Build 20091130 Package ID: l_cproc_p_11.1.064, l_cprof_p_11.1.064
 Auto Parallel: No
 File System: ext3
 System State: Multi-User Run Level 3

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L3 Cache: 12 MB I+D on chip per chip
Other Cache: None
Memory: 12 GB (6x2 GB PC3-10600E, 2 rank, CL9, ECC)
Disk Subsystem: 1 x SATA II, 400 GB, 7200 rpm
Other Hardware: None

Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: Binutils 2.18.50.0.7.20080502

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	12	1805	90.3	<u>1812</u>	<u>90.0</u>	1815	89.9	6	<u>876</u>	<u>93.1</u>	872	93.5	876	93.1
416.gamess	12	1404	167	<u>1436</u>	<u>164</u>	1439	163	6	<u>706</u>	<u>166</u>	706	166	706	166
433.milc	12	1070	103	<u>1071</u>	<u>103</u>	1071	103	12	<u>1071</u>	<u>103</u>	1071	103	1071	103
434.zeusmp	12	735	149	769	142	<u>737</u>	<u>148</u>	12	735	149	769	142	<u>737</u>	<u>148</u>
435.gromacs	12	544	157	535	160	<u>543</u>	<u>158</u>	12	535	160	531	161	<u>532</u>	<u>161</u>
436.cactusADM	12	918	156	<u>918</u>	<u>156</u>	917	156	12	918	156	<u>918</u>	<u>156</u>	917	156
437.leslie3d	12	1875	60.2	<u>1875</u>	<u>60.2</u>	1876	60.1	6	882	64.0	<u>881</u>	<u>64.0</u>	881	64.0
444.namd	12	630	153	<u>630</u>	<u>153</u>	635	152	12	613	157	614	157	<u>613</u>	<u>157</u>
447.dealII	12	<u>646</u>	<u>213</u>	646	213	658	208	12	<u>653</u>	<u>210</u>	653	210	655	210
450.soplex	12	1372	72.9	<u>1373</u>	<u>72.9</u>	1373	72.9	6	<u>590</u>	<u>84.8</u>	590	84.8	590	84.8
453.povray	12	285	224	281	227	<u>282</u>	<u>226</u>	12	<u>234</u>	<u>273</u>	235	272	232	275
454.calculix	12	<u>512</u>	<u>193</u>	513	193	512	194	12	<u>512</u>	<u>193</u>	513	193	512	194
459.GemsFDTD	12	<u>2201</u>	<u>57.9</u>	2201	57.9	2201	57.9	6	1080	58.9	1081	58.9	<u>1081</u>	<u>58.9</u>
465.tonto	12	770	153	<u>776</u>	<u>152</u>	795	149	12	725	163	763	155	<u>750</u>	<u>157</u>
470.lbm	12	2849	57.9	<u>2850</u>	<u>57.9</u>	2850	57.9	6	1345	61.3	<u>1346</u>	<u>61.3</u>	1346	61.3
481.wrf	12	1165	115	<u>1161</u>	<u>115</u>	1160	116	12	1165	115	<u>1161</u>	<u>115</u>	1160	116
482.sphinx3	12	2232	105	<u>2233</u>	<u>105</u>	2240	104	12	2114	111	<u>2120</u>	<u>110</u>	2125	110

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
numactl --localalloc --physcpubind was used to bind processes to cores and it's local memory.
Details on the parameters used may be found in the config file.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

Platform Notes

BIOS configuration:
Data Reuse Optimization = Disabled



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Base Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static

C++ benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static

Fortran benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static

Benchmarks using both Fortran and C:
-xSSE4.2 -ipo -O3 -no-prec-div -static



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Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

482.sphinx3: icc -m32

C++ benchmarks (except as noted below):

icpc -m64

450.soplex: icpc -m32

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
 416.gamess: -DSPEC_CPU_LP64
 433.milc: -DSPEC_CPU_LP64
 434.zeusmp: -DSPEC_CPU_LP64
 435.gromacs: -DSPEC_CPU_LP64 -nofor_main
 436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
 437.leslie3d: -DSPEC_CPU_LP64
 444.namd: -DSPEC_CPU_LP64
 447.dealII: -DSPEC_CPU_LP64
 453.povray: -DSPEC_CPU_LP64
 454.calculix: -DSPEC_CPU_LP64 -nofor_main
 459.GemsFDTD: -DSPEC_CPU_LP64
 465.tonto: -DSPEC_CPU_LP64
 470.lbm: -DSPEC_CPU_LP64
 481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

433.milc: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
 -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
 -fno-alias -opt-prefetch

470.lbm: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
 -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
 -opt-malloc-options=3 -ansi-alias -auto-ilp32

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Peak Optimization Flags (Continued)

482.sphinx3: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2

C++ benchmarks:

444.namd: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-fno-alias -auto-ilp32

447.dealII: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll2 -ansi-alias -scalar-rep-

450.soplex: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-opt-malloc-options=3

453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

416.gamess: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll2 -Ob0 -ansi-alias -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: -xSSE4.2 -ipo -O3 -no-prec-div -static

459.GemsFDTD: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll2 -Ob0

465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll4 -auto -inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-opt-prefetch -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

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Peak Optimization Flags (Continued)

481.wrf: basepeak = yes

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100316.01.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100316.01.xml>

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Tested with SPEC CPU2006 v1.1.
Report generated on Wed Jul 23 09:47:00 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 14 April 2010.