



SPEC[®] CINT2006 Result

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Acer Incorporated

SPECint[®]_rate2006 = 73.9

Acer AR380 F1(Intel Xeon E5502)

SPECint_rate_base2006 = 68.7

CPU2006 license: 97

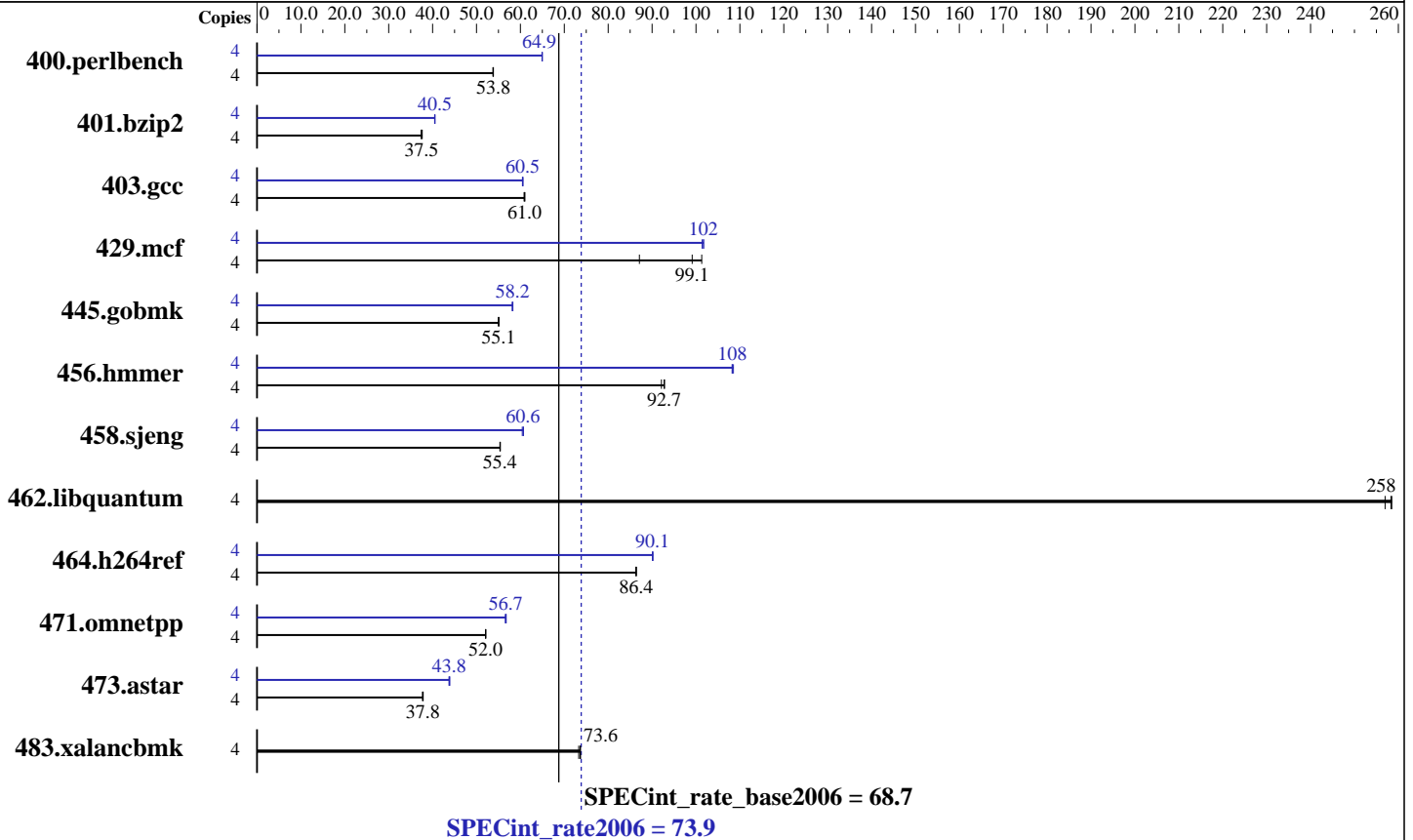
Test date: Mar-2010

Test sponsor: Acer Incorporated

Hardware Availability: Jan-2010

Tested by: Acer Incorporated

Software Availability: Oct-2009



Hardware

CPU Name: Intel Xeon E5502
 CPU Characteristics: 1867
 CPU MHz: 1867
 FPU: Integrated
 CPU(s) enabled: 4 cores, 2 chips, 2 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 4 MB I+D on chip per chip
 Other Cache: None
 Memory: 24 GB (12 x 2GB DDR3-1333 RDIMM, running at 800 MHz)
 Disk Subsystem: 1000 GB SATA II, 7200 RPM
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 11 (x86_64)
 Kernel 2.6.27.19-5
 Compiler: Intel C++ Professional Compiler for IA32 and Intel 64, Version 11.1
 Build 20091012 Package ID: l_cproc_p_11.1.059
 Auto Parallel: No
 File System: ReiserFS
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V8.1
 Binutils 2.18.50.0.7.20080502



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Results Table

| Benchmark | Base | | | | | | Peak | | | | | | | |
|----------------|--------|--------------------|--------------------|-------------------|--------------------|--------------------|--------------------|--------|-------------------|--------------------|-------------------|--------------------|-------------------|--------------------|
| | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 400.perlbench | 4 | 726 | 53.8 | <u>726</u> | <u>53.8</u> | 727 | 53.7 | 4 | <u>602</u> | <u>64.9</u> | 601 | 65.0 | 602 | 64.9 |
| 401.bzip2 | 4 | <u>1029</u> | <u>37.5</u> | 1026 | 37.6 | 1032 | 37.4 | 4 | 952 | 40.5 | 954 | 40.5 | <u>952</u> | <u>40.5</u> |
| 403.gcc | 4 | 528 | 61.0 | <u>528</u> | <u>61.0</u> | 529 | 60.8 | 4 | 533 | 60.5 | 531 | 60.6 | <u>532</u> | <u>60.5</u> |
| 429.mcf | 4 | <u>368</u> | <u>99.1</u> | 360 | 101 | 419 | 87.1 | 4 | <u>359</u> | <u>102</u> | 360 | 101 | 359 | 102 |
| 445.gobmk | 4 | 763 | 55.0 | 761 | 55.1 | <u>762</u> | <u>55.1</u> | 4 | 720 | 58.3 | 722 | 58.1 | <u>721</u> | <u>58.2</u> |
| 456.hammer | 4 | <u>402</u> | <u>92.7</u> | 402 | 92.8 | 405 | 92.1 | 4 | 345 | 108 | 344 | 109 | <u>345</u> | <u>108</u> |
| 458.sjeng | 4 | <u>874</u> | <u>55.4</u> | 873 | 55.4 | 874 | 55.4 | 4 | 797 | 60.7 | <u>799</u> | <u>60.6</u> | 801 | 60.4 |
| 462.libquantum | 4 | 323 | 257 | 321 | 259 | <u>321</u> | <u>258</u> | 4 | 323 | 257 | 321 | 259 | <u>321</u> | <u>258</u> |
| 464.h264ref | 4 | 1026 | 86.3 | 1024 | 86.4 | <u>1025</u> | <u>86.4</u> | 4 | <u>982</u> | <u>90.1</u> | 983 | 90.1 | 981 | 90.2 |
| 471.omnetpp | 4 | 479 | 52.2 | 480 | 52.0 | <u>480</u> | <u>52.0</u> | 4 | 442 | 56.5 | 440 | 56.8 | <u>441</u> | <u>56.7</u> |
| 473.astar | 4 | 742 | 37.9 | 745 | 37.7 | <u>743</u> | <u>37.8</u> | 4 | 639 | 43.9 | <u>641</u> | <u>43.8</u> | 642 | 43.7 |
| 483.xalancbmk | 4 | 374 | 73.7 | <u>375</u> | <u>73.6</u> | 377 | 73.3 | 4 | 374 | 73.7 | <u>375</u> | <u>73.6</u> | 377 | 73.3 |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
numactl was used to bind copies to the cores

Operating System Notes

'ulimit -s unlimited' was set for stacksize unlimited

General Notes

This result was measured on the Gateway GR380 F1.
The Acer AR380 F1 and Gateway GR380 F1 are electronically equivalent.

Base Compiler Invocation

C benchmarks:
icc -m32

C++ benchmarks:
icpc -m32



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Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.icl1.1/libic11.1-32bit -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m32

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks (except as noted below):

icpc -m32

473.astar: icpc -m64

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64

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Peak Portability Flags (Continued)

462.libquantum: -DSPEC_CPU_LINUX
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -ansi-alias

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -opt-prefetch -ansi-alias -auto-ilp32

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -static

429.mcf: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2
-ipo -no-prec-div -ansi-alias

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2
-ansi-alias -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -unroll4 -auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmartheap

473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-64bit -lsmartheap64

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Peak Optimization Flags (Continued)

483.xalanbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-int-linux64-revE.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-int-linux64-revE.xml>

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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