



SPEC® CFP2006 Result

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Intel Corporation

SPECfp®_rate2006 = 38.0

Asus P5E3 Pro motherboard (Intel Core 2 Duo E8600)

SPECfp_rate_base2006 = 36.9

CPU2006 license: 13

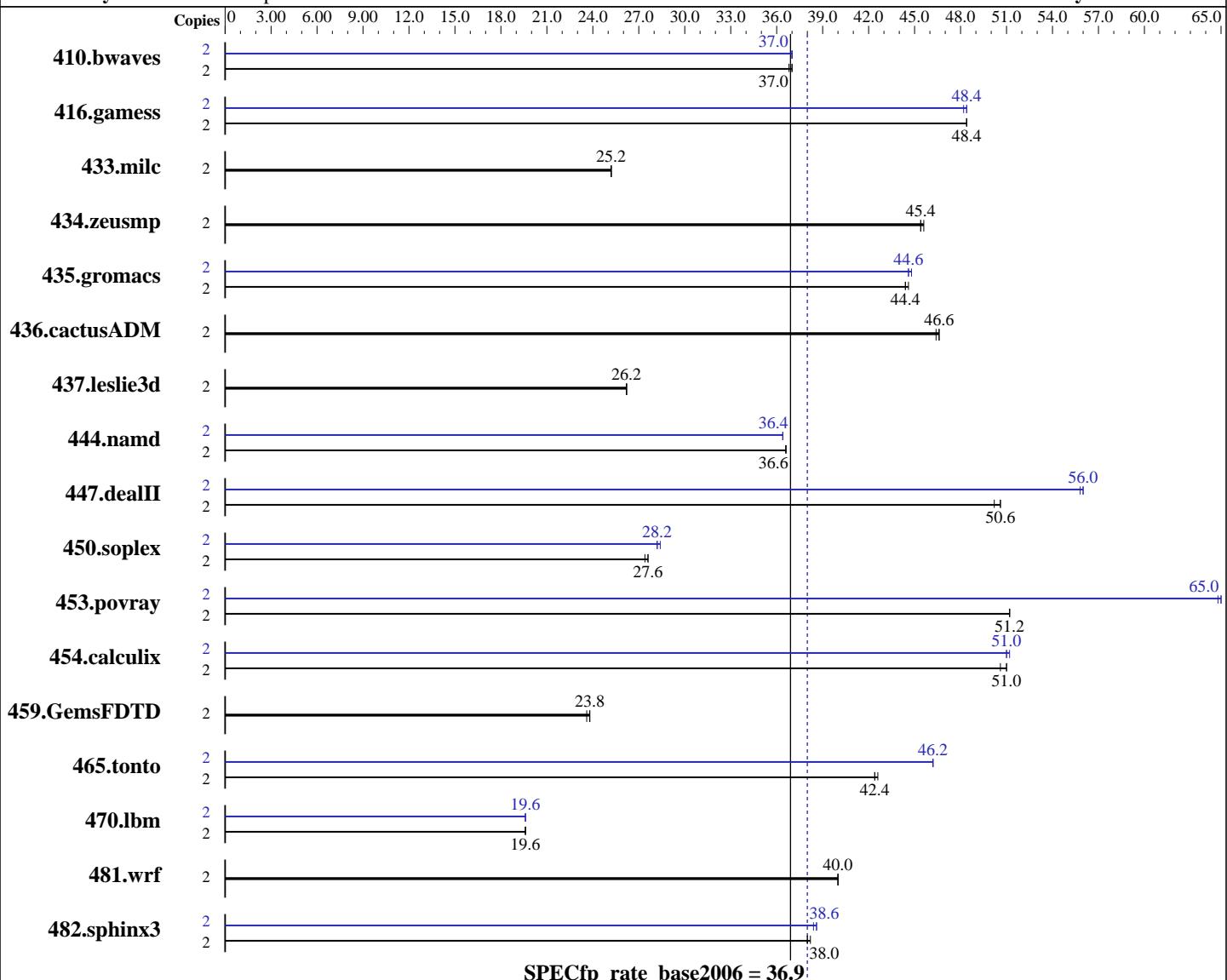
Test date: Feb-2010

Test sponsor: Intel Corporation

Hardware Availability: Jun-2009

Tested by: Intel Corporation

Software Availability: Oct-2009



Hardware

CPU Name: Intel Core 2 Duo E8600
CPU Characteristics:
CPU MHz: 3333
FPU: Integrated
CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip
CPU(s) orderable: 1 chip
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 6 MB I+D on chip per chip

Software

Operating System: Windows Vista Ultimate w/ SP1 (64-bit)
Compiler: Intel C++ Compiler Professional 11.1 for Intel 64 Build 20090903 Package ID: w_cproc_p_11.1.045
Intel Visual Fortran Compiler Professional 11.1 for Intel 64 Build 20090903 Package ID: w_cprof_p_11.1.045
Microsoft Visual Studio 2008 Professional SP1 (for libraries)
Auto Parallel: No

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Base Compiler Invocation

C benchmarks:

```
icl -Qvc9 -Qstd=c99
```

C++ benchmarks:

```
icl -Qvc9
```

Fortran benchmarks:

```
ifort
```

Benchmarks using both Fortran and C:

```
icl -Qvc9 -Qstd=c99 ifort
```

Base Portability Flags

```
410.bwaves: -DSPEC_CPU_P64 /Qlowercase  
416.gamess: -DSPEC_CPU_P64  
433.milc: -DSPEC_CPU_P64  
434.zeusmp: -DSPEC_CPU_P64  
435.gromacs: -DSPEC_CPU_P64  
436.cactusADM: -DSPEC_CPU_P64 -Qlowercase /assume:underscore  
437.leslie3d: -DSPEC_CPU_P64  
444.namd: -DSPEC_CPU_P64 /TP  
447.dealII: -DSPEC_CPU_P64 -DDEAL_II_MEMBER_VAR_SPECIALIZATION_BUG  
450.soplex: -DSPEC_CPU_P64  
453.povray: -DSPEC_CPU_P64 -DSPEC_CPU_WINDOWS_ICL  
454.calculix: -DSPEC_CPU_P64 -DSPEC_CPU_NOZMODIFIER -Qlowercase  
459.GemsFDTD: -DSPEC_CPU_P64  
465.tonto: -DSPEC_CPU_P64  
470.lbm: -DSPEC_CPU_P64  
481.wrf: -DSPEC_CPU_P64 -DSPEC_CPU_WINDOWS_ICL  
482.sphinx3: -DSPEC_CPU_P64
```

Base Optimization Flags

C benchmarks:

```
-QxSSE4.1 -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qauto-ilp32  
/F10000000000 -link /FORCE:MULTIPLE
```

C++ benchmarks:

```
-QxSSE4.1 -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qcxx-features  
-Qauto-ilp32 /F10000000000 shlw64M.lib -link /FORCE:MULTIPLE
```

Fortran benchmarks:

```
-QxSSE4.1 -Qipo -O3 -Qprec-div- -Qopt-prefetch /F10000000000  
-link /FORCE:MULTIPLE
```

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Base Optimization Flags (Continued)

Benchmarks using both Fortran and C:

```
-QxSSE4.1 -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qauto-ilp32  
/F10000000000 -link /FORCE:MULTIPLE
```

Peak Compiler Invocation

C benchmarks:

```
icl -Qvc9 -Qstd=c99
```

C++ benchmarks:

```
icl -Qvc9
```

Fortran benchmarks:

```
ifort
```

Benchmarks using both Fortran and C:

```
icl -Qvc9 -Qstd=c99 ifort
```

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

```
433.milc: basepeak = yes
```

```
470.lbm: -QxSSE4.1 -Qipo -O3 -Qprec-div- -Qopt-prefetch  
-Qauto-ilp32 /F10000000000 -link /FORCE:MULTIPLE
```

```
482.sphinx3: -QxSSE4.1 -Qipo -O3 -Qprec-div- -Qunroll2 -Qauto-ilp32  
/F10000000000 -link /FORCE:MULTIPLE
```

C++ benchmarks:

```
444.namd: -QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)  
-Qipo -O3 -Qprec-div- -Oa -Qauto-ilp32 /F10000000000  
shlw64M.lib -link /FORCE:MULTIPLE
```

```
447.dealII: -QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)  
-Qipo -O3 -Qprec-div- -Qunroll2 -Qansi-alias  
-Qscalar-rep- -Qauto-ilp32 /F10000000000 shlw64M.lib  
-link /FORCE:MULTIPLE
```

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Peak Optimization Flags (Continued)

450.soplex: -QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qauto-ilp32 /F100000000000 shlw64M.lib
-link /FORCE:MULTIPLE

453.povray: -QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Qunroll14 -Qansi-alias -Qauto-ilp32
/F100000000000 shlw64M.lib -link /FORCE:MULTIPLE

Fortran benchmarks:

410.bwaves: -QxSSE4.1 -Qipo -O3 -Qprec-div- -Qopt-prefetch
/F10000000000 -link /FORCE:MULTIPLE

416.gamess: Same as 410.bwaves

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: basepeak = yes

465.tonto: -QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Qunroll14 -Qauto /F10000000000
-link /FORCE:MULTIPLE

Benchmarks using both Fortran and C:

435.gromacs: -QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Qopt-prefetch -Qauto-ilp32
/F10000000000 -link /FORCE:MULTIPLE

436.cactusADM: basepeak = yes

454.calculix: -QxSSE4.1 -Qipo -O3 -Qprec-div- -Qauto-ilp32 /F10000000000
-link /FORCE:MULTIPLE

481.wrf: basepeak = yes

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-winx64-revA.20100302.01.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-winx64-revA.20100302.01.xml>



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