SPEC® CFP2006 Result
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Acer Incorporated

Gateway GR380 F1 (Intel Xeon X5570)

$SPECfp®\_rate2006 =$ 199

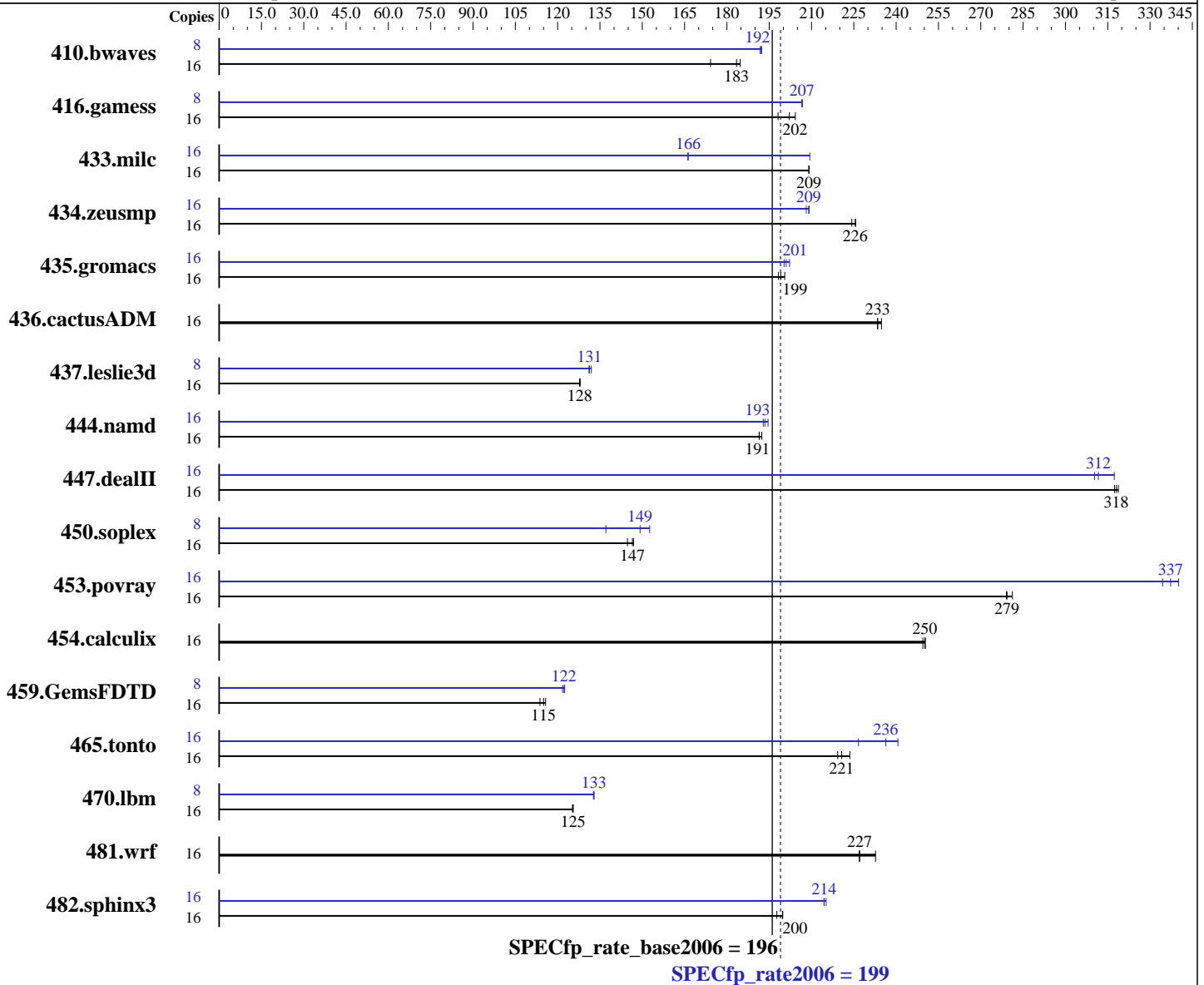$SPECfp\_rate\_base2006 =$ 196

CPU2006 license: 97
Test sponsor: Acer Incorporated
Tested by: Acer Incorporated

Test date: Jan-2010
Hardware Availability: Jan-2010
Software Availability: Sep-2009

Benchmark	Copies	Result
410.bwaves	8	192
	16	183
416.gamess	8	207
	16	202
433.milc	16	166
	16	209
434.zeusmp	16	209
	16	226
435.gromacs	16	201
	16	199
436.cactusADM	16	233
437.leslie3d	8	131
	16	128
444.namd	16	193
	16	191
447.dealII	16	312
	16	318
450.soplex	8	149
	16	147
453.povray	16	337
	16	279
454.calculix	16	250
459.GemsFDTD	8	122
	16	115
465.tonto	16	236
	16	221
470.lbm	8	133
	16	125
481.wrf	16	227
482.sphinx3	16	214
	16	200

SPECfp_rate_base2006 = 196

SPECfp_rate2006 = 199

Hardware

CPU Name:	Intel Xeon X5570
CPU Characteristics:	Intel Turbo Boost Technology up to 3.33 GHz
CPU MHz:	2933
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip, 2 threads/core
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core

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Software

Operating System:	SUSE Linux Enterprise Server 10 SP2 Kernel 2.6.16.60-0.21-smp
Compiler:	Intel C++ and Fortran Professional Compiler for IA32 and Intel 64, Version 11.1 Build 20091012 Package ID: l_cproc_p_11.1.059, l_cprof_p_11.1.059
Auto Parallel:	No
File System:	ReiserFS
System State:	Run level 3 (multi-user)

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L3 Cache:	8 MB I+D on chip per chip	Base Pointers:	64-bit
Other Cache:	None	Peak Pointers:	32/64-bit
Memory:	24 GB (6 x 4GB DDR3-1333 RDIMM)	Other Software:	Binutils 2.18.50.0.7.20080502
Disk Subsystem:	1 x 500 GB SATA II, 7200 RPM		
Other Hardware:	None		

Results Table

Benchmark	Base Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Peak Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	16	1248	174	1177	185	**1186**	**183**	8	**566**	**192**	565	192	567	192
416.gamess	16	**1550**	**202**	1534	204	1581	198	8	758	207	758	207	**758**	**207**
433.milc	16	702	209	**702**	**209**	703	209	16	701	209	**883**	**166**	884	166
434.zeusmp	16	645	226	649	224	**646**	**226**	16	696	209	700	208	**697**	**209**
435.gromacs	16	**574**	**199**	570	200	576	198	16	565	202	**568**	**201**	570	200
436.cactusADM	16	814	235	819	233	**819**	**233**	16	814	235	819	233	**819**	**233**
437.leslie3d	16	1175	128	1176	128	**1176**	**128**	8	570	132	573	131	**573**	**131**
444.namd	16	667	192	**670**	**191**	670	191	16	665	193	**663**	**193**	659	195
447.dealII	16	574	319	**575**	**318**	577	317	16	577	317	590	310	**587**	**312**
450.soplex	16	908	147	922	145	**911**	**147**	8	487	137	**447**	**149**	437	153
453.povray	16	303	281	305	279	**305**	**279**	16	**252**	**337**	254	334	250	340
454.calculix	16	529	249	**528**	**250**	527	250	16	529	249	**528**	**250**	527	250
459.GemsFDTD	16	1492	114	1467	116	**1476**	**115**	8	**694**	**122**	697	122	693	123
465.tonto	16	718	219	704	224	**713**	**221**	16	654	241	**666**	**236**	695	227
470.lbm	16	1755	125	**1752**	**125**	1752	125	8	827	133	**827**	**133**	828	133
481.wrf	16	**787**	**227**	768	233	788	227	16	**787**	**227**	768	233	788	227
482.sphinx3	16	1577	198	**1562**	**200**	1561	200	16	1449	215	**1454**	**214**	1454	214

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
   The config file option 'submit' was used.
    numactl was used to bind copies to the cores
```

Operating System Notes

```
   'ulimit -s unlimited' was set for stacksize unlimited
```

Base Compiler Invocation

```
   C benchmarks:
      icc   -m64
```

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Base Compiler Invocation (Continued)

C++ benchmarks:
    icpc -m64

Fortran benchmarks:
    ifort -m64

Benchmarks using both Fortran and C:
    icc   -m64 ifort -m64

Base Portability Flags

         410.bwaves: -DSPEC_CPU_LP64
         416.gamess: -DSPEC_CPU_LP64
           433.milc: -DSPEC_CPU_LP64
         434.zeusmp: -DSPEC_CPU_LP64
        435.gromacs: -DSPEC_CPU_LP64
      436.cactusADM: -DSPEC_CPU_LP64
        437.leslie3d: -DSPEC_CPU_LP64
          444.namd: -DSPEC_CPU_LP64
          447.dealII: -DSPEC_CPU_LP64
         450.soplex: -DSPEC_CPU_LP64
         453.povray: -DSPEC_CPU_LP64
        454.calculix: -DSPEC_CPU_LP64
       459.GemsFDTD: -DSPEC_CPU_LP64
          465.tonto: -DSPEC_CPU_LP64
           470.lbm: -DSPEC_CPU_LP64
           481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
         482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
    -xSSE4.2 -ipo -O3 -no-prec-div -static

C++ benchmarks:
    -xSSE4.2 -ipo -O3 -no-prec-div -static

Fortran benchmarks:
    -xSSE4.2 -ipo -O3 -no-prec-div -static

Benchmarks using both Fortran and C:

    435.gromacs: -xSSE4.2 -ipo -O3 -no-prec-div -static -nofor_main(*)

  436.cactusADM: Same as 435.gromacs

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Base Optimization Flags (Continued)

454.calculix: Same as 435.gromacs

481.wrf: `-xSSE4.2 -ipo -O3 -no-prec-div -static`

(*) Indicates an optimization flag that was found in a portability variable.

Peak Compiler Invocation

C benchmarks (except as noted below):
      `icc   -m64`

482.sphinx3: `icc -m32`

C++ benchmarks (except as noted below):
      `icpc  -m64`

450.soplex: `icpc -m32`

Fortran benchmarks:
      `ifort -m64`

Benchmarks using both Fortran and C:
      `icc   -m64 ifort -m64`

Peak Portability Flags

410.bwaves: `-DSPEC_CPU_LP64`
416.gamess: `-DSPEC_CPU_LP64`
433.milc: `-DSPEC_CPU_LP64`
434.zeusmp: `-DSPEC_CPU_LP64`
435.gromacs: `-DSPEC_CPU_LP64`
436.cactusADM: `-DSPEC_CPU_LP64`
437.leslie3d: `-DSPEC_CPU_LP64`
444.namd: `-DSPEC_CPU_LP64`
447.dealII: `-DSPEC_CPU_LP64`
453.povray: `-DSPEC_CPU_LP64`
454.calculix: `-DSPEC_CPU_LP64`
459.GemsFDTD: `-DSPEC_CPU_LP64`
465.tonto: `-DSPEC_CPU_LP64`
470.lbm: `-DSPEC_CPU_LP64`
481.wrf: `-DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX`

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Peak Optimization Flags

C benchmarks:

    433.milc: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
              -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
              -fno-alias -opt-prefetch

    470.lbm: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
             -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
             -opt-malloc-options=3 -ansi-alias -auto-ilp32

    482.sphinx3: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2

C++ benchmarks:

    444.namd: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
              -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
              -fno-alias -auto-ilp32

    447.dealII: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
                -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
                -unroll2 -ansi-alias -scalar-rep-

    450.soplex: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
                -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
                -opt-malloc-options=3

    453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
                -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
                -unroll4 -ansi-alias

Fortran benchmarks:

    410.bwaves: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

    416.gamess: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
                -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
                -unroll2 -Ob0 -ansi-alias -scalar-rep-

    434.zeusmp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
                -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)

    437.leslie3d: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
                  -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
                  -opt-malloc-options=3 -opt-prefetch

  459.GemsFDTD: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
                -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
                -unroll2 -Ob0

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Peak Optimization Flags (Continued)

465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll4 -auto -inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-opt-prefetch -auto-ilp32 -nofor_main(*)

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes

(*) Indicates an optimization flag that was found in a portability variable.

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revD.20091208.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revD.20091208.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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