SPEC® CFP2006 Result
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Acer Incorporated

Gateway GR160 F1 (Intel Xeon X5570)

$SPECfp^{®}_rate2006 = $ 193

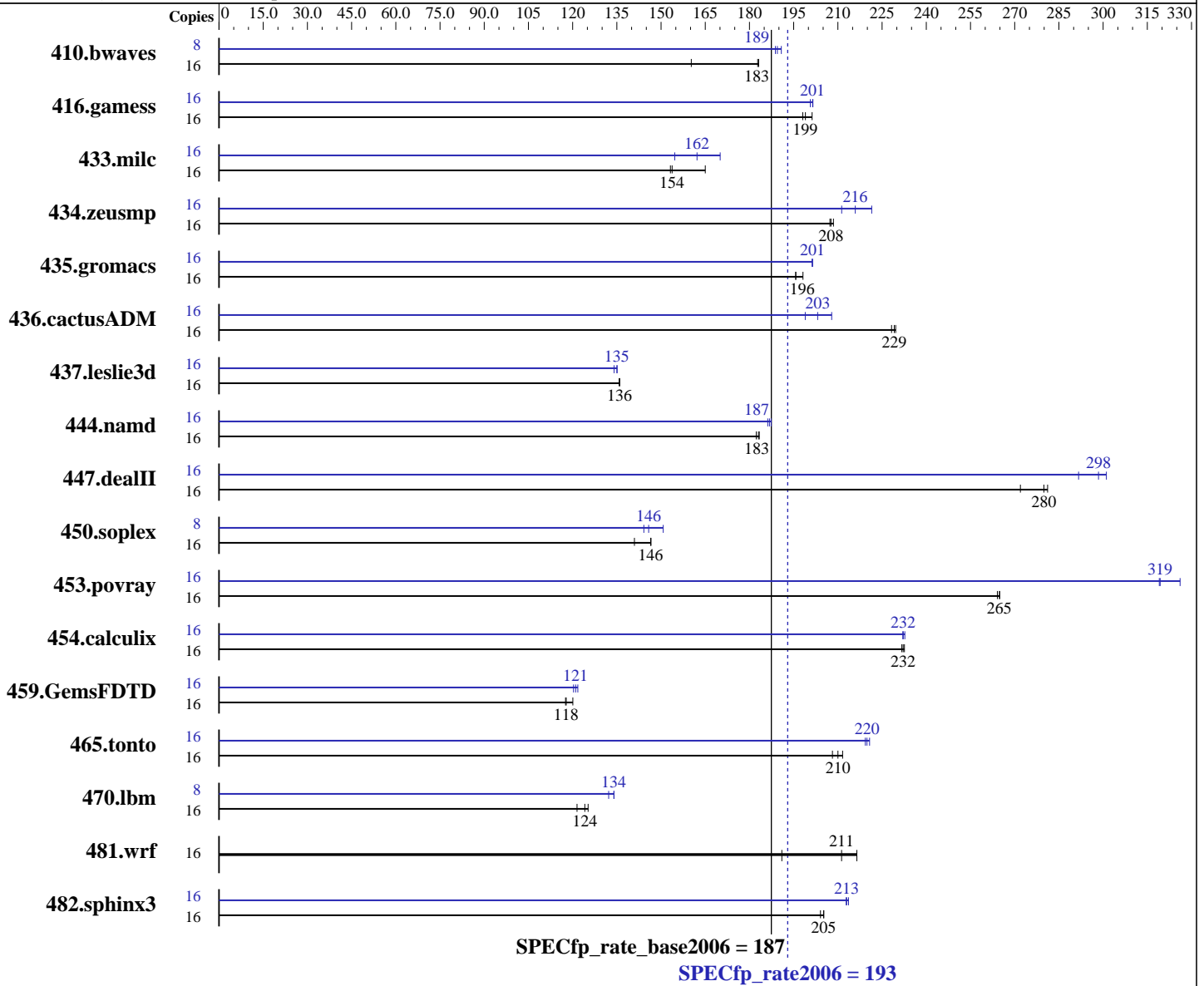$SPECfp_rate_base2006 = $ 187

CPU2006 license: 97
Test sponsor: Acer Incorporated
Tested by: Acer Incorporated

Test date: Dec-2009
Hardware Availability: Jan-2010
Software Availability: Feb-2009

SPECfp_rate_base2006 = 187

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Hardware

CPU Name: Intel Xeon X5570
CPU Characteristics: Intel Turbo Boost Technology up to 3.33 GHz
CPU MHz: 2933
FPU: Integrated
CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip, 2 threads/core
CPU(s) orderable: 1, 2 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core

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Software

Operating System: SUSE Linux Enterprise Server 11 (x86_64)
Kernel 2.6.27.19-5
Compiler: Intel C++ and Fortran Compiler 11.0 for Linux
Build 20090131 Package ID: l_cproc_p_11.0.080,
l_cprof_p_11.0.080
Auto Parallel: No
File System: ReiserFS
System State: Run level 3 (multi-user)
Base Pointers: 64-bit

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L3 Cache: 8 MB I+D on chip per chip
Other Cache: None
Memory: 24 GB (6 x 4 GB DDR3-1333 RDIMM)
Disk Subsystem: 160 GB SATA II, 5400 RPM
Other Hardware: None

Peak Pointers: 32/64-bit
Other Software: Binutils 2.18.50.0.7.20080502

Results Table

Benchmark	Base						Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	16	1357	160	**1190**	**183**	1187	183	8	**574**	**189**	570	191	576	189
416.gamess	16	**1574**	**199**	1557	201	1582	198	16	1561	201	1554	202	**1555**	**201**
433.milc	16	890	165	**955**	**154**	959	153	16	864	170	**906**	**162**	950	155
434.zeusmp	16	702	207	698	208	**701**	**208**	16	689	211	**674**	**216**	657	221
435.gromacs	16	577	198	**584**	**196**	584	196	16	567	201	568	201	**567**	**201**
436.cactusADM	16	833	230	838	228	**834**	**229**	16	**941**	**203**	961	199	920	208
437.leslie3d	16	1106	136	**1107**	**136**	1107	136	16	1113	135	1122	134	**1114**	**135**
444.namd	16	**701**	**183**	700	183	704	182	16	689	186	**687**	**187**	685	187
447.dealII	16	673	272	651	281	**654**	**280**	16	**613**	**298**	628	292	608	301
450.soplex	16	947	141	**911**	**146**	910	147	8	**458**	**146**	463	144	443	151
453.povray	16	**321**	**265**	321	265	322	264	16	267	319	**267**	**319**	261	326
454.calculix	16	568	233	**569**	**232**	570	232	16	569	232	**569**	**232**	567	233
459.GemsFDTD	16	1444	118	**1441**	**118**	1414	120	16	1411	120	**1403**	**121**	1394	122
465.tonto	16	**750**	**210**	744	212	756	208	16	718	219	**716**	**220**	713	221
470.lbm	16	1755	125	**1771**	**124**	1810	121	8	831	132	**820**	**134**	820	134
481.wrf	16	**846**	**211**	826	216	936	191	16	**846**	**211**	826	216	936	191
482.sphinx3	16	1528	204	**1520**	**205**	1519	205	16	1465	213	**1464**	**213**	1460	214

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

    The config file option 'submit' was used.
     numactl was used to bind copies to the cores

General Notes

    This result was measured on the Gateway GR160 F1.
    The Acer AR160 F1 and Gateway GR160 F1 are electronically equivalent.

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Base Compiler Invocation

C benchmarks:
    icc

C++ benchmarks:
    icpc

Fortran benchmarks:
    ifort

Benchmarks using both Fortran and C:
    icc ifort

Base Portability Flags

         410.bwaves: -DSPEC_CPU_LP64
         416.gamess: -DSPEC_CPU_LP64
           433.milc: -DSPEC_CPU_LP64
         434.zeusmp: -DSPEC_CPU_LP64
        435.gromacs: -DSPEC_CPU_LP64 -nofor_main
       436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
        437.leslie3d: -DSPEC_CPU_LP64
           444.namd: -DSPEC_CPU_LP64
          447.dealII: -DSPEC_CPU_LP64
          450.soplex: -DSPEC_CPU_LP64
          453.povray: -DSPEC_CPU_LP64
        454.calculix: -DSPEC_CPU_LP64 -nofor_main
       459.GemsFDTD: -DSPEC_CPU_LP64
           465.tonto: -DSPEC_CPU_LP64
            470.lbm: -DSPEC_CPU_LP64
            481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
        482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
    -xSSE4.2 -ipo -O3 -no-prec-div -static

C++ benchmarks:
    -xSSE4.2 -ipo -O3 -no-prec-div -static

Fortran benchmarks:
    -xSSE4.2 -ipo -O3 -no-prec-div -static

Benchmarks using both Fortran and C:
    -xSSE4.2 -ipo -O3 -no-prec-div -static

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Peak Compiler Invocation

C benchmarks (except as noted below):
    `icc`

    482.sphinx3: `icc -m32`

C++ benchmarks (except as noted below):
    `icpc`

    450.soplex: `icpc -m32`

Fortran benchmarks (except as noted below):
    `ifort`

    437.leslie3d: `ifort -m32`

Benchmarks using both Fortran and C:
    `icc ifort`

Peak Portability Flags

    410.bwaves: `-DSPEC_CPU_LP64`
    416.gamess: `-DSPEC_CPU_LP64`
      433.milc: `-DSPEC_CPU_LP64`
    434.zeusmp: `-DSPEC_CPU_LP64`
    435.gromacs: `-DSPEC_CPU_LP64 -nofor_main`
    436.cactusADM: `-DSPEC_CPU_LP64 -nofor_main`
      444.namd: `-DSPEC_CPU_LP64`
     447.dealII: `-DSPEC_CPU_LP64`
    453.povray: `-DSPEC_CPU_LP64`
    454.calculix: `-DSPEC_CPU_LP64 -nofor_main`
    459.GemsFDTD: `-DSPEC_CPU_LP64`
      465.tonto: `-DSPEC_CPU_LP64`
       470.lbm: `-DSPEC_CPU_LP64`
       481.wrf: `-DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

        433.milc: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)`
            `-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)`
            `-fno-alias`

        470.lbm: `-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch`
            `-auto-ilp32`

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Peak Optimization Flags (Continued)

482.sphinx3: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2

C++ benchmarks:

444.namd: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-fno-alias -auto-ilp32

447.dealII: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll2 -ansi-alias -scalar-rep-

450.soplex: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-opt-malloc-options=3

453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

416.gamess: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll2 -Ob0 -ansi-alias -scalar-rep-

434.zeusmp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)

437.leslie3d: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-opt-malloc-options=3 -opt-prefetch

459.GemsFDTD: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll2 -Ob0 -opt-prefetch

465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll4 -auto

Benchmarks using both Fortran and C:

435.gromacs: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-opt-prefetch -auto-ilp32

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Peak Optimization Flags (Continued)

436.cactusADM: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll2 -opt-prefetch -auto-ilp32

454.calculix: -xSSE4.2 -ipo -O3 -no-prec-div -static -auto-ilp32

481.wrf: basepeak = yes

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic11.0-fp-linux64-revF.20100201.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic11.0-fp-linux64-revF.20100201.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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