



SPEC[®] CFP2006 Result

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Dell Inc.

SPECfp[®]_rate2006 = 125

PowerEdge R805 (AMD Opteron 2423 HE, 2.00 GHz)

SPECfp_rate_base2006 = 113

CPU2006 license: 55

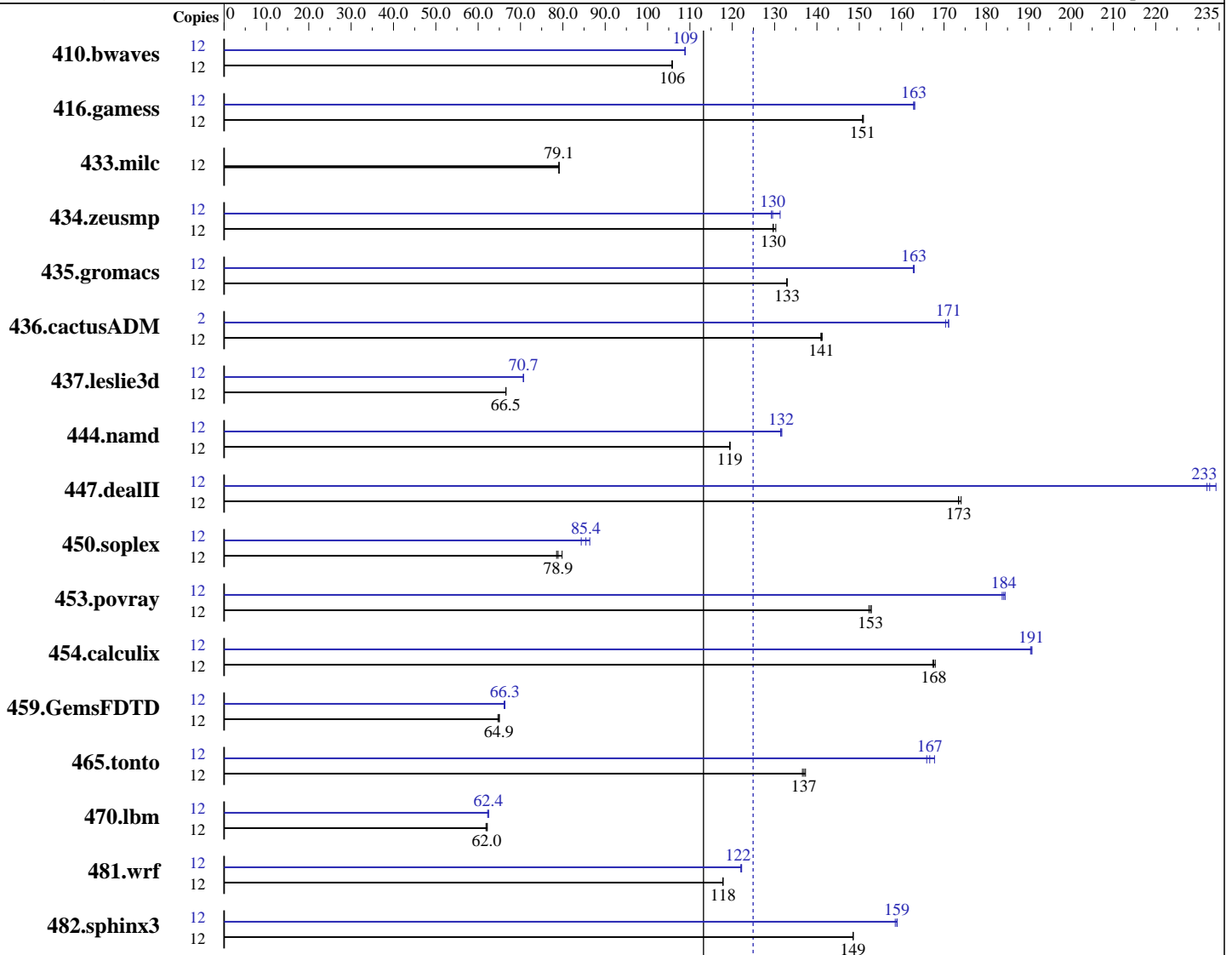
Test date: Jul-2009

Test sponsor: Dell Inc.

Hardware Availability: Aug-2009

Tested by: Dell Inc.

Software Availability: Apr-2009



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Hardware

CPU Name: AMD Opteron 2423 HE
 CPU Characteristics:
 CPU MHz: 2000
 FPU: Integrated
 CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 64 KB I + 64 KB D on chip per core
 Secondary Cache: 512 KB I+D on chip per core

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Software

Operating System: Red Hat Enterprise Linux Server release 5.3, Kernel 2.6.18-128.el5
 Compiler: PGI Server Complete Version 8.0 x86 Open64 4.2.2 Compiler Suite (from AMD)
 Auto Parallel: Yes
 File System: ext3
 System State: Run level 3 (Full multiuser with network)
 Base Pointers: 64-bit
 Peak Pointers: 32/64-bit

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L3 Cache: 6 MB I+D on chip per chip
Other Cache: None
Memory: 32 GB (8 x 4 GB DDR2-800)
Disk Subsystem: 1 x 73 GB 15000 RPM SAS
Other Hardware: None

Other Software: binutils 2.18

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	12	1542	106	<u>1541</u>	<u>106</u>	1541	106	12	<u>1497</u>	<u>109</u>	1497	109	1499	109
416.gamess	12	1558	151	1556	151	<u>1558</u>	<u>151</u>	12	<u>1442</u>	<u>163</u>	1443	163	1441	163
433.milc	12	<u>1393</u>	<u>79.1</u>	1393	79.1	1393	79.1	12	<u>1393</u>	<u>79.1</u>	1393	79.1	1393	79.1
434.zeusmp	12	<u>842</u>	<u>130</u>	838	130	842	130	12	832	131	845	129	<u>843</u>	<u>130</u>
435.gromacs	12	645	133	644	133	<u>644</u>	<u>133</u>	12	526	163	<u>526</u>	<u>163</u>	526	163
436.cactusADM	12	<u>1016</u>	<u>141</u>	1018	141	1015	141	2	<u>140</u>	<u>171</u>	140	170	140	171
437.leslie3d	12	<u>1696</u>	<u>66.5</u>	1696	66.5	1694	66.6	12	<u>1596</u>	<u>70.7</u>	1595	70.7	1596	70.7
444.namd	12	<u>806</u>	<u>119</u>	806	119	805	120	12	<u>732</u>	<u>132</u>	732	131	731	132
447.dealII	12	<u>791</u>	<u>173</u>	789	174	792	173	12	591	232	<u>590</u>	<u>233</u>	586	234
450.soplex	12	1274	78.5	<u>1269</u>	<u>78.9</u>	1254	79.8	12	1187	84.3	<u>1171</u>	<u>85.4</u>	1159	86.4
453.povray	12	419	152	<u>418</u>	<u>153</u>	418	153	12	<u>347</u>	<u>184</u>	347	184	346	184
454.calculix	12	592	167	<u>591</u>	<u>168</u>	589	168	12	519	191	520	190	<u>519</u>	<u>191</u>
459.GemsFDTD	12	1969	64.7	1958	65.0	<u>1961</u>	<u>64.9</u>	12	<u>1921</u>	<u>66.3</u>	1926	66.1	1920	66.3
465.tonto	12	860	137	<u>862</u>	<u>137</u>	865	137	12	<u>709</u>	<u>167</u>	704	168	712	166
470.lbm	12	2652	62.2	<u>2658</u>	<u>62.0</u>	2666	61.9	12	2640	62.5	2649	62.2	<u>2642</u>	<u>62.4</u>
481.wrf	12	<u>1138</u>	<u>118</u>	1137	118	1138	118	12	1098	122	<u>1097</u>	<u>122</u>	1097	122
482.sphinx3	12	1574	149	1575	149	<u>1575</u>	<u>149</u>	12	<u>1472</u>	<u>159</u>	1476	158	1472	159

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
'numactl' was used to bind copies to the cores.
See the configuration file for details.

Operating System Notes

'ulimit -s unlimited' was used to set environment stack size
'ulimit -l 2097152' was used to set environment locked pages in memory limit

Set vm/nr_hugepages=5400 in /etc/sysctl.conf
mount -t hugetlbfs nodev /mnt/hugepages



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General Notes

Environment variables set by runspec before the start of the run:

HUGETLB_LIMIT = "450"

LD_LIBRARY_PATH = "/root/cpu2006-1.1/amd0905is-libs/64:/root/cpu2006-1.1/amd0905is-libs/32"

NCPUS = "6"

PGI_HUGE_PAGES = "450"

The x86 Open64 Compiler Suite is only available from (and supported by) AMD at <http://developer.amd.com/cpu/open64>

Base Compiler Invocation

C benchmarks:

pgcc

C++ benchmarks:

pgcpp

Fortran benchmarks:

pgf95

Benchmarks using both Fortran and C:

pgcc pgf95

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -Mnomain
436.cactusADM: -DSPEC_CPU_LP64 -Mnomain
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.deallI: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -Mnomain
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64



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Base Optimization Flags

C benchmarks:

-fastsse -Msmartalloc=huge -Mfprelaxed -Mipa=fast -Mipa=inline
-tp shanghai-64 -Bstatic_pgi

C++ benchmarks:

-fastsse -Msmartalloc=huge -Mfprelaxed --zc_eh -Mipa=fast
-Mipa=inline -tp shanghai-64 -Bstatic_pgi

Fortran benchmarks:

-fastsse -Msmartalloc=huge -Mfprelaxed -Mvect=short -Mipa=fast
-Mipa=inline -tp shanghai-64 -Bstatic_pgi

Benchmarks using both Fortran and C:

-fastsse -Msmartalloc=huge -Mfprelaxed -Mipa=fast -Mipa=inline
-tp shanghai-64 -Mvect=short -Bstatic_pgi

Base Other Flags

C benchmarks:

-Mipa=jobs:4

C++ benchmarks:

-Mipa=jobs:4

Fortran benchmarks:

-Mipa=jobs:4

Benchmarks using both Fortran and C:

-Mipa=jobs:4

Peak Compiler Invocation

C benchmarks:

pgcc

C++ benchmarks (except as noted below):

openCC

444.namd: pgcpp

Fortran benchmarks (except as noted below):

openf95

410.bwaves: pgf95

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Peak Compiler Invocation (Continued)

434.zeusmp: pgf95

437.leslie3d: pgf95

Benchmarks using both Fortran and C (except as noted below):

pgcc pgf95

435.gromacs: opencc openf95

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
 416.gamess: -DSPEC_CPU_LP64
 433.milc: -DSPEC_CPU_LP64
 434.zeusmp: -DSPEC_CPU_LP64
 435.gromacs: -DSPEC_CPU_LP64
 436.cactusADM: -DSPEC_CPU_LP64 -Mnomain
 437.leslie3d: -DSPEC_CPU_LP64
 444.namd: -DSPEC_CPU_LP64
 453.povray: -DSPEC_CPU_LP64
 454.calculix: -DSPEC_CPU_LP64 -Mnomain
 459.GemsFDTD: -DSPEC_CPU_LP64
 465.tonto: -DSPEC_CPU_LP64
 470.lbm: -DSPEC_CPU_LP64
 481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
 482.sphinx3: -DSPEC_CPU_LP64

Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

470.lbm: -fastsse -Msmartalloc=huge -Mprefetch=t0 -Mloop32
-Mfprelaxed -Mipa=fast -Mipa=inline -tp shanghai-64
-Bstatic_pgi

482.sphinx3: -Mphi=indirect(pass 1) -Mpfo=indirect(pass 2)
-Mipa=fast(pass 2) -Mipa=inline(pass 2) -fastsse
-Mfprelaxed -Msmartalloc -tp shanghai-64 -Bstatic_pgi

C++ benchmarks:

444.namd: -Mphi(pass 1) -Mpfo(pass 2) -Mipa=fast(pass 2)
-Mipa=inline(pass 2) -fastsse -Munroll=n:4 -Munroll=m:8
-Msmartalloc=huge -Mnodepchk -Mfprelaxed --zc_eh
-tp shanghai-64 -Bstatic_pgi

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Peak Optimization Flags (Continued)

447.dealll: -march=barcelona -Ofast -static -INLINE:aggressive=on
-LNO:opt=0 -Wf,-fno-exceptions -m32 -OPT:unroll_times_max=8
-OPT:unroll_size=256 -OPT:unroll_level=2 -HP:bdt=2m:heap=2m
-GRA:unspill=on -CG:cmp_peep=on -TENV:frame_pointer=off

450.soplex: -march=barcelona -fb_create fbdata(pass 1)
-fb_opt fbdata(pass 2) -O3 -INLINE:aggressive=on
-OPT:IEEE_arith=3 -OPT:IEEE_NaN_Inf=off
-OPT:fold_unsigned_relops=on -OPT:malloc_alg=1
-CG:load_exe=0 -fno-exceptions -m32 -HP:bdt=2m

453.povray: -march=barcelona -fb_create fbdata(pass 1)
-fb_opt fbdata(pass 2) -Ofast -INLINE:aggressive=on
-HP:bdt=2m:heap=2m

Fortran benchmarks:

410.bwaves: -fastsse -Msmartalloc -Mprefetch=nta -Mfprelaxed
-Mipa=fast -Mipa=inline -tp shanghai-64 -Bstatic_pgi

416.gamess: -march=barcelona -fb_create fbdata(pass 1)
-fb_opt fbdata(pass 2) -O2 -OPT:Ofast -OPT:ro=3
-OPT:unroll_size=256 -HP:bdt=2m:heap=2m

434.zeusmp: -fastsse -Mfprelaxed -Mprefetch=distance:8 -Mprefetch=t0
-Msmartalloc=huge -Msmartalloc=hugebss -Mipa=fast
-Mipa=inline -tp shanghai-64 -Bstatic_pgi

437.leslie3d: -Mphi=indirect(pass 1) -Mpfo=indirect(pass 2)
-Mipa=fast(pass 2) -Mipa=inline(pass 2) -fastsse
-Mvect=fuse -Msmartalloc=huge -Mprefetch=distance:8
-Mprefetch=t0 -Mfprelaxed -tp shanghai-64 -Bstatic_pgi

459.GemsFDTD: -march=barcelona -Ofast -LNO:fission=2 -LNO:simd=2
-LNO:prefetch_ahead=1 -CG:load_exe=0 -HP

465.tonto: -march=barcelona -Ofast -OPT:alias=no_f90_pointer_alias
-LNO:blocking=off -CG:load_exe=1 -IPA:plimit=525 -HP

Benchmarks using both Fortran and C:

435.gromacs: -march=barcelona -Ofast -OPT:rsqrt=2 -HP:bdt=2m:heap=2m

436.cactusADM: -fastsse -Mconcur -Msmartalloc=huge -Mfprelaxed -Mipa=fast
-Mipa=inline -tp shanghai-64 -Bstatic_pgi

454.calculix: -Mphi=indirect(pass 1) -Mpfo=indirect(pass 2)
-Mipa=fast(pass 2) -Mipa=inline(pass 2) -fastsse
-Mvect=short -Msmartalloc=huge -Mprefetch=t0 -Mpre

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Peak Optimization Flags (Continued)

454.calculix (continued):

-Mfprelaxed -tp shanghai-64 -Bstatic_pgi

481.wrf: -fastsse -Mvect=noaltcode -Msmartalloc=huge

-Mprefetch=distance:8 -Mfprelaxed -tp shanghai-64

-Bstatic_pgi

Peak Other Flags

C benchmarks:

-Mipa=jobs:4(pass 2)

C++ benchmarks:

444.namd: -Mipa=jobs:4(pass 2)

Fortran benchmarks:

410.bwaves: -Mipa=jobs:4

434.zeusmp: -Mipa=jobs:4

437.leslie3d: -Mipa=jobs:4(pass 2)

Benchmarks using both Fortran and C:

436.cactusADM: -Mipa=jobs:4

454.calculix: -Mipa=jobs:4(pass 2)

The flags files that were used to format this result can be browsed at

http://www.spec.org/cpu2006/flags/pgi80_linux_flags.20090710.html

<http://www.spec.org/cpu2006/flags/x86-open64-4.2.2-flags-revB.html>

<http://www.spec.org/cpu2006/flags/amd-platform.html>

You can also download the XML flags sources by saving the following links:

http://www.spec.org/cpu2006/flags/pgi80_linux_flags.20090710.xml

<http://www.spec.org/cpu2006/flags/x86-open64-4.2.2-flags-revB.xml>

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