



SPEC[®] CFP2006 Result

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NEC Corporation

Express5800/T120a-M
(Intel Xeon E5504)

SPECfp[®]_rate2006 = 59.5

SPECfp_rate_base2006 = 57.5

CPU2006 license: 9006

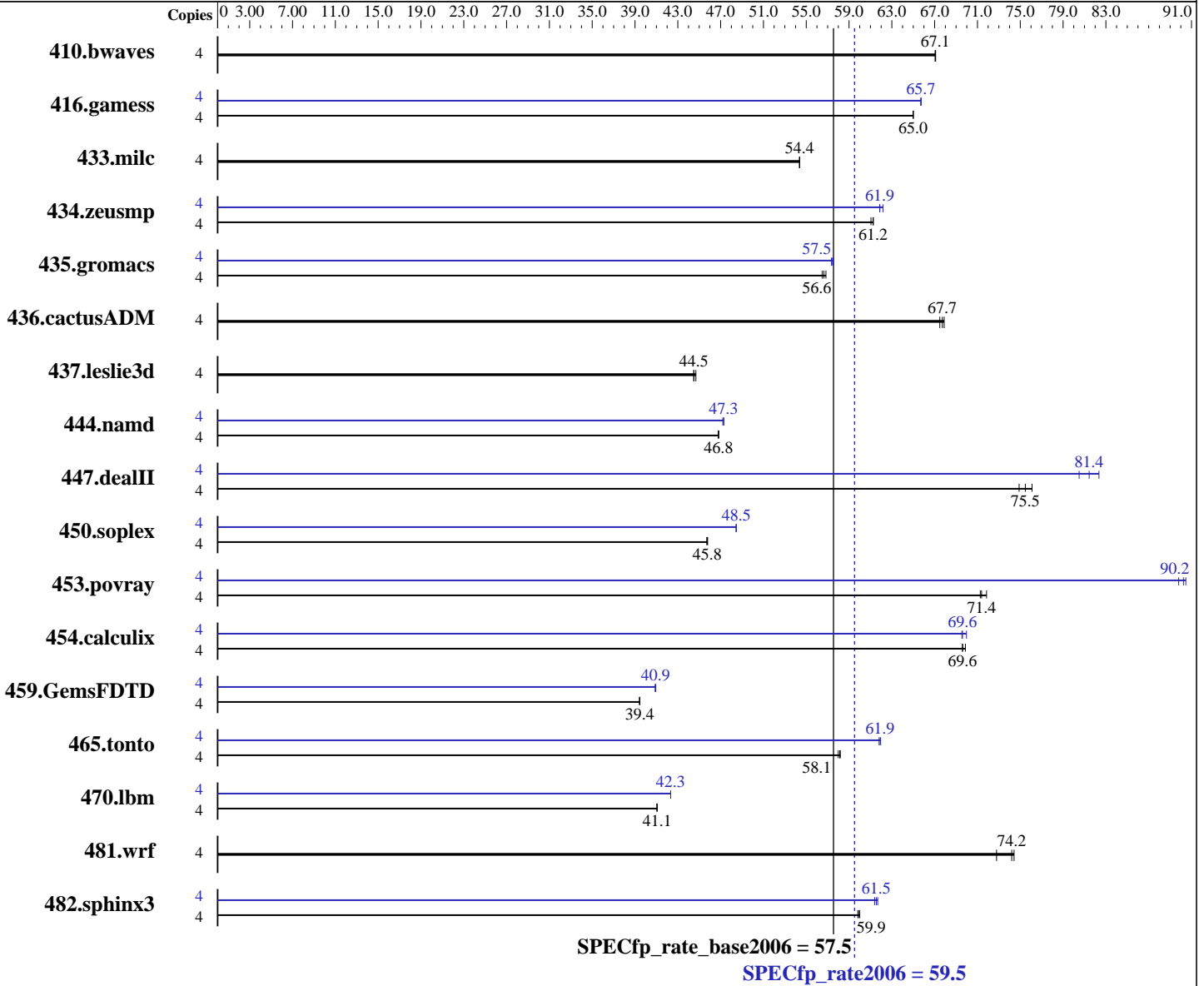
Test sponsor: NEC Corporation

Tested by: NEC Corporation

Test date: May-2009

Hardware Availability: Apr-2009

Software Availability: Feb-2009



Hardware

CPU Name: Intel Xeon E5504
 CPU Characteristics:
 CPU MHz: 2000
 FPU: Integrated
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

Continued on next page

Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64)
 SP2 with patch Linux kernel 20090119,
 Kernel 2.6.16.60-0.34-smp
 Compiler: Intel C++ and Fortran Compiler Professional 11.0
 for Linux
 Build 20090131 Package ID: l_cproc_p_11.0.081,
 l_cprof_p_11.0.081
 Auto Parallel: No
 File System: ReiserFS

Continued on next page



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L3 Cache: 4 MB I+D on chip per chip
Other Cache: None
Memory: 24 GB (6 X 4 GB PC3-8500R running at 800 MHz)
Disk Subsystem: 1x73.2 GB SAS, 15000 RPM
Other Hardware: None

System State: Run level 3 (multi-user)
Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: Binutils 2.18.50.0.7.20080502

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	4	811	67.0	811	67.1	810	67.1	4	811	67.0	811	67.1	810	67.1
416.gamess	4	1205	65.0	1205	65.0	1205	65.0	4	1191	65.7	1192	65.7	1192	65.7
433.milc	4	675	54.4	675	54.4	675	54.4	4	675	54.4	675	54.4	675	54.4
434.zeusmp	4	594	61.2	594	61.3	596	61.0	4	588	61.9	585	62.2	588	61.9
435.gromacs	4	506	56.5	502	56.8	504	56.6	4	498	57.4	497	57.5	497	57.5
436.cactusADM	4	706	67.7	708	67.5	704	67.9	4	706	67.7	708	67.5	704	67.9
437.leslie3d	4	841	44.7	845	44.5	845	44.5	4	841	44.7	845	44.5	845	44.5
444.namd	4	685	46.8	685	46.8	686	46.8	4	678	47.3	678	47.3	680	47.2
447.dealII	4	601	76.1	606	75.5	611	74.9	4	568	80.5	556	82.4	562	81.4
450.soplex	4	729	45.8	730	45.7	729	45.8	4	688	48.5	688	48.5	688	48.5
453.povray	4	296	71.8	298	71.4	299	71.3	4	236	90.2	235	90.5	237	89.8
454.calculix	4	472	69.9	474	69.6	474	69.6	4	474	69.6	475	69.5	472	70.0
459.GemsFDTD	4	1076	39.4	1076	39.4	1076	39.4	4	1038	40.9	1037	40.9	1037	40.9
465.tonto	4	679	58.0	677	58.2	677	58.1	4	635	62.0	636	61.9	637	61.8
470.lbm	4	1339	41.0	1338	41.1	1338	41.1	4	1298	42.3	1298	42.3	1298	42.3
481.wrf	4	602	74.2	600	74.4	614	72.8	4	602	74.2	600	74.4	614	72.8
482.sphinx3	4	1302	59.9	1299	60.0	1303	59.8	4	1264	61.7	1267	61.5	1270	61.4

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
numactl was used to bind copies to the cores

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

Platform Notes

Default BIOS settings were used.



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General Notes

The NEC Express5800/T120a-M(Intel Xeon E5504) and the Bull NovaScale T860 E2 (Intel Xeon E5504, 2.00 GHz) models are electronically equivalent. The results have been measured on a NEC Express5800/T120a-M(Intel Xeon E5504) model.

Base Compiler Invocation

C benchmarks:

icc

C++ benchmarks:

icpc

Fortran benchmarks:

ifort

Benchmarks using both Fortran and C:

icc ifort

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static

Continued on next page



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Base Optimization Flags (Continued)

Fortran benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static

Benchmarks using both Fortran and C:

-xSSE4.2 -ipo -O3 -no-prec-div -static

Peak Compiler Invocation

C benchmarks (except as noted below):

icc

482.sphinx3: icc -m32

C++ benchmarks (except as noted below):

icpc

450.soplex: icpc -m32

Fortran benchmarks:

ifort

Benchmarks using both Fortran and C:

icc ifort

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
 416.gamess: -DSPEC_CPU_LP64
 433.milc: -DSPEC_CPU_LP64
 434.zeusmp: -DSPEC_CPU_LP64
 435.gromacs: -DSPEC_CPU_LP64 -nofor_main
 436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
 437.leslie3d: -DSPEC_CPU_LP64
 444.namd: -DSPEC_CPU_LP64
 447.dealII: -DSPEC_CPU_LP64
 453.povray: -DSPEC_CPU_LP64
 454.calculix: -DSPEC_CPU_LP64 -nofor_main
 459.GemsFDTD: -DSPEC_CPU_LP64
 465.tonto: -DSPEC_CPU_LP64
 470.lbm: -DSPEC_CPU_LP64
 481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX



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Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

470.lbm: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch
-auto-ilp32

482.sphinx3: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2

C++ benchmarks:

444.namd: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-fno-alias -auto-ilp32

447.dealII: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll2 -ansi-alias -scalar-rep-

450.soplex: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-opt-malloc-options=3

453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll2 -Ob0 -ansi-alias -scalar-rep-

434.zeusmp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)

437.leslie3d: basepeak = yes

459.GemsFDTD: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll2 -Ob0 -opt-prefetch

465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll4 -auto

Benchmarks using both Fortran and C:

Continued on next page



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Peak Optimization Flags (Continued)

435.gromacs: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-opt-prefetch -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: -xSSE4.2 -ipo -O3 -no-prec-div -static -auto-ilp32

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-fp-linux64-revG.html>

<http://www.spec.org/cpu2006/flags/NEC-Intel-Linux-Settings-flags-revE.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-fp-linux64-revG.xml>

<http://www.spec.org/cpu2006/flags/NEC-Intel-Linux-Settings-flags-revE.xml>

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For other inquiries, please contact webmaster@spec.org.

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