SPEC® CFP2006 Result

GIGA-BYTE Technology Co. Ltd.

(Test Sponsor: Intel Corporation)

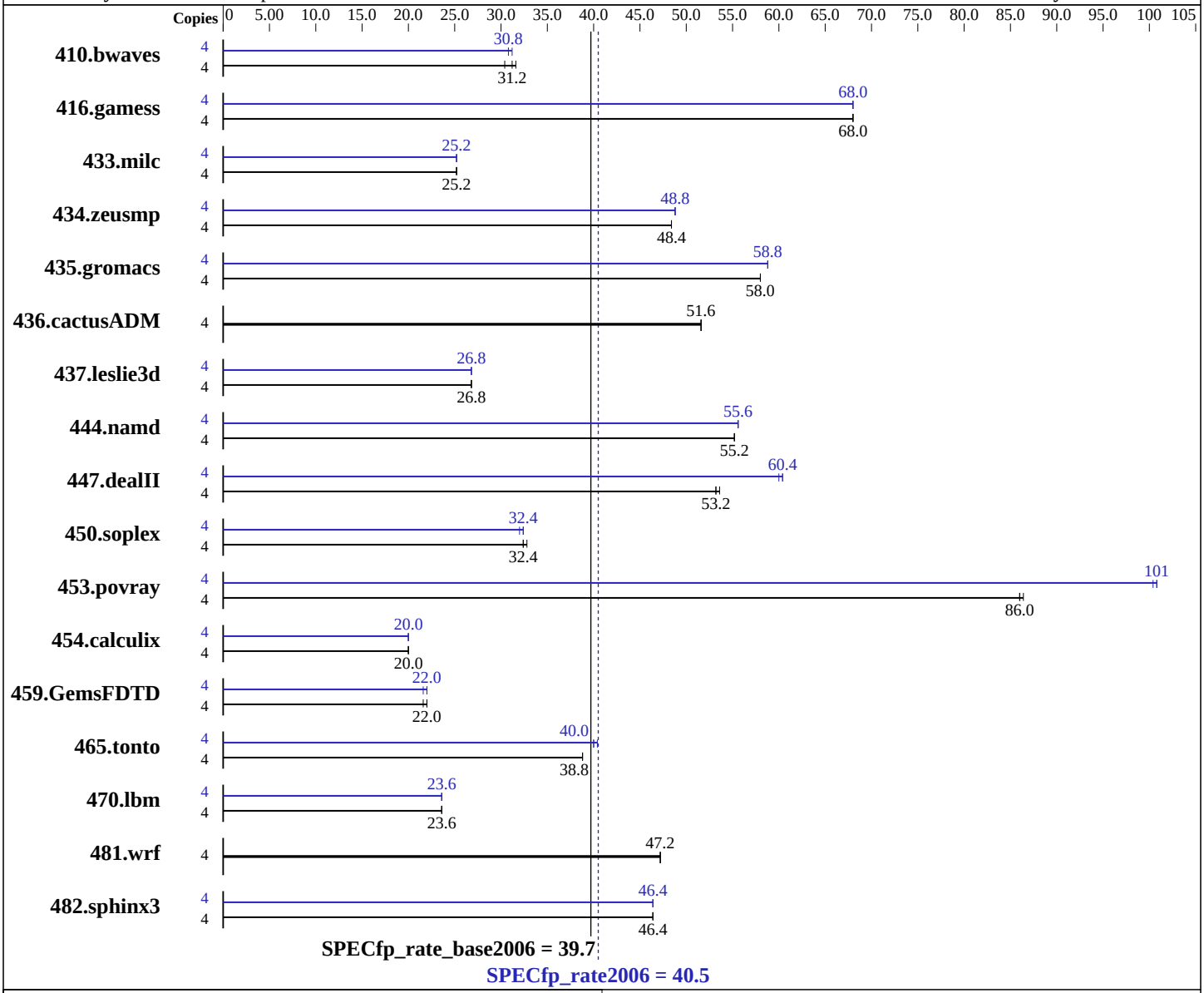
Gigabyte MA78GM-S2H Motherboard (AMD Phenom II X4 920)

SPECfp®_rate2006 = 40.5

SPECfp_rate_base2006 = 39.7

CPU2006 license: 13
Test sponsor: Intel Corporation
Tested by: Intel Corporation

Test date: May-2009
Hardware Availability: Jan-2009
Software Availability: Nov-2008



Hardware

CPU Name:	AMD Phenom II X4 920
CPU Characteristics:	
CPU MHz:	2800
FPU:	Integrated
CPU(s) enabled:	4 cores, 1 chip, 4 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	64 KB I + 64 KB D on chip per core
Secondary Cache:	512 KB I+D on chip per core

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Software

Operating System:	Windows Vista Ultimate w/ SP1 (64-bit)
Compiler:	Intel C++ Compiler Professional 11.0 for IA32 Build 20080930 Package ID: w_cproc_p_11.0.054 Intel Visual Fortran Compiler Professional 11.0 for IA32 Build 20080930 Package ID: w_cprof_p_11.0.054 Microsoft Visual Studio 2008 (for libraries)
Auto Parallel:	No
File System:	NTFS

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L3 Cache:	6 MB I+D on chip per chip	System State:	Default
Other Cache:	None	Base Pointers:	32-bit
Memory:	4 GB (4x1GB DDR2-800 CL5)	Peak Pointers:	32-bit
Disk Subsystem:	Seagate 320 GB SATA, 7200RPM	Other Software:	SmartHeap Library Version 8.1 from http://www.microquill.com/
Other Hardware:	None		

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	4	1725	31.6	**1741**	**31.2**	1799	30.4	4	1742	31.2	1776	30.8	**1766**	**30.8**
416.gamess	4	1150	68.0	1149	68.0	**1150**	**68.0**	4	1153	68.0	1154	68.0	**1153**	**68.0**
433.milc	4	**1459**	**25.2**	1459	25.2	1458	25.2	4	**1457**	**25.2**	1458	25.2	1457	25.2
434.zeusmp	4	753	48.4	**753**	**48.4**	751	48.4	4	749	48.8	**748**	**48.8**	748	48.8
435.gromacs	4	**494**	**58.0**	493	58.0	494	58.0	4	**487**	**58.8**	487	58.8	487	58.8
436.cactusADM	4	926	51.6	**926**	**51.6**	927	51.6	4	926	51.6	**926**	**51.6**	927	51.6
437.leslie3d	4	1399	26.8	1397	26.8	**1398**	**26.8**	4	1398	26.8	1397	26.8	**1397**	**26.8**
444.namd	4	581	55.2	581	55.2	**581**	**55.2**	4	577	55.6	577	55.6	**577**	**55.6**
447.dealII	4	**857**	**53.2**	862	53.2	856	53.6	4	**759**	**60.4**	763	60.0	757	60.4
450.soplex	4	1023	32.8	1032	32.4	**1025**	**32.4**	4	1037	32.0	1027	32.4	**1034**	**32.4**
453.povray	4	248	86.0	247	86.4	**247**	**86.0**	4	211	101	212	100	**211**	**101**
454.calculix	4	1642	20.0	1642	20.0	**1642**	**20.0**	4	1642	20.0	**1642**	**20.0**	1643	20.0
459.GemsFDTD	4	1951	21.6	1941	22.0	**1944**	**22.0**	4	1954	21.6	1941	22.0	**1944**	**22.0**
465.tonto	4	1015	38.8	1013	38.8	**1014**	**38.8**	4	**979**	**40.0**	982	40.0	977	40.4
470.lbm	4	2327	23.6	**2328**	**23.6**	2328	23.6	4	2327	23.6	2328	23.6	**2327**	**23.6**
481.wrf	4	944	47.2	948	47.2	**947**	**47.2**	4	944	47.2	948	47.2	**947**	**47.2**
482.sphinx3	4	1685	46.4	1680	46.4	**1680**	**46.4**	4	1682	46.4	**1680**	**46.4**	1679	46.4

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The config file option 'submit' was used.
```

General Notes

```
Tested systems can be used with Shin-G ATX case, Antec NeoPower 480W power supply
Binaries were built on Windows Vista Ultimate (32-bit)
```

Base Compiler Invocation

C benchmarks:

```
icl -Qvc9 -Qc99
```

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Base Compiler Invocation (Continued)

C++ benchmarks:
icl -Qvc9

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icl -Qvc9 -Qc99 ifort

Base Portability Flags

436.cactusADM: -Qlowercase /assume:underscore
444.namd: -TP
447.dealII: -DDEAL_II_MEMBER_VAR_SPECIALIZATION_BUG
453.povray: -DSPEC_CPU_WINDOWS_ICL
454.calculix: -DSPEC_CPU_NOZMODIFIER -Qlowercase
481.wrf: -DSPEC_CPU_WINDOWS_ICL

Base Optimization Flags

C benchmarks:
/arch:SSE2 -Qipo -O3 -Qprec-div- -Qopt-prefetch /F1000000000

C++ benchmarks:
/arch:SSE2 -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qcxx-features
/F1000000000 shlw32m.lib -link /FORCE:MULTIPLE

Fortran benchmarks:
/arch:SSE2 -Qipo -O3 -Qprec-div- -Qopt-prefetch /F1000000000

Benchmarks using both Fortran and C:
/arch:SSE2 -Qipo -O3 -Qprec-div- -Qopt-prefetch /F1000000000

Peak Compiler Invocation

C benchmarks:
icl -Qvc9 -Qc99

C++ benchmarks:
icl -Qvc9

Fortran benchmarks:
ifort

Continued on next page

Peak Compiler Invocation (Continued)

Benchmarks using both Fortran and C:
icl -Qvc9 -Qc99 ifort

Peak Portability Flags

436.cactusADM: -Qlowercase /assume:underscore
444.namd: -TP
447.dealII: -DDEAL_II_MEMBER_VAR_SPECIALIZATION_BUG
453.povray: -DSPEC_CPU_WINDOWS_ICL
454.calculix: -DSPEC_CPU_NOZMODIFIER -Qlowercase
481.wrf: -DSPEC_CPU_WINDOWS_ICL

Peak Optimization Flags

C benchmarks:

433.milc: /arch:SSE2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Oa /F1000000000

470.lbm: /arch:SSE2 -Qipo -O3 -Qprec-div- -Qopt-prefetch
/F1000000000

482.sphinx3: /arch:SSE2 -Qipo -O3 -Qprec-div- -Qunroll2 /F1000000000

C++ benchmarks:

444.namd: /arch:SSE2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Oa /F1000000000 shlw32m.lib
-link /FORCE:MULTIPLE

447.dealII: /arch:SSE2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Qunroll2 -Qansi-alias
-Qscalar-rep- /F1000000000 shlw32m.lib
-link /FORCE:MULTIPLE

450.soplex: /arch:SSE2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- /F1000000000 shlw32m.lib
-link /FORCE:MULTIPLE

453.povray: /arch:SSE2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Qunroll4 -Qansi-alias /F1000000000
shlw32m.lib -link /FORCE:MULTIPLE

Fortran benchmarks:

Continued on next page

Peak Optimization Flags (Continued)

410.bwaves: /arch:SSE2 -Qipo -O3 -Qprec-div- -Qopt-prefetch /F1000000000

416.gamess: /arch:SSE2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qunroll2 -Ob0 -Qansi-alias -Qscalar-rep- /F1000000000

434.zeusmp: /arch:SSE2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- /F1000000000

437.leslie3d: /arch:SSE2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qopt-prefetch /F1000000000

459.GemsFDTD: /arch:SSE2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qunroll2 -Ob0 -Qopt-prefetch /F1000000000

465.tonto: /arch:SSE2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qunroll4 -Qauto /F1000000000

Benchmarks using both Fortran and C:

435.gromacs: /arch:SSE2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qopt-prefetch /F1000000000

436.cactusADM: basepeak = yes

454.calculix: /arch:SSE2 -Qipo -O3 -Qprec-div- /F1000000000

481.wrf: basepeak = yes

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic11.0-win32-revA.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic11.0-win32-revA.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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