SPEC® CINT2006 Result
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Intel Corporation

Intel DQ45CB motherboard (Intel Core 2 Duo E7200)

SPECint®_rate2006 =   35.7

SPECint_rate_base2006 = 34.2
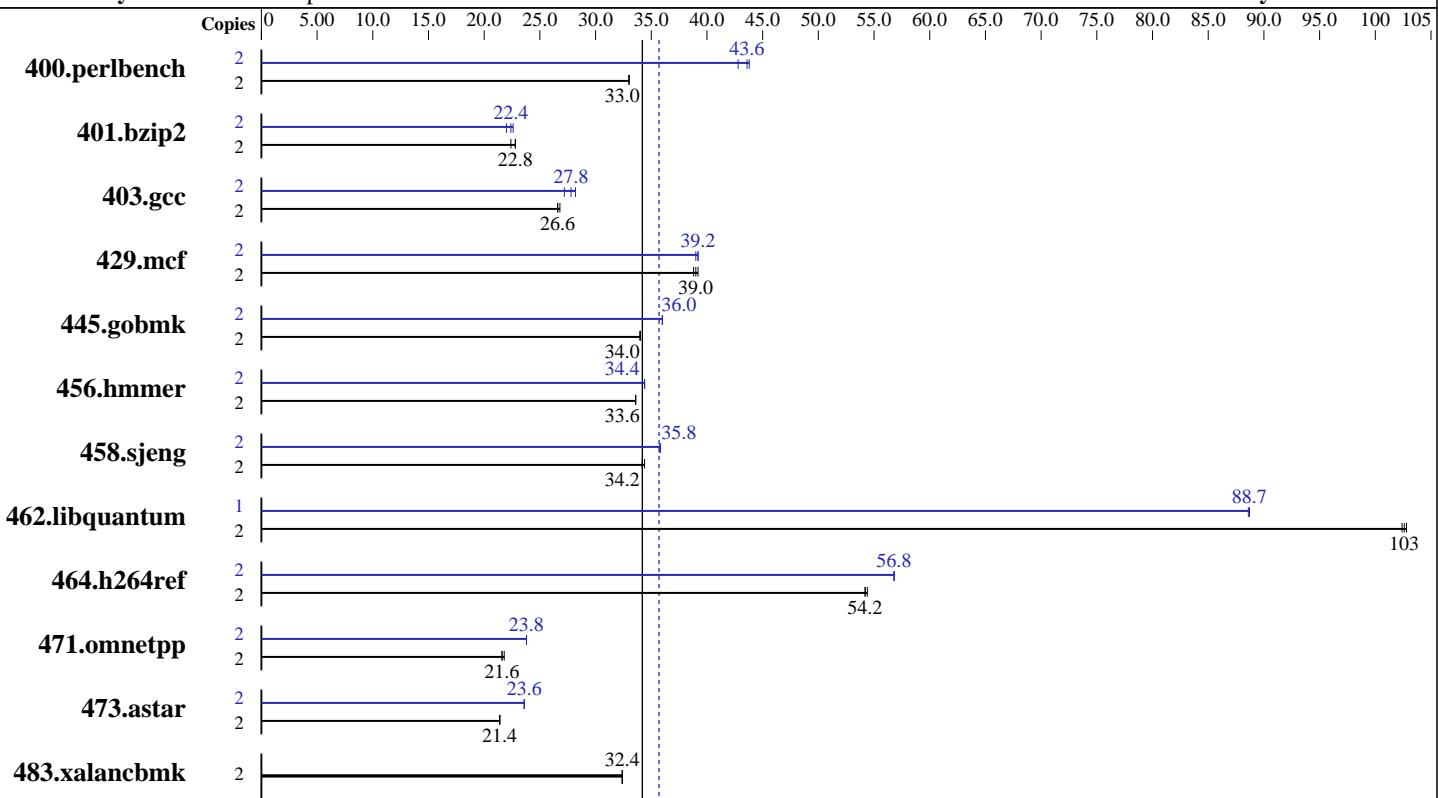
CPU2006 license: 13

Test sponsor: Intel Corporation

Tested by: Intel Corporation

Test date: Apr-2009

Hardware Availability: May-2009

Software Availability: Nov-2008

SPECint_rate_base2006 = 34.2

SPECint_rate2006 = 35.7

Hardware

CPU Name:	Intel Core 2 Duo E7200
CPU Characteristics:	
CPU MHz:	2533
FPU:	Integrated
CPU(s) enabled:	2 cores, 1 chip, 2 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	3 MB I+D on chip per chip
L3 Cache:	None
Other Cache:	None
Memory:	4 GB (4x1GB DDR2-800 CL5)
Disk Subsystem:	Seagate 320 GB SATA, 7200RPM
Other Hardware:	None

Software

Operating System:	Windows Vista Ultimate w/ SP1 (64-bit)
Compiler:	Intel C++ Compiler Professional 11.0 for IA32 Build 20080930 Package ID: w_cproc_p_11.0.054 Microsoft Visual Studio 2008 (for libraries)
Auto Parallel:	Yes
File System:	NTFS
System State:	Default
Base Pointers:	32-bit
Peak Pointers:	32-bit
Other Software:	SmartHeap Library Version 8.1 from http://www.microquill.com/

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	2	**593**	**33.0**	593	33.0	592	33.0	2	456	42.8	**448**	**43.6**	447	43.8
401.bzip2	2	859	22.4	**847**	**22.8**	847	22.8	2	857	22.6	**864**	**22.4**	874	22.0
403.gcc	2	606	26.6	**605**	**26.6**	600	26.8	2	591	27.2	572	28.2	**580**	**27.8**
429.mcf	2	465	39.2	469	38.8	**468**	**39.0**	2	467	39.0	465	39.2	**466**	**39.2**
445.gobmk	2	617	34.0	616	34.0	**616**	**34.0**	2	584	36.0	**584**	**36.0**	584	36.0
456.hmmer	2	555	33.6	**555**	**33.6**	554	33.6	2	543	34.4	**543**	**34.4**	543	34.4
458.sjeng	2	706	34.2	**706**	**34.2**	706	34.4	2	677	35.8	676	35.8	**676**	**35.8**
462.libquantum	2	**404**	**103**	404	102	404	103	1	234	88.6	234	88.7	**234**	**88.7**
464.h264ref	2	816	54.2	815	54.4	**815**	**54.2**	2	780	56.8	779	56.8	**780**	**56.8**
471.omnetpp	2	576	21.6	**576**	**21.6**	575	21.8	2	526	23.8	526	23.8	**526**	**23.8**
473.astar	2	658	21.4	**657**	**21.4**	657	21.4	2	595	23.6	594	23.6	**594**	**23.6**
483.xalancbmk	2	**426**	**32.4**	426	32.4	425	32.4	2	**426**	**32.4**	426	32.4	425	32.4

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.

General Notes

    Tested systems can be used with Shin-G ATX case,
    Antec Truepower Trio power supply TP3-650
    Binaries were built on Windows Vista Ultimate (32-bit)
    Binaries were built on Windows Vista Ultimate (32-bit)
    OMP_NUM_THREADS set to number of logical processors as seen by the OS
    KMP_AFFINITY set to physical,0
    submit disabled for 462.libquantum peak

Base Compiler Invocation

C benchmarks:
    icl -Qvc9 -Qc99

C++ benchmarks:
    icl -Qvc9

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Base Portability Flags

```
        403.gcc: -DSPEC_CPU_WIN32
     464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
   483.xalancbmk: -Qoption,cpp,--no_wchar_t_keyword
```

Base Optimization Flags

C benchmarks:
```
    -QxSSE4.1 -Qipo -O3 -Qprec-div- -Qopt-prefetch /F512000000
```

C++ benchmarks:
```
    -QxSSE4.1 -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qcxx-features
    /F512000000 shlw32m.lib          -link /FORCE:MULTIPLE
```

Base Other Flags

C benchmarks:

```
        403.gcc: -Dalloca=_alloca
```

Peak Compiler Invocation

C benchmarks:
```
    icl -Qvc9 -Qc99
```

C++ benchmarks:
```
    icl -Qvc9
```

Peak Portability Flags

```
        403.gcc: -DSPEC_CPU_WIN32
     464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
   483.xalancbmk: -Qoption,cpp,--no_wchar_t_keyword
```

Peak Optimization Flags

C benchmarks:

```
    400.perlbench: -QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
                   -Qipo -O3 -Qprec-div- -Qansi-alias -Qopt-prefetch
                   /F512000000 shlw32m.lib          -link /FORCE:MULTIPLE
```

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Peak Optimization Flags (Continued)

```
 401.bzip2: -QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
            -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qansi-alias
            /F512000000

   403.gcc: -QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
            -Qipo -O3 -Qprec-div- -Qopt-prefetch /F512000000

   429.mcf: -QxSSE4.1 -Qipo -O3 -Qprec-div- -Qopt-prefetch
            /F512000000

 445.gobmk: -QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
            -Qipo -O2 -Qprec-div- -Qansi-alias /F512000000

 456.hmmer: -QxSSE4.1 -Qipo -O3 -Qprec-div- -Qunroll2 -Qansi-alias
            /F512000000

 458.sjeng: -QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
            -Qipo -O3 -Qprec-div- -Qunroll4 /F512000000

462.libquantum: -QxSSE4.1 -Qipo -O3 -Qprec-div- -Qopt-prefetch
            -Qparallel -Qpar-runtime-control -Qvec-guard-write
            /F512000000

464.h264ref: -QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
            -Qipo -O3 -Qprec-div- -Qunroll2 -Qansi-alias /F512000000
```

C++ benchmarks:

```
471.omnetpp: -QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
            -Qipo -O3 -Qprec-div- -Qansi-alias
            -Qopt-ra-region-strategy=block /F512000000 shlw32m.lib
                      -link /FORCE:MULTIPLE

  473.astar: -QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
            -Qipo -O3 -Qprec-div- -Qansi-alias
            -Qopt-ra-region-strategy=routine /F512000000 shlw32m.lib
                      -link /FORCE:MULTIPLE

483.xalancbmk: basepeak = yes
```

Peak Other Flags

C benchmarks:

```
   403.gcc: -Dalloca=_alloca
```

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The flags file that was used to format this result can be browsed at

`http://www.spec.org/cpu2006/flags/Intel-ic11.0-win32-revA.20090710.html`

You can also download the XML flags source by saving the following link:

`http://www.spec.org/cpu2006/flags/Intel-ic11.0-win32-revA.20090710.xml`

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