



SPEC® CFP2006 Result

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Intel Corporation

SPECfp®_rate2006 = 34.6

Intel DQ45CB motherboard (Intel Core 2 Duo E8600)

SPECfp_rate_base2006 = 32.9

CPU2006 license: 13

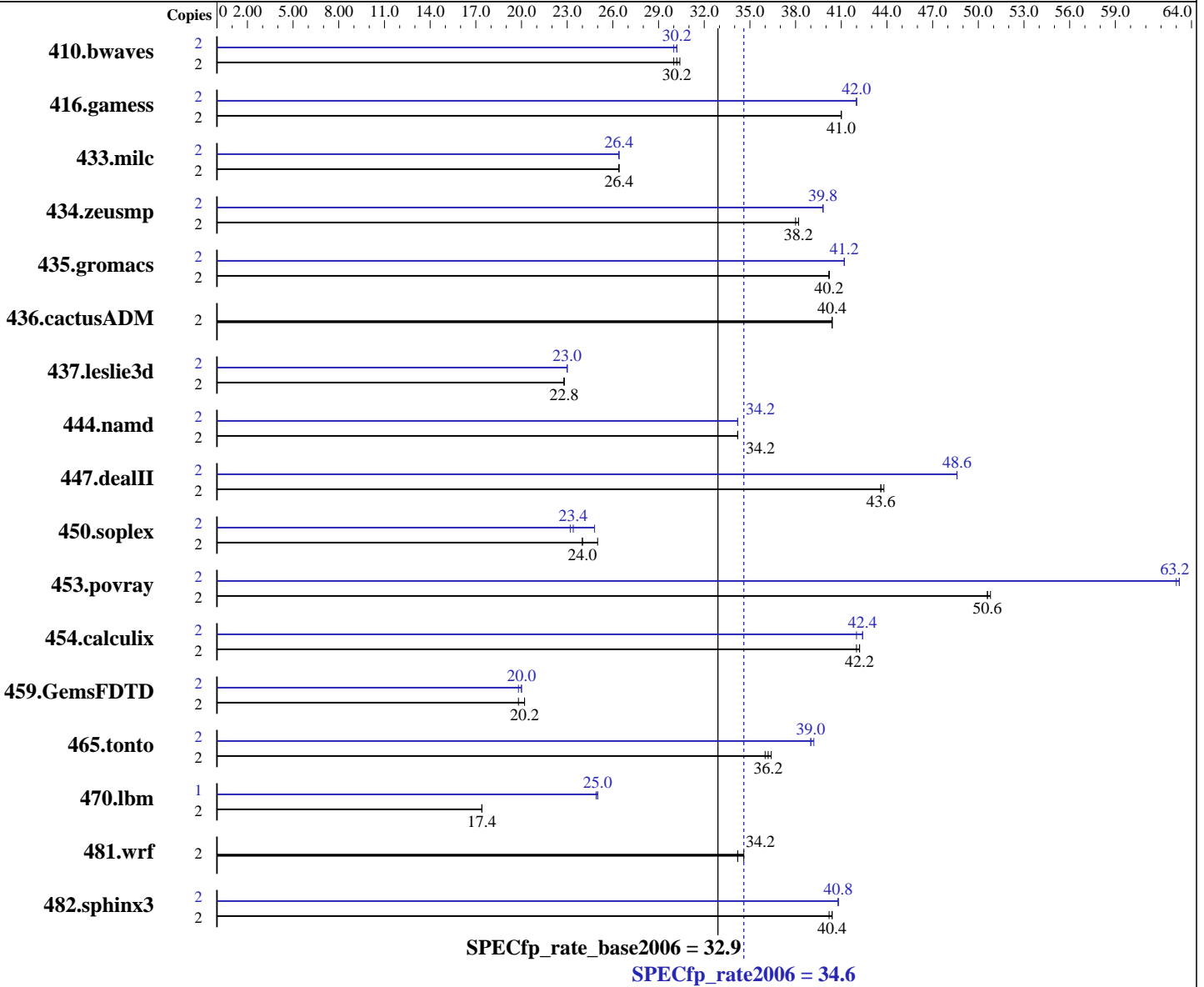
Test sponsor: Intel Corporation

Tested by: Intel Corporation

Test date: Apr-2009

Hardware Availability: May-2009

Software Availability: Nov-2008



Hardware

CPU Name: Intel Core 2 Duo E8600
 CPU Characteristics: 3333
 CPU MHz: 3333
 FPU: Integrated
 CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 6 MB I+D on chip per chip

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Software

Operating System: Windows Vista Ultimate w/ SP1 (64-bit)
 Compiler: Intel C++ Compiler Professional 11.0 for IA32
 Build 20080930 Package ID: w_cproc_p_11.0.054
 Intel Visual Fortran Compiler Professional 11.0 for IA32
 Build 20080930 Package ID: w_cprof_p_11.0.054
 Microsoft Visual Studio 2008 (for libraries)
 Auto Parallel: No
 File System: NTFS

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L3 Cache: None
Other Cache: None
Memory: 4 GB (4x1GB DDR2-800 CL5)
Disk Subsystem: Seagate 320 GB SATA, 7200RPM
Other Hardware: None

System State: Default
Base Pointers: 32-bit
Peak Pointers: 32-bit
Other Software: SmartHeap Library Version 8.1 from <http://www.microquill.com/>

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	2	<u>901</u>	<u>30.2</u>	897	30.4	904	30.0	2	900	30.2	<u>902</u>	<u>30.2</u>	903	30.0
416.gamess	2	<u>954</u>	<u>41.0</u>	954	41.0	953	41.0	2	<u>932</u>	<u>42.0</u>	933	42.0	932	42.0
433.milc	2	697	26.4	<u>698</u>	<u>26.4</u>	698	26.4	2	<u>696</u>	<u>26.4</u>	695	26.4	696	26.4
434.zeusmp	2	477	38.2	<u>477</u>	<u>38.2</u>	478	38.0	2	<u>457</u>	<u>39.8</u>	457	39.8	457	39.8
435.gromacs	2	<u>355</u>	<u>40.2</u>	355	40.2	356	40.2	2	346	41.2	<u>347</u>	<u>41.2</u>	347	41.2
436.cactusADM	2	592	40.4	<u>592</u>	<u>40.4</u>	591	40.4	2	592	40.4	<u>592</u>	<u>40.4</u>	591	40.4
437.leslie3d	2	825	22.8	828	22.8	<u>826</u>	<u>22.8</u>	2	818	23.0	814	23.0	<u>814</u>	<u>23.0</u>
444.namd	2	469	34.2	470	34.2	<u>470</u>	<u>34.2</u>	2	<u>470</u>	<u>34.2</u>	470	34.2	470	34.2
447.dealII	2	523	43.8	525	43.6	<u>524</u>	<u>43.6</u>	2	470	48.6	<u>470</u>	<u>48.6</u>	471	48.6
450.soplex	2	670	25.0	694	24.0	<u>693</u>	<u>24.0</u>	2	670	24.8	717	23.2	<u>714</u>	<u>23.4</u>
453.povray	2	<u>210</u>	<u>50.6</u>	210	50.8	211	50.6	2	169	63.0	168	63.2	<u>168</u>	<u>63.2</u>
454.calculix	2	<u>391</u>	<u>42.2</u>	390	42.2	394	42.0	2	<u>390</u>	<u>42.4</u>	392	42.0	389	42.4
459.GemsFDTD	2	1054	20.2	1068	19.8	<u>1054</u>	<u>20.2</u>	2	1065	20.0	<u>1066</u>	<u>20.0</u>	1069	19.8
465.tonto	2	542	36.4	548	36.0	<u>543</u>	<u>36.2</u>	2	503	39.2	504	39.0	<u>503</u>	<u>39.0</u>
470.lbm	2	1578	17.4	1580	17.4	<u>1579</u>	<u>17.4</u>	1	551	24.9	550	25.0	<u>551</u>	<u>25.0</u>
481.wrf	2	<u>655</u>	<u>34.2</u>	644	34.6	655	34.2	2	<u>655</u>	<u>34.2</u>	644	34.6	655	34.2
482.sphinx3	2	965	40.4	968	40.2	<u>966</u>	<u>40.4</u>	2	954	40.8	956	40.8	<u>956</u>	<u>40.8</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.

General Notes

Tested systems can be used with Shin-G ATX case,
Antec Truepower Trio power supply TP3-650
Binaries were built on Windows Vista Ultimate (32-bit)

Base Compiler Invocation

C benchmarks:
icl -Qvc9 -Qc99

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Base Compiler Invocation (Continued)

C++ benchmarks:

icl -Qvc9

Fortran benchmarks:

ifort

Benchmarks using both Fortran and C:

icl -Qvc9 -Qc99 ifort

Base Portability Flags

436.cactusADM: -Qlowercase /assume:underscore
444.namd: -TP
447.dealII: -DDEAL_II_MEMBER_VAR_SPECIALIZATION_BUG
453.povray: -DSPEC_CPU_WINDOWS_ICL
454.calculix: -DSPEC_CPU_NOZMODIFIER -Qlowercase
481.wrf: -DSPEC_CPU_WINDOWS_ICL

Base Optimization Flags

C benchmarks:

-QxSSE4.1 -Qipo -O3 -Qprec-div- -Qopt-prefetch /F1000000000

C++ benchmarks:

-QxSSE4.1 -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qcxx-features
/F1000000000 shlw32m.lib -link /FORCE:MULTIPLE

Fortran benchmarks:

-QxSSE4.1 -Qipo -O3 -Qprec-div- -Qopt-prefetch /F1000000000

Benchmarks using both Fortran and C:

-QxSSE4.1 -Qipo -O3 -Qprec-div- -Qopt-prefetch /F1000000000

Peak Compiler Invocation

C benchmarks:

icl -Qvc9 -Qc99

C++ benchmarks:

icl -Qvc9

Fortran benchmarks:

ifort

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Peak Compiler Invocation (Continued)

Benchmarks using both Fortran and C:
icl -Qvc9 -Qc99 ifort

Peak Portability Flags

436.cactusADM: -Qlowercase /assume:underscore
444.namd: -TP
447.dealII: -DDEAL_II_MEMBER_VAR_SPECIALIZATION_BUG
453.povray: -DSPEC_CPU_WINDOWS_ICL
454.calculix: -DSPEC_CPU_NOZMODIFIER -Qlowercase
481.wrf: -DSPEC_CPU_WINDOWS_ICL

Peak Optimization Flags

C benchmarks:

433.milc: -QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Oa /F1000000000

470.lbm: -QxSSE4.1 -Qipo -O3 -Qprec-div- -Qopt-prefetch
/F1000000000

482.sphinx3: -QxSSE4.1 -Qipo -O3 -Qprec-div- -Qunroll2 /F1000000000

C++ benchmarks:

444.namd: -QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Oa /F1000000000 shlw32m.lib
-link /FORCE:MULTIPLE

447.dealII: -QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Qunroll2 -Qansi-alias
-Qscalar-rep- /F1000000000 shlw32m.lib
-link /FORCE:MULTIPLE

450.soplex: -QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- /F1000000000 shlw32m.lib
-link /FORCE:MULTIPLE

453.povray: -QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Qunroll4 -Qansi-alias /F1000000000
shlw32m.lib -link /FORCE:MULTIPLE

Fortran benchmarks:

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Peak Optimization Flags (Continued)

410.bwaves: -QxSSE4.1 -Qipo -O3 -Qprec-div- -Qopt-prefetch /F1000000000

416.gamess: -QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qunroll2 -Ob0 -Qansi-alias -Qscalar-rep- /F1000000000

434.zeusmp: -QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- /F1000000000

437.leslie3d: -QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qopt-prefetch /F1000000000

459.GemsFDTD: -QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qunroll2 -Ob0 -Qopt-prefetch /F1000000000

465.tonto: -QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qunroll4 -Qauto /F1000000000

Benchmarks using both Fortran and C:

435.gromacs: -QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qopt-prefetch /F1000000000

436.cactusADM: basepeak = yes

454.calculix: -QxSSE4.1 -Qipo -O3 -Qprec-div- /F1000000000

481.wrf: basepeak = yes

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-win32-revA.20090710.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-win32-revA.20090710.xml>

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For other inquiries, please contact webmaster@spec.org.

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