



# SPEC® CINT2006 Result

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## Intel Corporation

## SPECint®\_rate2006 = Not Run

### Acer Aspire 3810T (Intel Core 2 Solo ULV SU3500)

## SPECint\_rate\_base2006 = 11.7

CPU2006 license: 13

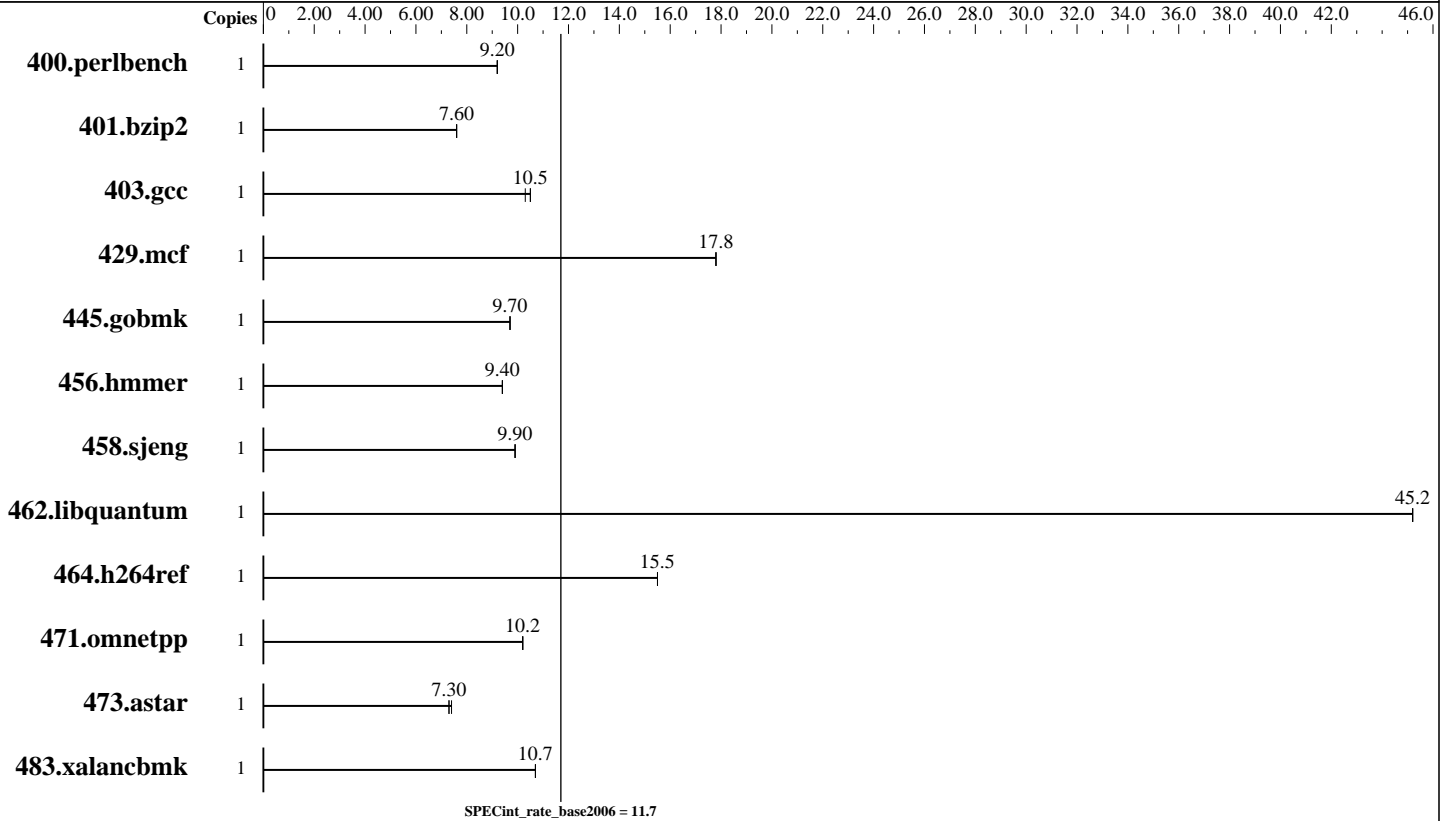
Test sponsor: Intel Corporation

Tested by: Intel Corporation

Test date: May-2009

Hardware Availability: May-2009

Software Availability: Nov-2008



### Hardware

CPU Name: Intel Core 2 Solo ULV SU3500  
 CPU Characteristics:  
 CPU MHz: 1400  
 FPU: Integrated  
 CPU(s) enabled: 1 core, 1 chip, 1 core/chip  
 CPU(s) orderable: 1 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per chip  
 Secondary Cache: 3 MB I+D on chip per chip  
 L3 Cache: None  
 Other Cache: None  
 Memory: 2 GB (2x1GB DDR3-1066 CL7; bios sets it to DDR2-800 CL5)  
 Disk Subsystem: ST9250315AS 250 GB SATA, 5400RPM  
 Other Hardware: None

### Software

Operating System: Windows Vista Home Premium w/ SP1 (32-bit)  
 Compiler: Intel C++ Compiler Professional 11.0 for IA32  
 Build 20080930 Package ID: w\_cproc\_p\_11.0.054  
 Microsoft Visual Studio 2008 (for libraries)  
 Auto Parallel: No  
 File System: NTFS  
 System State: Default  
 Base Pointers: 32-bit  
 Peak Pointers: Not Applicable  
 Other Software: SmartHeap Library Version 8.1 from <http://www.microquill.com/>



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	1	1058	9.20	1060	9.20	<b><u>1059</u></b>	<b><u>9.20</u></b>							
401.bzip2	1	1264	7.60	1262	7.60	<b><u>1263</u></b>	<b><u>7.60</u></b>							
403.gcc	1	784	10.3	<b><u>767</u></b>	<b><u>10.5</u></b>	765	10.5							
429.mcf	1	513	17.8	<b><u>513</u></b>	<b><u>17.8</u></b>	513	17.8							
445.gobmk	1	<b><u>1087</u></b>	<b><u>9.70</u></b>	1087	9.70	1086	9.70							
456.hammer	1	997	9.40	996	9.40	<b><u>996</u></b>	<b><u>9.40</u></b>							
458.sjeng	1	1222	9.90	1223	9.90	<b><u>1222</u></b>	<b><u>9.90</u></b>							
462.libquantum	1	458	45.2	<b><u>458</u></b>	<b><u>45.2</u></b>	458	45.2							
464.h264ref	1	<b><u>1426</u></b>	<b><u>15.5</u></b>	1426	15.5	1427	15.5							
471.omnetpp	1	616	10.2	615	10.2	<b><u>616</u></b>	<b><u>10.2</u></b>							
473.astar	1	955	7.40	<b><u>955</u></b>	<b><u>7.30</u></b>	955	7.30							
483.xalancbmk	1	<b><u>644</u></b>	<b><u>10.7</u></b>	644	10.7	644	10.7							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.

## General Notes

The system bus runs at 800 MHz  
Binaries were built on Windows Vista Ultimate (32-bit)

## Base Compiler Invocation

C benchmarks:  
icl -Qvc9 -Qc99

C++ benchmarks:  
icl -Qvc9

## Base Portability Flags

403.gcc: -DSPEC\_CPU\_WIN32  
464.h264ref: -DSPEC\_CPU\_NO\_INTTYPES -DWIN32  
483.xalancbmk: -Qoption,cpp,--no\_wchar\_t\_keyword



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## Base Optimization Flags

C benchmarks:

`-QxSSE4.1 -Qipo -O3 -Qprec-div- -Qopt-prefetch /F512000000`

C++ benchmarks:

`-QxSSE4.1 -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qcxx-features  
/F512000000 shlw32m.lib -link /FORCE:MULTIPLE`

## Base Other Flags

C benchmarks:

`403.gcc: -Dalloca=_alloca`

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-win32-revA.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-win32-revA.xml>

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For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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