SPEC® CINT2006 Result

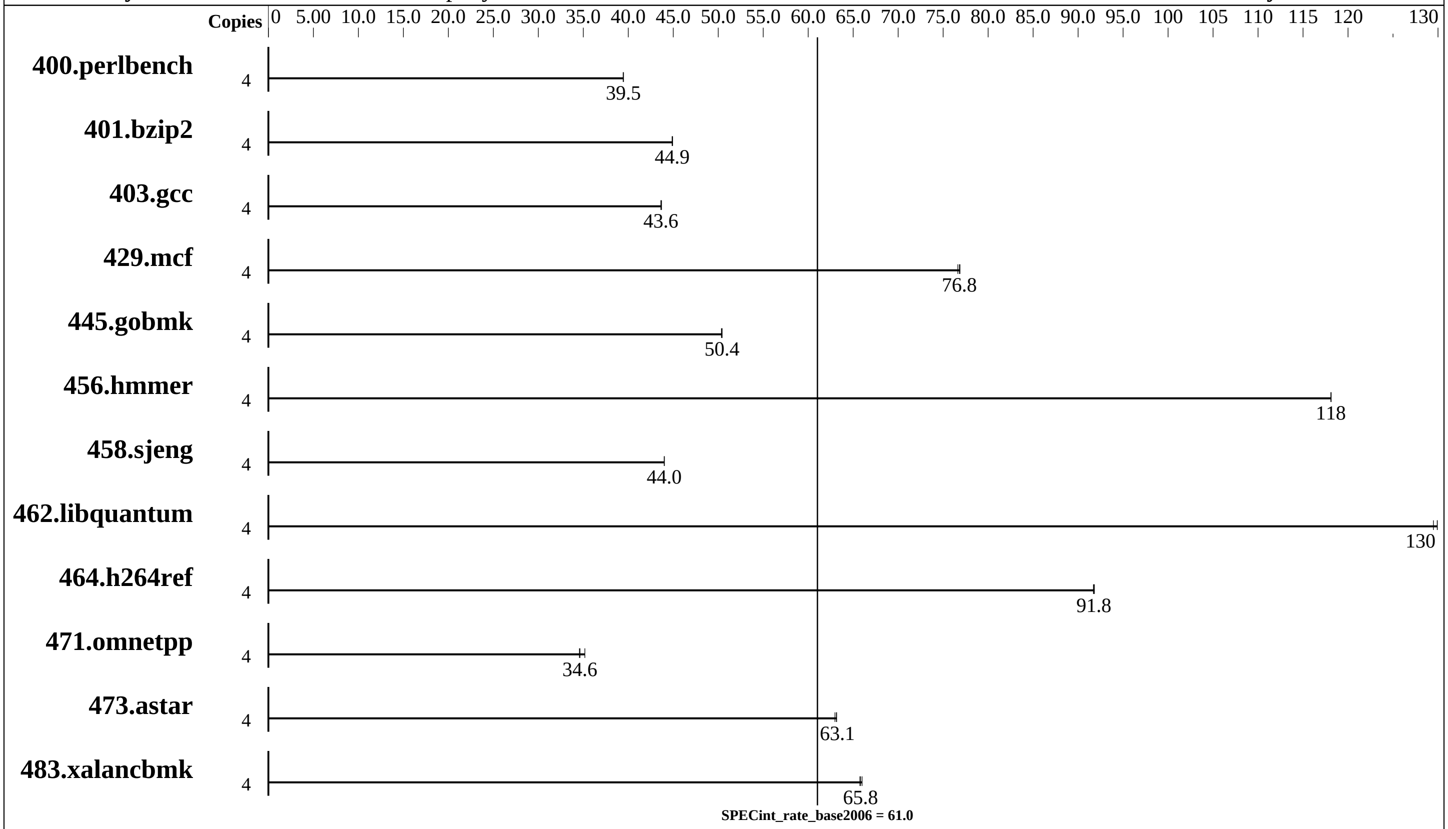
Hewlett-Packard Company

HP Integrity BL860c (1.66GHz/18MB Dual-Core Intel Itanium 2)

SPECint®_rate2006 = Not Run

SPECint_rate_base2006 = 61.0

CPU2006 license:	03	**Test date:**	May-2009
Test sponsor:	Hewlett-Packard Company	**Hardware Availability:**	Nov-2007
Tested by:	Hewlett-Packard Company	**Software Availability:**	Mar-2009



Hardware

CPU Name:	Dual-Core Intel Itanium 9140M
CPU Characteristics:	1.66GHz/18MB, 667MHz FSB
CPU MHz:	1666
FPU:	Integrated
CPU(s) enabled:	4 cores, 2 chips, 2 cores/chip, 2 threads/core
CPU(s) orderable:	1-2 chips
Primary Cache:	16 KB I + 16 KB D on chip per core
Secondary Cache:	1 MB I + 256 KB D on chip per core
L3 Cache:	9 MB I+D on chip per core
Other Cache:	None
Memory:	48 GB (12x4GB PC-4200 DIMMs)
Disk Subsystem:	2x73 GB 15K RPM SAS
Other Hardware:	None

Software

Operating System:	HPUX11i-DC-OE B.11.31.0903
Compiler:	HP C/aC++ Developer's Bundle C.11.31.04.2
Auto Parallel:	No
File System:	vxfs
System State:	Multi-user
Base Pointers:	32-bit
Peak Pointers:	32-bit
Other Software:	MallocNextGen B.11.31.0903.02

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Results Table

	Base							Peak						
Benchmark	**Copies**	**Seconds**	**Ratio**	**Seconds**	**Ratio**	**Seconds**	**Ratio**	**Copies**	**Seconds**	**Ratio**	**Seconds**	**Ratio**	**Seconds**	**Ratio**
400.perlbench	4	**990**	**39.5**	991	39.4	990	39.5							
401.bzip2	4	860	44.9	859	44.9	**860**	**44.9**							
403.gcc	4	737	43.7	**738**	**43.6**	738	43.6							
429.mcf	4	475	76.9	**475**	**76.8**	476	76.6							
445.gobmk	4	832	50.4	**832**	**50.4**	833	50.4							
456.hmmer	4	316	118	316	118	**316**	**118**							
458.sjeng	4	**1100**	**44.0**	1100	44.0	1100	44.0							
462.libquantum	4	**638**	**130**	638	130	640	129							
464.h264ref	4	964	91.8	**965**	**91.8**	965	91.7							
471.omnetpp	4	723	34.6	711	35.2	**722**	**34.6**							
473.astar	4	445	63.2	446	63.0	**445**	**63.1**							
483.xalancbmk	4	418	66.0	420	65.7	**419**	**65.8**							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The config file option 'submit' was used.
```

Operating System Notes

```
The system had the March 2009 HP-UX 11i v3 Data Center
Operating Environment and March 2009 compilers installed.

The following kernel tunables were set, in addition to the
defaults set by the Base Operating Environment:

filecache_max=25%
filecache_min=25%
maxdsiz=3221225472
fcache_fb_policy=1
base_pagesize=64
pagezero_daemon_enabled=0
vxfs_ifree_timelag=-1
```

Platform Notes

```
Hardware Threading was disabled
The following config file entry was used to bind
processes to cores using the HP-UX "mpsched" utility:
submit = let "MYCPU=\$SPECCOPYNUM*2" ;mpsched -c \$MYCPU $command
```

Base Compiler Invocation

C benchmarks:
`/opt/ansic/bin/cc -AC99`

C++ benchmarks:
`/opt/aCC/bin/aCC -Aa`

Base Portability Flags

400.perlbench: `-DSPEC_CPU_HPUX_IA64`
403.gcc: `-DSPEC_CPU_HPUX`
462.libquantum: `-DSPEC_CPU_HPUX`
483.xalancbmk: `-DSPEC_CPU_HPUX_IA64`

Base Optimization Flags

C benchmarks:
`+Ofaster +Otype_safety=ansi -Wl,-aarchive_shared -Wl,+pd,64M -Wl,+pi,64K -Wl,-N`

C++ benchmarks:
`+Ofaster +Otype_safety=ansi -Wl,-aarchive_shared -Wl,+pd,64M -Wl,+pi,64K -Wl,-N -lmallocng`

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Itanium-HPUX-flags.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Itanium-HPUX-flags.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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