SPEC® CFP2006 Result
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Hewlett-Packard Company

ProLiant DL380 G6
(2.67 GHz, Intel Xeon X5550)

$SPECfp^{®}\_rate2006 =$ **188**

$SPECfp\_rate\_base2006 =$ **182**

Benchmark	Copies	Result
410.bwaves	8	187
	16	184
416.gamess	16	187
	16	181
433.milc	16	177
434.zeusmp	16	206
	16	196
435.gromacs	16	186
	16	181
436.cactusADM	16	204
	16	216
437.leslie3d	16	134
444.namd	16	170
	16	168
447.dealII	16	286
	16	268
450.soplex	8	148
	16	144
453.povray	16	296
	16	245
454.calculix	16	216
459.GemsFDTD	16	123
	16	120
465.tonto	16	209
	16	196
470.lbm	8	131
	16	124
481.wrf	16	221
482.sphinx3	16	208
	16	200

SPECfp_rate_base2006 = 182

SPECfp_rate2006 = 188

Hardware

CPU Name: Intel Xeon X5550
CPU Characteristics: Intel Turbo Boost Technology up to 3.07 GHz
CPU MHz: 2667
FPU: Integrated
CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip, 2 threads/core
CPU(s) orderable: 1,2 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core

Continued on next page

Software

Operating System: Red Hat Enterprise Linux Server release 5.3
Kernel 2.6.18-128.el5
Compiler: Intel C++ Compiler 11.0 for Linux
Build 20090131 Package ID: l_cproc_p_11.0.080
l_cprof_p_11.0.080
Auto Parallel: No
File System: ext3
System State: Run level 3 (multi-user)
Base Pointers: 32-bit

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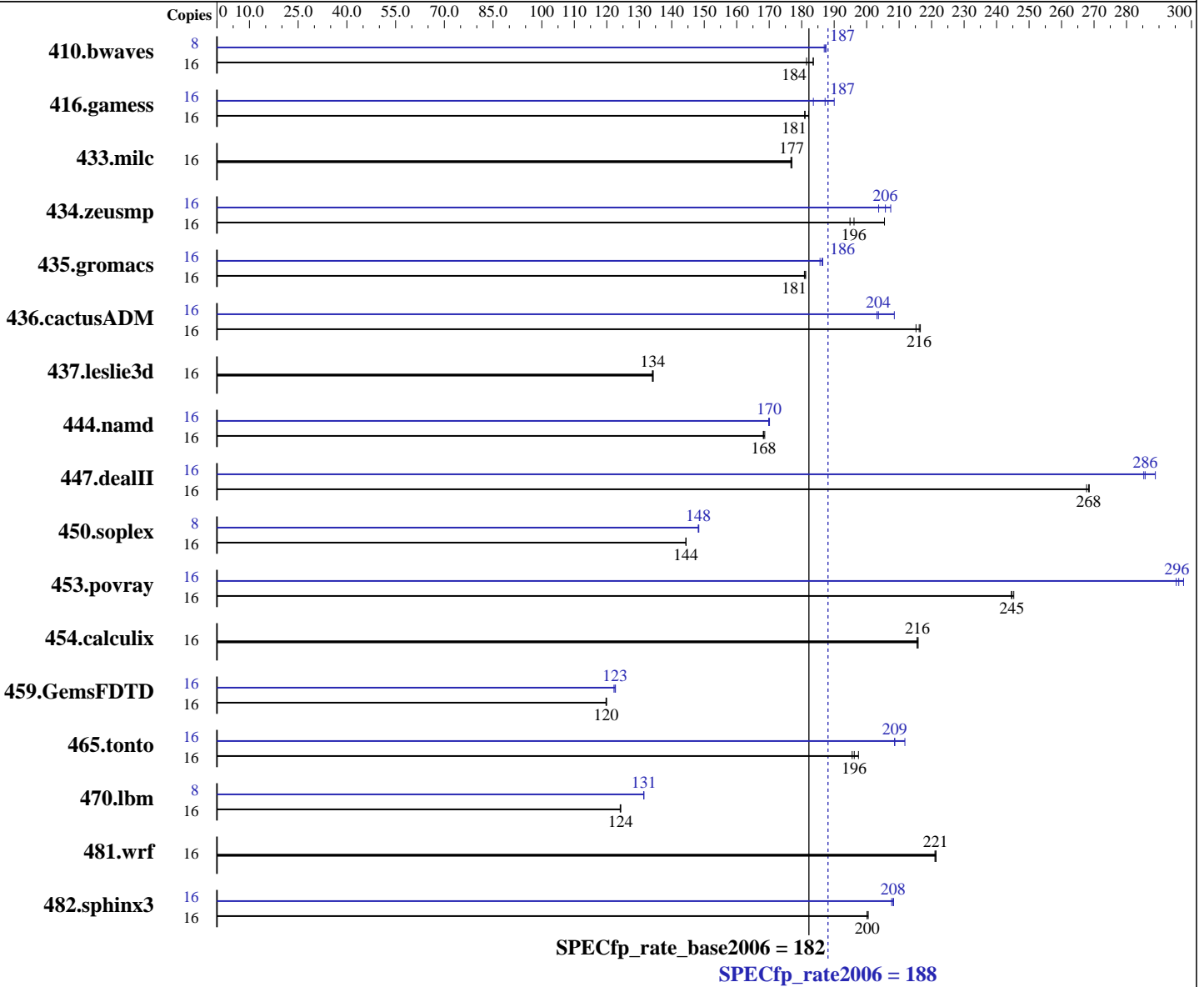|	
CPU2006 license: 3	**Test date:** May-2009
Test sponsor: Hewlett-Packard Company	**Hardware Availability:** Mar-2009
Tested by: Hewlett-Packard Company	**Software Availability:** Feb-2009

L3 Cache:	8 MB I+D on chip per chip	Peak Pointers:	32/64-bit
Other Cache:	None	Other Software:	Microquill SmartHeap V8.1
Memory:	48 GB (6x8 GB PC3-10600R CL9)		Binutils 2.18.50.0.7.20080502
Disk Subsystem:	1x146 GB 10K RPM SAS		
Other Hardware:	None		

Results Table

Benchmark	Base						Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	16	1198	182	**1185**	**184**	1184	184	8	582	187	580	188	**580**	**187**
416.gamess	16	1720	182	**1730**	**181**	1732	181	16	**1673**	**187**	1706	184	1649	190
433.milc	16	831	177	**830**	**177**	830	177	16	831	177	**830**	**177**	830	177
434.zeusmp	16	747	195	**742**	**196**	709	205	16	715	204	**708**	**206**	702	207
435.gromacs	16	**631**	**181**	632	181	630	181	16	613	186	615	186	**613**	**186**
436.cactusADM	16	883	217	**884**	**216**	889	215	16	**939**	**204**	941	203	917	209
437.leslie3d	16	**1121**	**134**	1120	134	1122	134	16	**1121**	**134**	1120	134	1122	134
444.namd	16	**762**	**168**	761	169	763	168	16	**755**	**170**	756	170	754	170
447.dealII	16	**682**	**268**	684	268	682	269	16	**641**	**286**	634	289	642	285
450.soplex	16	924	144	924	144	**924**	**144**	8	450	148	**450**	**148**	450	148
453.povray	16	348	245	**348**	**245**	347	245	16	**287**	**296**	286	298	288	295
454.calculix	16	612	216	612	216	**612**	**216**	16	612	216	612	216	**612**	**216**
459.GemsFDTD	16	1415	120	**1417**	**120**	1417	120	16	1389	122	1384	123	**1385**	**123**
465.tonto	16	**802**	**196**	797	197	805	196	16	**754**	**209**	743	212	755	209
470.lbm	16	**1769**	**124**	1769	124	1771	124	8	**837**	**131**	837	131	836	131
481.wrf	16	809	221	**808**	**221**	807	221	16	809	221	**808**	**221**	807	221
482.sphinx3	16	**1558**	**200**	1558	200	1555	200	16	**1498**	**208**	1501	208	1497	208

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

    The config file option 'submit' was used.
     numactl was used to bind copies to the cores

Platform Notes

    BIOS configuration:
       Power Regulator set to Static High Performance Mode
       Power Profile set to Maximum Performance
       Thermal Configuration set to Increased Cooling

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Base Compiler Invocation

C benchmarks:
        icc

C++ benchmarks:
        icpc

Fortran benchmarks:
        ifort

Benchmarks using both Fortran and C:
        icc ifort

Base Portability Flags

        410.bwaves: -DSPEC_CPU_LP64
        416.gamess: -DSPEC_CPU_LP64
          433.milc: -DSPEC_CPU_LP64
        434.zeusmp: -DSPEC_CPU_LP64
       435.gromacs: -DSPEC_CPU_LP64 -nofor_main
      436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
       437.leslie3d: -DSPEC_CPU_LP64
          444.namd: -DSPEC_CPU_LP64
         447.dealII: -DSPEC_CPU_LP64
        450.soplex: -DSPEC_CPU_LP64
        453.povray: -DSPEC_CPU_LP64
       454.calculix: -DSPEC_CPU_LP64 -nofor_main
      459.GemsFDTD: -DSPEC_CPU_LP64
         465.tonto: -DSPEC_CPU_LP64
          470.lbm: -DSPEC_CPU_LP64
           481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
       482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
        -xSSE4.2 -ipo -O3 -no-prec-div -static

C++ benchmarks:
        -xSSE4.2 -ipo -O3 -no-prec-div -static

Fortran benchmarks:
        -xSSE4.2 -ipo -O3 -no-prec-div -static

Benchmarks using both Fortran and C:
        -xSSE4.2 -ipo -O3 -no-prec-div -static

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Peak Compiler Invocation

C benchmarks (except as noted below):
      `icc`

      482.sphinx3: `icc -m32`

C++ benchmarks (except as noted below):
      `icpc`

       450.soplex: `icpc -m32`

Fortran benchmarks:
      `ifort`

Benchmarks using both Fortran and C:
      `icc ifort`

Peak Portability Flags

          410.bwaves: `-DSPEC_CPU_LP64`
          416.gamess: `-DSPEC_CPU_LP64`
            433.milc: `-DSPEC_CPU_LP64`
          434.zeusmp: `-DSPEC_CPU_LP64`
         435.gromacs: `-DSPEC_CPU_LP64 -nofor_main`
       436.cactusADM: `-DSPEC_CPU_LP64 -nofor_main`
         437.leslie3d: `-DSPEC_CPU_LP64`
            444.namd: `-DSPEC_CPU_LP64`
           447.dealII: `-DSPEC_CPU_LP64`
          453.povray: `-DSPEC_CPU_LP64`
         454.calculix: `-DSPEC_CPU_LP64 -nofor_main`
        459.GemsFDTD: `-DSPEC_CPU_LP64`
            465.tonto: `-DSPEC_CPU_LP64`
             470.lbm: `-DSPEC_CPU_LP64`
             481.wrf: `-DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

        433.milc: `basepeak = yes`

        470.lbm: `-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch`
                `-auto-ilp32`

      482.sphinx3: `-xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2`

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Peak Optimization Flags (Continued)

C++ benchmarks:

 444.namd: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
 -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
 -fno-alias -auto-ilp32

 447.dealII: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
 -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
 -unroll2 -ansi-alias -scalar-rep-

 450.soplex: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
 -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
 -opt-malloc-options=3

 453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
 -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
 -unroll4 -ansi-alias

Fortran benchmarks:

 410.bwaves: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

 416.gamess: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
 -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
 -unroll2 -Ob0 -ansi-alias -scalar-rep-

 434.zeusmp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
 -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)

 437.leslie3d: basepeak = yes

 459.GemsFDTD: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
 -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
 -unroll2 -Ob0 -opt-prefetch

 465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
 -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
 -unroll4 -auto

Benchmarks using both Fortran and C:

 435.gromacs: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
 -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
 -opt-prefetch -auto-ilp32

 436.cactusADM: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
 -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
 -unroll2 -opt-prefetch -auto-ilp32

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Peak Optimization Flags (Continued)

454.calculix: basepeak = yes

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/HP-Intel-Linux-Settings-flags.20090710.html
http://www.spec.org/cpu2006/flags/Intel-ic11.0-fp-linux64-revA.20090710.13.html

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You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/HP-Intel-Linux-Settings-flags.20090710.xml
http://www.spec.org/cpu2006/flags/Intel-ic11.0-fp-linux64-revA.20090710.13.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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