SPEC® CINT2006 Result
Copyright 2006-2014 Standard Performance Evaluation Corporation

Fujitsu

CELSIUS M470, Intel Xeon W3520

$SPECint^®\_rate2006 =$ 101

$SPECint\_rate\_base2006 =$ 94.1

Copies — 400.perlbench: 8 (96.1), 8 (83.0); 401.bzip2: 8 (64.7), 8 (61.0); 403.gcc: 8 (85.2), 8 (84.3); 429.mcf: 4 (118), 8 (117); 445.gobmk: 8 (101), 8 (91.6); 456.hmmer: 4 (93.4), 8 (66.8); 458.sjeng: 8 (95.4), 8 (86.5); 462.libquantum: 8 (318), 8 (318); 464.h264ref: 8 (123), 8 (119); 471.omnetpp: 8 (75.4); 473.astar: 8 (66.5), 8 (59.9); 483.xalancbmk: 8 (106)

SPECint_rate_base2006 = 94.1

SPECint_rate2006 = 101

Hardware

CPU Name:	Intel Xeon W3520
CPU Characteristics:	Intel Turbo Boost Technology up to 2.93 GHz
CPU MHz:	2667
FPU:	Integrated
CPU(s) enabled:	4 cores, 1 chip, 4 cores/chip, 2 threads/core
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	8 MB I+D on chip per chip
Other Cache:	None
Memory:	12 GB (6x2 GB PC3 10600E, 2 rank, ECC, running at 1066 MHz)
Disk Subsystem:	1 x SATA II, 400 GB, 7200 rpm
Other Hardware:	None

Software

Operating System:	SuSe Linux Enterprise Server 10 (x86_64) SP2, kernel 2.6.16.60-0.21-smp
Compiler:	Intel C++ Compiler 11.0 for Linux Build 20090131 Package ID: l_cproc_p_11.0.080
Auto Parallel:	No
File System:	ext3
System State:	Multi-User Run Level 3
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502

SPEC CINT2006 Result
Copyright 2006-2014 Standard Performance Evaluation Corporation

Fujitsu

CELSIUS M470, Intel Xeon W3520

SPECint_rate2006 = 101
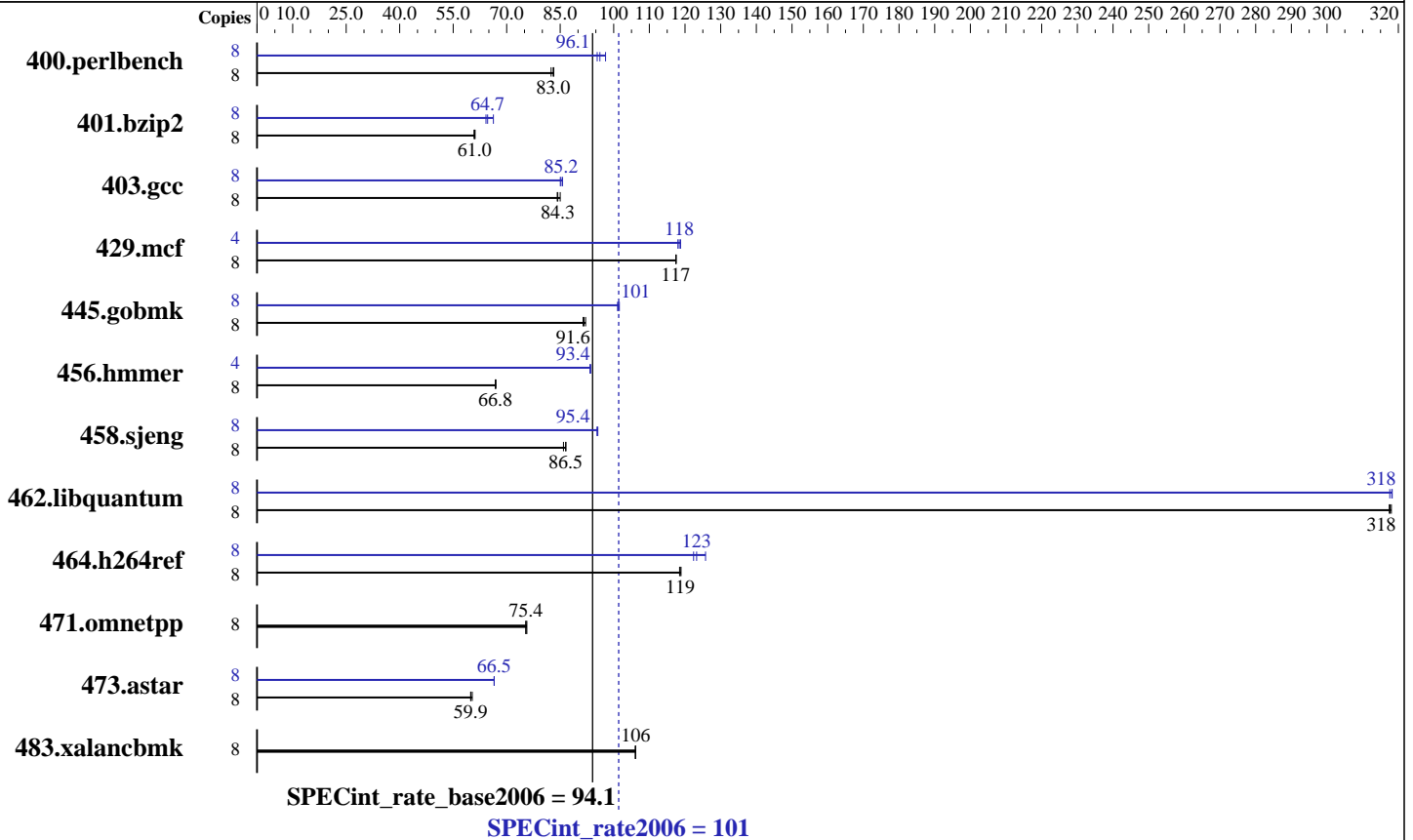
SPECint_rate_base2006 = 94.1

CPU2006 license: 19
Test sponsor: Fujitsu
Tested by: Fujitsu

Test date: Apr-2009
Hardware Availability: Apr-2009
Software Availability: Feb-2009

Results Table

Benchmark	Copies	Base Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Peak Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	939	83.2	948	82.5	**941**	**83.0**	8	**813**	**96.1**	800	97.7	819	95.4
401.bzip2	8	**1265**	**61.0**	1269	60.8	1263	61.1	8	1203	64.2	1165	66.3	**1194**	**64.7**
403.gcc	8	765	84.2	758	85.0	**764**	**84.3**	8	757	85.0	**756**	**85.2**	752	85.7
429.mcf	8	621	117	621	118	**621**	**117**	4	307	119	309	118	**308**	**118**
445.gobmk	8	**916**	**91.6**	918	91.4	911	92.1	8	826	102	**828**	**101**	830	101
456.hmmer	8	**1117**	**66.8**	1117	66.8	1114	67.0	4	400	93.4	399	93.5	**399**	**93.4**
458.sjeng	8	1118	86.6	1127	85.9	**1119**	**86.5**	8	1015	95.4	**1015**	**95.4**	1013	95.6
462.libquantum	8	**522**	**318**	522	317	521	318	8	522	318	**521**	**318**	521	318
464.h264ref	8	1494	118	**1492**	**119**	1489	119	8	1408	126	1446	122	**1436**	**123**
471.omnetpp	8	664	75.3	**663**	**75.4**	661	75.7	8	664	75.3	**663**	**75.4**	661	75.7
473.astar	8	**937**	**59.9**	937	59.9	931	60.4	8	845	66.5	**844**	**66.5**	844	66.5
483.xalancbmk	8	**520**	**106**	520	106	520	106	8	**520**	**106**	520	106	520	106

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

Platform Notes

    BIOS configuration:
    Memory speed set to "Max Performance" (Switch in "Advanced Memory Options")

General Notes

    For information about Fujitsu please visit:
    http://www.fujitsu.com

Base Compiler Invocation

C benchmarks:
     icc

C++ benchmarks:
     icpc

SPEC CINT2006 Result
Copyright 2006-2014 Standard Performance Evaluation Corporation

Fujitsu

CELSIUS M470, Intel Xeon W3520

SPECint_rate2006 = 101

SPECint_rate_base2006 = 94.1

CPU2006 license: 19
Test sponsor: Fujitsu
Tested by: Fujitsu

Test date: Apr-2009
Hardware Availability: Apr-2009
Software Availability: Feb-2009

Base Portability Flags

```
    400.perlbench: -DSPEC_CPU_LINUX_IA32
    462.libquantum: -DSPEC_CPU_LINUX
    483.xalancbmk: -DSPEC_CPU_LINUX
```

Base Optimization Flags

C benchmarks:
```
    -xSSE4.2 -ipo -O3 -no-prec-div -static -inline-calloc
    -opt-malloc-options=3 -opt-prefetch
```

C++ benchmarks:
```
    -xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
    -L/spec/cpu2006.1.1/lib -lsmartheap
```

Base Other Flags

C benchmarks:

```
        403.gcc: -Dalloca=_alloca
```

Peak Compiler Invocation

C benchmarks (except as noted below):
```
    icc
```

```
    401.bzip2: /opt/intel/Compiler/11.0/080/bin/intel64/icc
```

```
    456.hmmer: /opt/intel/Compiler/11.0/080/bin/intel64/icc
```

```
    458.sjeng: /opt/intel/Compiler/11.0/080/bin/intel64/icc
```

C++ benchmarks (except as noted below):
```
    icpc
```

```
    473.astar: /opt/intel/Compiler/11.0/080/bin/intel64/icpc
```

Peak Portability Flags

```
    400.perlbench: -DSPEC_CPU_LINUX_IA32
    401.bzip2: -DSPEC_CPU_LP64
    456.hmmer: -DSPEC_CPU_LP64
```

Fujitsu

CELSIUS M470, Intel Xeon W3520

SPECint_rate2006 = 101

SPECint_rate_base2006 = 94.1

CPU2006 license:	19	**Test date:**	Apr-2009
Test sponsor:	Fujitsu	**Hardware Availability:**	Apr-2009
Tested by:	Fujitsu	**Software Availability:**	Feb-2009

Peak Portability Flags (Continued)

```
     458.sjeng: -DSPEC_CPU_LP64
 462.libquantum: -DSPEC_CPU_LINUX
      473.astar: -DSPEC_CPU_LP64
  483.xalancbmk: -DSPEC_CPU_LINUX
```

Peak Optimization Flags

C benchmarks:

```
   400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                  -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
                  -prof-use(pass 2) -ansi-alias -opt-prefetch

      401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                  -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
                  -prof-use(pass 2) -opt-prefetch -ansi-alias -auto-ilp32

        403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -static -inline-calloc
                  -opt-malloc-options=3

        429.mcf: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                  -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
                  -prof-use(pass 2) -opt-prefetch

      445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2
                  -ipo -no-prec-div -ansi-alias

      456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2
                  -ansi-alias -auto-ilp32

      458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                  -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
                  -prof-use(pass 2) -unroll4 -auto-ilp32

  462.libquantum: -xSSE4.2 -ipo -O3 -no-prec-div -static
                  -opt-malloc-options=3 -opt-prefetch

    464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                  -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
                  -prof-use(pass 2) -unroll2 -ansi-alias
```

C++ benchmarks:

```
    471.omnetpp: basepeak = yes

      473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                  -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
                  -ansi-alias -opt-ra-region-strategy=routine -auto-ilp32
                  -Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap64
```

Fujitsu

CELSIUS M470, Intel Xeon W3520

SPECint_rate2006 =   101

SPECint_rate_base2006 = 94.1

CPU2006 license: 19		**Test date:**	Apr-2009
Test sponsor: Fujitsu		**Hardware Availability:**	Apr-2009
Tested by: Fujitsu		**Software Availability:**	Feb-2009

Peak Optimization Flags (Continued)

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090710.05.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090710.05.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.
Report generated on Wed Jul 23 00:22:02 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 26 May 2009.