



SPEC® CFP2006 Result

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Acer Incorporated

SPECfp®_rate2006 = 103

Acer Altos G540 M2 (Intel Xeon X5570)

SPECfp_rate_base2006 = 99.5

CPU2006 license: 97

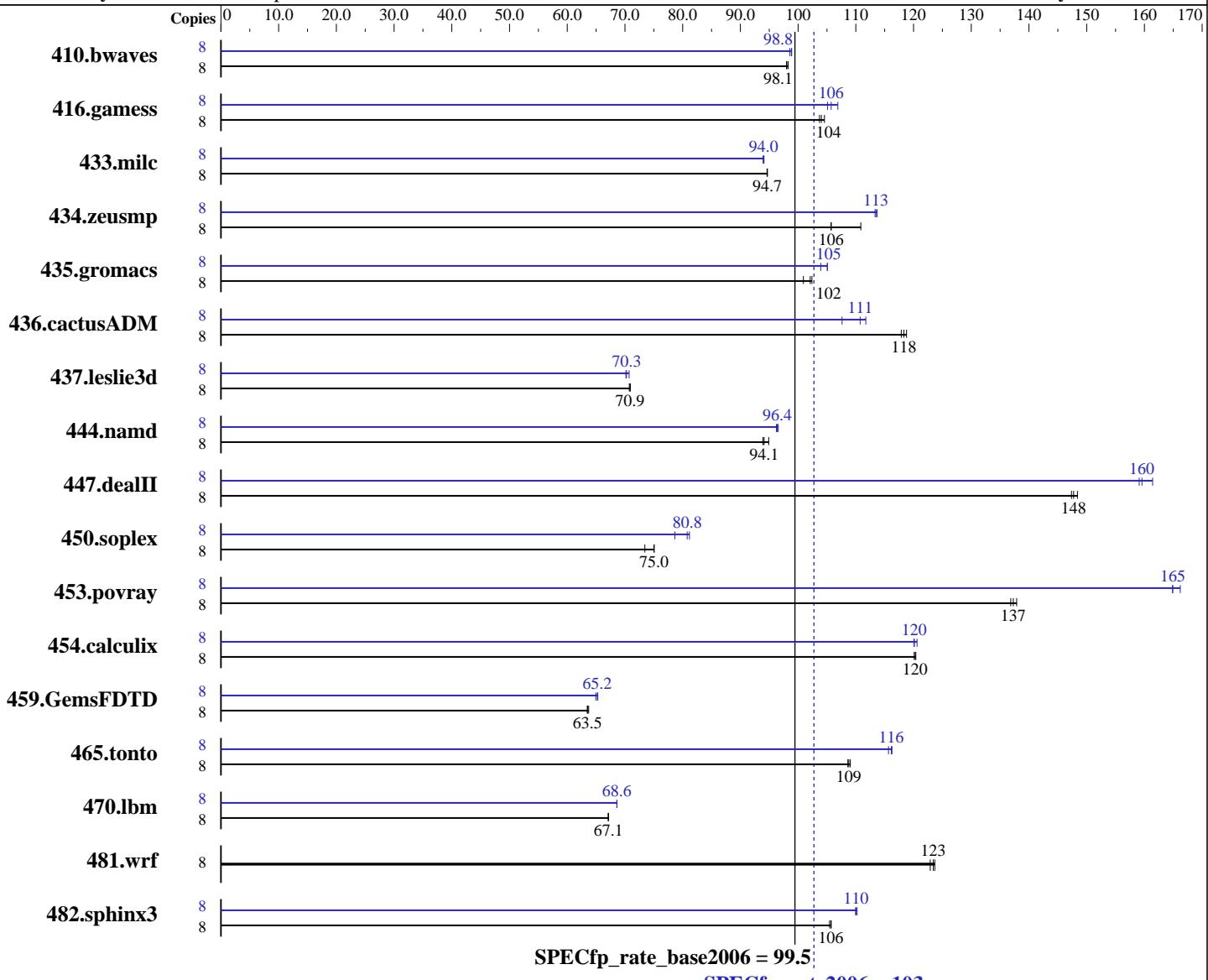
Test date: Apr-2009

Test sponsor: Acer Incorporated

Hardware Availability: Mar-2009

Tested by: Acer Incorporated

Software Availability: Feb-2009



Hardware

CPU Name: Intel Xeon X5570
CPU Characteristics: Intel Turbo Boost Technology up to 3.33 GHz
CPU MHz: 2933
FPU: Integrated
CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip, 2 threads/core
CPU(s) orderable: 1,2 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core

Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64) SP2, Kernel 2.6.16.60-0.21-smp
Compiler: Intel C++ and Fortran Compiler 11.0 for Linux Build 20090131 Package ID: l_cproc_p_11.0.080, l_cprof_p_11.0.080
Auto Parallel: No
File System: ReiserFS
System State: Run level 3 (multi-user)
Base Pointers: 64-bit

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L3 Cache: 8 MB I+D on chip per chip
 Other Cache: None
 Memory: 12 GB (3 x 4 GB Registered ECC DDR3-1333)
 Disk Subsystem: 320 GB SATA, 7200RPM
 Other Hardware: None

Peak Pointers: 32/64-bit
 Other Software: Binutils 2.18.50.0.7.20080502

Results Table

| Benchmark | Base | | | | | | | Peak | | | | | | |
|---------------|--------|-------------|-------------|-------------|-------------|-------------|-------------|--------|-------------|-------------|-------------|-------------|-------------|-------------|
| | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 410.bwaves | 8 | 1110 | 98.0 | 1106 | 98.3 | 1109 | 98.1 | 8 | 1099 | 98.9 | 1100 | 98.8 | 1103 | 98.6 |
| 416.gamess | 8 | 1510 | 104 | 1506 | 104 | 1498 | 105 | 8 | 1465 | 107 | 1482 | 106 | 1490 | 105 |
| 433.milc | 8 | 776 | 94.7 | 776 | 94.6 | 776 | 94.7 | 8 | 781 | 94.0 | 781 | 94.1 | 782 | 93.9 |
| 434.zeusmp | 8 | 657 | 111 | 688 | 106 | 689 | 106 | 8 | 642 | 113 | 642 | 113 | 640 | 114 |
| 435.gromacs | 8 | 558 | 102 | 559 | 102 | 566 | 101 | 8 | 544 | 105 | 544 | 105 | 550 | 104 |
| 436.cactusADM | 8 | 805 | 119 | 811 | 118 | 808 | 118 | 8 | 863 | 111 | 888 | 108 | 856 | 112 |
| 437.leslie3d | 8 | 1061 | 70.9 | 1063 | 70.7 | 1060 | 70.9 | 8 | 1063 | 70.7 | 1072 | 70.2 | 1070 | 70.3 |
| 444.namd | 8 | 683 | 93.9 | 676 | 94.9 | 682 | 94.1 | 8 | 664 | 96.6 | 666 | 96.4 | 667 | 96.2 |
| 447.dealII | 8 | 617 | 148 | 619 | 148 | 621 | 147 | 8 | 574 | 160 | 575 | 159 | 567 | 161 |
| 450.soplex | 8 | 908 | 73.4 | 889 | 75.1 | 889 | 75.0 | 8 | 848 | 78.7 | 822 | 81.2 | 826 | 80.8 |
| 453.povray | 8 | 310 | 137 | 309 | 138 | 311 | 137 | 8 | 258 | 165 | 258 | 165 | 256 | 166 |
| 454.calculix | 8 | 549 | 120 | 548 | 120 | 550 | 120 | 8 | 547 | 121 | 550 | 120 | 549 | 120 |
| 459.GemsFDTD | 8 | 1332 | 63.7 | 1337 | 63.5 | 1337 | 63.5 | 8 | 1302 | 65.2 | 1307 | 64.9 | 1300 | 65.3 |
| 465.tonto | 8 | 724 | 109 | 722 | 109 | 725 | 109 | 8 | 677 | 116 | 678 | 116 | 681 | 116 |
| 470.lbm | 8 | 1637 | 67.1 | 1638 | 67.1 | 1638 | 67.1 | 8 | 1602 | 68.6 | 1602 | 68.6 | 1602 | 68.6 |
| 481.wrf | 8 | 724 | 123 | 722 | 124 | 727 | 123 | 8 | 724 | 123 | 722 | 124 | 727 | 123 |
| 482.sphinx3 | 8 | 1475 | 106 | 1475 | 106 | 1479 | 105 | 8 | 1415 | 110 | 1417 | 110 | 1418 | 110 |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
 numactl was used to bind copies to the cores

Operating System Notes

'ulimit -s unlimited' was set for stacksize unlimited

Platform Notes

BIOS setting
 Hardware Prefetcher: Enabled
 Adjacent Cache Line Prefetch: Enabled



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Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icc ifort

Base Portability Flags

```
410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64
```

Base Optimization Flags

C benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static

C++ benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static

Fortran benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static

Benchmarks using both Fortran and C:
-xSSE4.2 -ipo -O3 -no-prec-div -static



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Peak Compiler Invocation

C benchmarks (except as noted below):

icc

482.sphinx3: icc -m32

C++ benchmarks (except as noted below):

icpc

450.soplex: icpc -m32

Fortran benchmarks (except as noted below):

ifort

437.leslie3d: ifort -m32

Benchmarks using both Fortran and C:

icc ifort

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

433.milc: -xsse4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-fno-alias

470.lbm: -xsse4.2 -ipo -O3 -no-prec-div -static -opt-prefetch
-auto-ilp32

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Peak Optimization Flags (Continued)

482.sphinx3: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2

C++ benchmarks:

444.namd: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-fno-alias -auto-ilp32

447.dealII: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll2 -ansi-alias -scalar-rep-

450.soplex: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-opt-malloc-options=3

453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

416.gamess: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll2 -Ob0 -ansi-alias -scalar-rep-

434.zeusmp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)

437.leslie3d: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-opt-malloc-options=3 -opt-prefetch

459.GemsFDTD: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll2 -Ob0 -opt-prefetch

465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll4 -auto

Benchmarks using both Fortran and C:

435.gromacs: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-opt-prefetch -auto-ilp32

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Peak Optimization Flags (Continued)

436.cactusADM: -xsSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll2 -opt-prefetch -auto-ilp32

454.calculix: -xsSE4.2 -ipo -O3 -no-prec-div -static -auto-ilp32

481.wrf: basepeak = yes

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-fp-linux64-revA.20090710.04.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-fp-linux64-revA.20090710.04.xml>

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Tested with SPEC CPU2006 v1.1.

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