



SPEC® CFP2006 Result

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Sun Microsystems

Sun Blade X6275 (Intel Xeon X5570 2.93GHz)

SPECfp®_rate2006 = 197

CPU2006 license: 6

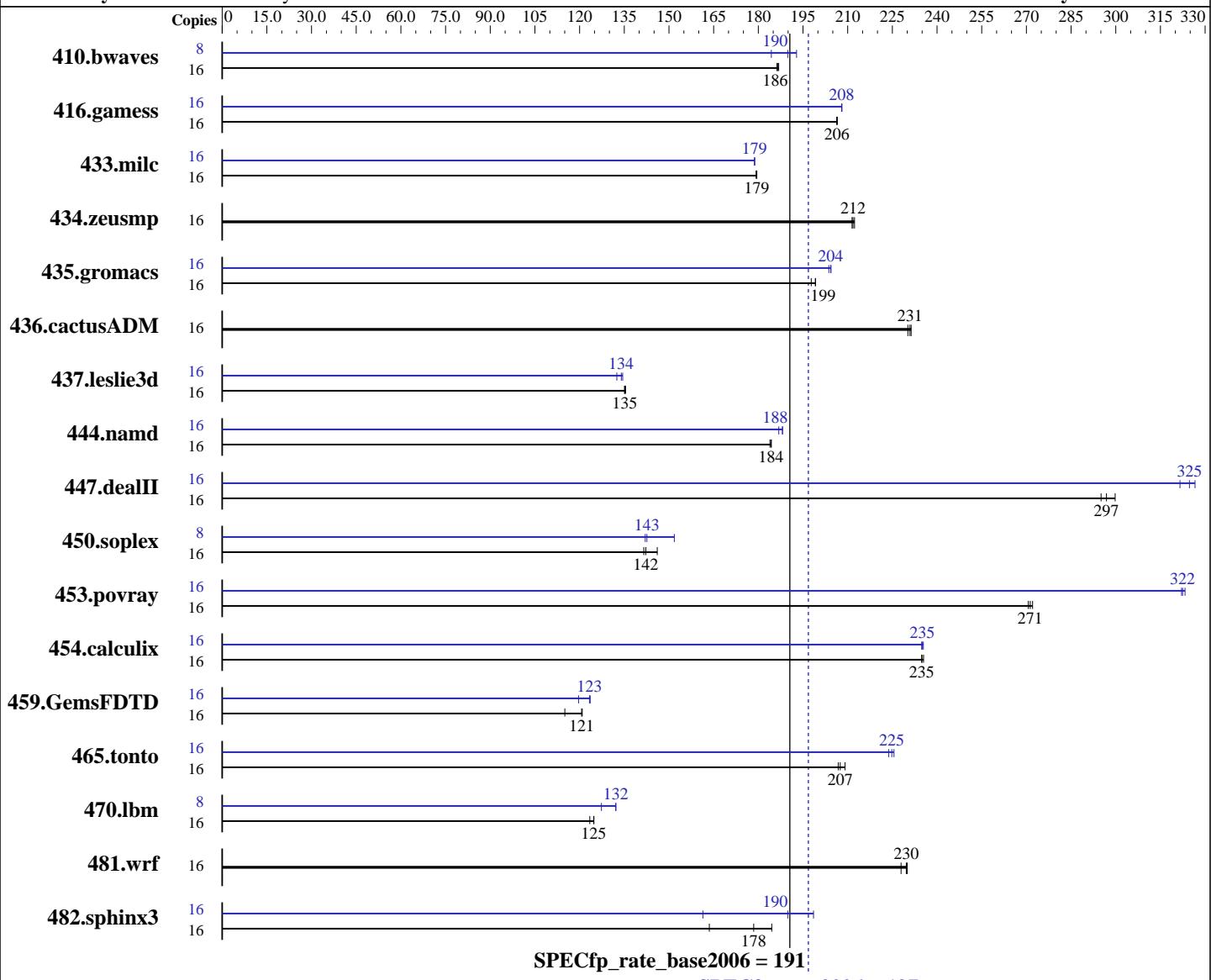
Test date: Mar-2009

Hardware Availability: Apr-2009

Software Availability: Nov-2008

Test sponsor: Sun Microsystems

Tested by: Sun Microsystems



Hardware		Software	
CPU Name:	Intel Xeon X5570	Operating System:	SuSe Linux Enterprise Server 10 (x86_64)
CPU Characteristics:	Intel Turbo Boost Technology up to 3.33 GHz	Compiler:	SP2, kernel 2.6.16.60-0.21-smp
CPU MHz:	2933	Auto Parallel:	Intel C++ and Fortran Compiler 11.0 for Linux
FPU:	Integrated	File System:	Build 20080930 Package ID: l_cproc_p_11.0.066,
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip, 2 threads/core	System State:	l_cprof_p_11.0.066
CPU(s) orderable:	1 or 2 chips		No
Primary Cache:	32 KB I + 32 KB D on chip per core		NFSv3
Secondary Cache:	256 KB I+D on chip per core		(See additional details below)

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L3 Cache: 8 MB I+D on chip per chip
 Other Cache: None
 Memory: 24 GB (6 x 4 GB DDR3-1333)
 Disk Subsystem: 48 x SATA 250 GB 7200 RPM via NFS for SPEC CPU2006
 Other Hardware: None

Base Pointers: 64-bit
 Peak Pointers: 32/64-bit
 Other Software: Binutils 2.18.50

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	16	1166	186	1164	187	1167	186	8	590	184	573	190	564	193
416.gamess	16	1518	206	1517	207	1518	206	16	1506	208	1506	208	1506	208
433.milc	16	818	180	818	179	820	179	16	822	179	822	179	822	179
434.zeusmp	16	686	212	689	211	688	212	16	686	212	689	211	688	212
435.gromacs	16	578	198	574	199	574	199	16	559	204	559	204	561	204
436.cactusADM	16	831	230	828	231	827	231	16	831	230	828	231	827	231
437.leslie3d	16	1111	135	1114	135	1113	135	16	1135	132	1119	134	1122	134
444.namd	16	696	184	698	184	696	184	16	687	187	682	188	682	188
447.dealII	16	620	295	617	297	611	300	16	560	327	564	325	569	322
450.soplex	16	938	142	943	142	914	146	8	470	142	468	143	440	152
453.povray	16	314	271	313	272	315	271	16	264	322	264	322	263	323
454.calculix	16	562	235	561	235	562	235	16	562	235	561	235	562	235
459.GemsFDTD	16	1476	115	1404	121	1407	121	16	1419	120	1377	123	1374	124
465.tonto	16	753	209	759	207	761	207	16	700	225	704	224	698	226
470.lbm	16	1781	123	1763	125	1761	125	8	832	132	863	127	832	132
481.wrf	16	784	228	778	230	777	230	16	784	228	778	230	777	230
482.sphinx3	16	1748	178	1690	185	1907	164	16	1932	161	1571	199	1642	190

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
 numactl was used to bind copies to the cores
 "(For details, please see the config file.)"

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

Platform Notes

Default BIOS settings used.



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General Notes

NFS for file system: NFS server, Sun Fire X4540 equipped with 48 x 250GB SATA 7200 RPM, serves the client over Gigabit ethernet connection.

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icc ifort

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static

C++ benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static

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Base Optimization Flags (Continued)

Fortran benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static

Benchmarks using both Fortran and C:

-xSSE4.2 -ipo -O3 -no-prec-div -static

Peak Compiler Invocation

C benchmarks (except as noted below):

icc

482.sphinx3: icc -m32

C++ benchmarks (except as noted below):

icpc

450.soplex: icpc -m32

Fortran benchmarks (except as noted below):

ifort

437.leslie3d: ifort -m32

Benchmarks using both Fortran and C:

icc ifort

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX



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Peak Optimization Flags

C benchmarks:

```
433.milc: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
           -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
           -fno-alias
```

```
470.lbm: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch
          -auto-ilp32
```

```
482.sphinx3: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2
```

C++ benchmarks:

```
444.namd: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
           -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
           -fno-alias -auto-ilp32
```

```
447.dealII: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
             -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
             -unroll2 -ansi-alias -scalar-rep-
```

```
450.soplex: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
             -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
             -opt-malloc-options=3
```

```
453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
              -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
              -unroll4 -ansi-alias
```

Fortran benchmarks:

```
410.bwaves: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch
```

```
416.gamess: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
              -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
              -unroll2 -Ob0 -ansi-alias -scalar-rep-
```

```
434.zeusmp: basepeak = yes
```

```
437.leslie3d: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
                -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
                -opt-malloc-options=3 -opt-prefetch
```

```
459.GemsFDTD: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
                 -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
                 -unroll2 -Ob0 -opt-prefetch
```

```
465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
              -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
              -unroll4 -auto
```

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Peak Optimization Flags (Continued)

Benchmarks using both Fortran and C:

```
435.gromacs: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
               -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
               -opt-prefetch -auto-ilp32
```

```
436.cactusADM: basepeak = yes
```

```
454.calculix: -xSSE4.2 -ipo -O3 -no-prec-div -static -auto-ilp32
```

```
481.wrf: basepeak = yes
```

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-fp-linux64-revA.20090710.09.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-fp-linux64-revA.20090710.09.xml>

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For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.

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