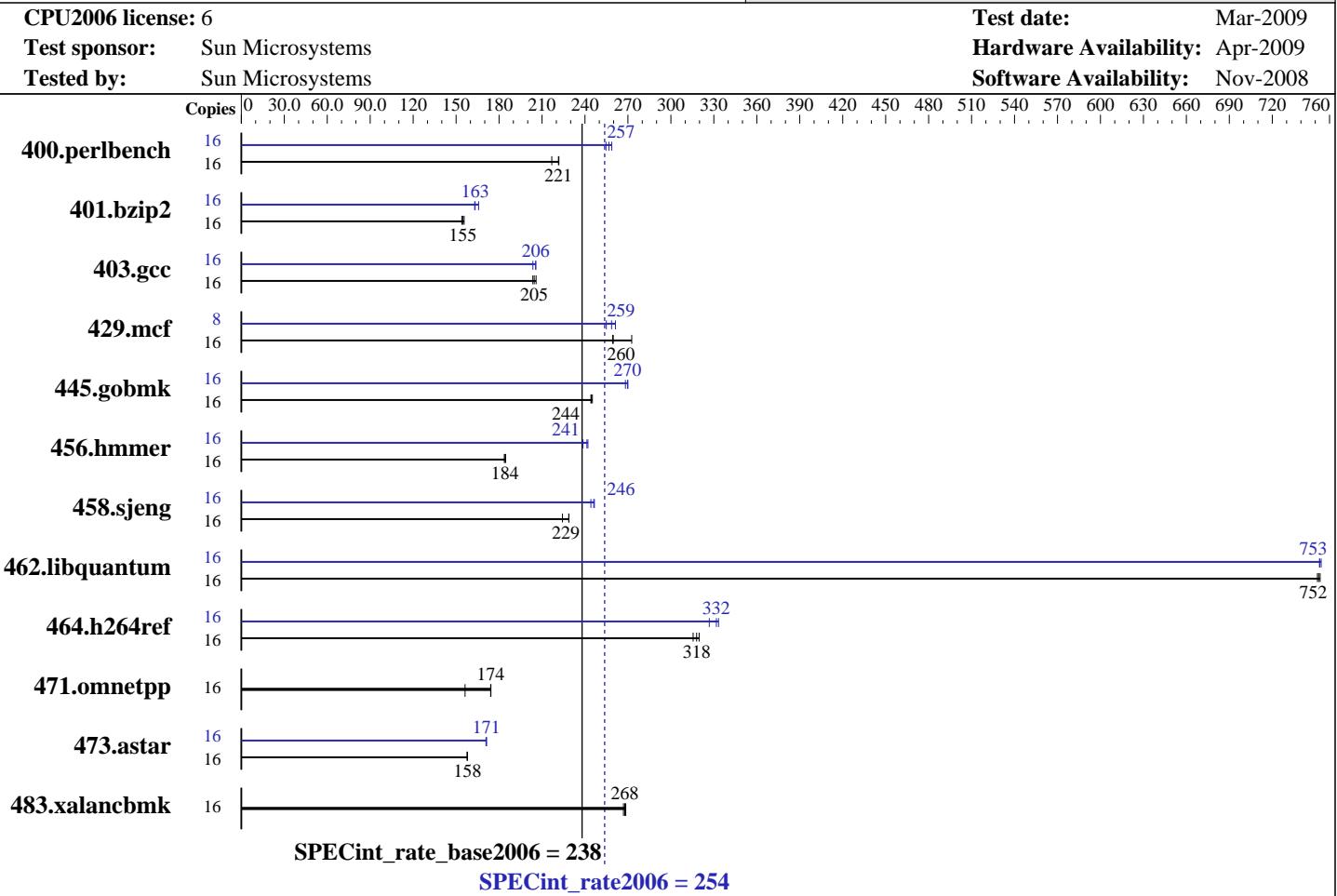# SPEC® CINT2006 Result
Copyright 2006-2014 Standard Performance Evaluation Corporation

Sun Microsystems

Sun Blade X6270 (Intel Xeon X5570 2.93GHz)

SPECint®_rate2006 = 254

SPECint_rate_base2006 = 238

CPU2006 license: 6

Test sponsor: Sun Microsystems

Tested by: Sun Microsystems

Test date: Mar-2009

Hardware Availability: Apr-2009

Software Availability: Nov-2008

SPECint_rate_base2006 = 238

SPECint_rate2006 = 254

Hardware

CPU Name: Intel Xeon X5570

CPU Characteristics: Intel Turbo Boost Technology up to 3.33 GHz

CPU MHz: 2933

FPU: Integrated

CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip, 2 threads/core

CPU(s) orderable: 1 or 2 chips

Primary Cache: 32 KB I + 32 KB D on chip per core

Secondary Cache: 256 KB I+D on chip per core

L3 Cache: 8 MB I+D on chip per chip

Other Cache: None

Memory: 24 GB (6 x 4 GB DDR3-1333)

Disk Subsystem: 134 GB using 1x ST914602SSUN146GSAS SAS 10 K RPM

Other Hardware: None

Software

Operating System: SuSe Linux Enterprise Server 10 (x86_64) SP2, kernel 2.6.16.60-0.21-smp

Compiler: Intel C++ and Fortran Compiler 11.0 for Linux Build 20080930 Package ID: l_cproc_p_11.0.066, l_cprof_p_11.0.066

Auto Parallel: No

File System: ReiserFS

System State: Run level 3 (multi-user)

Base Pointers: 32-bit

Peak Pointers: 32/64-bit

Other Software: Microquill SmartHeap V8.1 Binutils 2.18.50

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	16	721	217	705	222	**706**	**221**	16	614	255	**609**	**257**	604	259
401.bzip2	16	1003	154	**998**	**155**	992	156	16	**946**	**163**	947	163	932	166
403.gcc	16	626	206	**630**	**205**	633	204	16	**627**	**206**	633	204	626	206
429.mcf	16	**562**	**260**	563	259	535	273	8	**282**	**259**	286	255	279	261
445.gobmk	16	685	245	687	244	**687**	**244**	16	626	268	**622**	**270**	622	270
456.hmmer	16	809	185	813	184	**810**	**184**	16	626	239	617	242	**619**	**241**
458.sjeng	16	863	224	**847**	**229**	846	229	16	**787**	**246**	785	247	793	244
462.libquantum	16	441	752	**441**	**752**	440	753	16	**440**	**753**	440	754	440	753
464.h264ref	16	**1114**	**318**	1107	320	1122	316	16	1062	333	1083	327	**1067**	**332**
471.omnetpp	16	574	174	640	156	**574**	**174**	16	574	174	640	156	**574**	**174**
473.astar	16	712	158	**711**	**158**	711	158	16	656	171	657	171	**656**	**171**
483.xalancbmk	16	411	268	**412**	**268**	414	267	16	411	268	**412**	**268**	414	267

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
     The config file option 'submit' was used.
      numactl was used to bind copies to the cores
       "(For details, please see the config file.)"
```

Operating System Notes

```
     'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
```

Platform Notes

```
      Default BIOS settings used.
```

Base Compiler Invocation

C benchmarks:
    icc

C++ benchmarks:
    icpc

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Base Portability Flags

```
400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX
```

Base Optimization Flags

C benchmarks:
```
-xSSE4.2 -ipo -O3 -no-prec-div -static -inline-calloc
-opt-malloc-options=3 -opt-prefetch
```

C++ benchmarks:
```
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/spec/cpu2006.1.1/lib -lsmartheap
```

Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

Peak Compiler Invocation

C benchmarks (except as noted below):
```
icc
```

```
401.bzip2: /opt/intel/Compiler/11.0/066/bin/intel64/icc
```

```
456.hmmer: /opt/intel/Compiler/11.0/066/bin/intel64/icc
```

C++ benchmarks:
```
icpc
```

Peak Portability Flags

```
400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX
```

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Peak Optimization Flags

C benchmarks:

```
  400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
                 -prof-use(pass 2) -ansi-alias -opt-prefetch

      401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
                 -prof-use(pass 2) -opt-prefetch -ansi-alias

        403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -static -inline-calloc
                 -opt-malloc-options=3

        429.mcf: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
                 -prof-use(pass 2) -opt-prefetch

      445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2
                 -ipo -no-prec-div -ansi-alias

      456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2
                 -ansi-alias

      458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
                 -prof-use(pass 2) -unroll4

  462.libquantum: -xSSE4.2 -ipo -O3 -no-prec-div -static
                 -opt-malloc-options=3 -opt-prefetch

    464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
                 -prof-use(pass 2) -unroll2 -ansi-alias
```

C++ benchmarks:

```
    471.omnetpp: basepeak = yes

      473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
                 -ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs
                 -L/spec/cpu2006.1.1/lib -lsmartheap

    483.xalancbmk: basepeak = yes
```

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Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090710.07.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090710.07.xml`