SPEC® CINT2006 Result

NEC Corporation

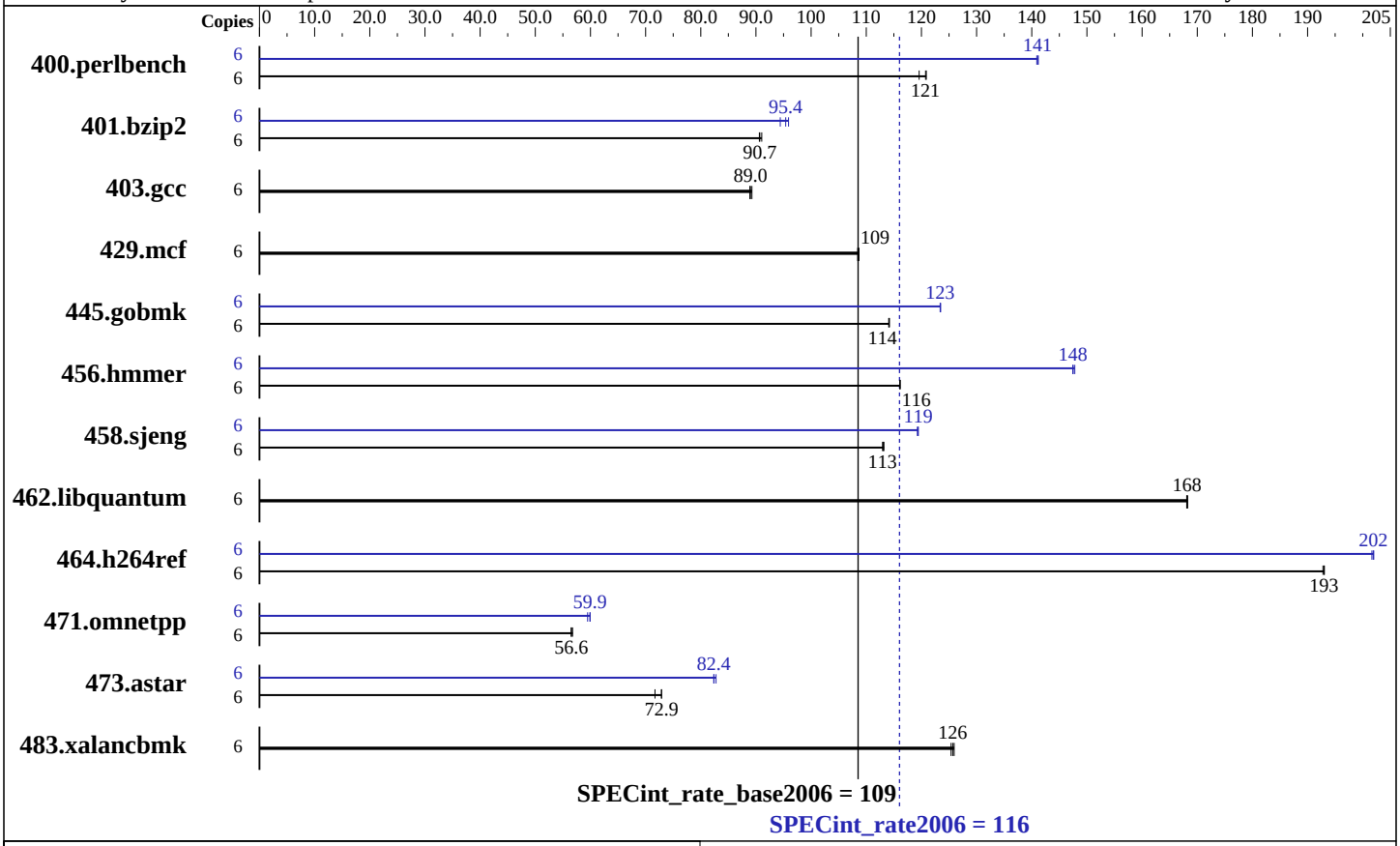
Express5800/B140a-T
(Intel Xeon E7220)

SPECint®_rate2006 = 116

SPECint_rate_base2006 = 109

CPU2006 license: 9006
Test sponsor: NEC Corporation
Tested by: NEC Corporation

Test date: Apr-2009
Hardware Availability: Feb-2009
Software Availability: Nov-2008



Hardware

CPU Name:	Intel Xeon E7220
CPU Characteristics:	1066 MHz system bus
CPU MHz:	2933
FPU:	Integrated
CPU(s) enabled:	6 cores, 3 chips, 2 cores/chip
CPU(s) orderable:	1,2,3,4 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	4 MB I+D on chip per core
L3 Cache:	None
Other Cache:	None
Memory:	32 GB (16x2 GB PC2-5300F, 2 rank, CL5-5-5, ECC)
Disk Subsystem:	1x146.5 GB SAS, 10000 RPM
Other Hardware:	None

Software

Operating System:	SUSE Linux Enterprise Server 10 (x86_64) SP2, Kernel 2.6.16.60-0.21-smp
Compiler:	Intel C++ Compiler 11.0 for Linux Build 20081105 Package ID: l_cproc_p_11.0.074
Auto Parallel:	No
File System:	ReiserFS
System State:	Run level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	MicroQuill SmartHeap Library 8.1 Binutils 2.18.50.0.7.20080502

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	6	490	120	**485**	**121**	485	121	6	**416**	**141**	416	141	415	141
401.bzip2	6	639	90.6	636	91.1	**638**	**90.7**	6	**607**	**95.4**	604	95.9	613	94.4
403.gcc	6	**543**	**89.0**	541	89.3	543	88.9	6	**543**	**89.0**	541	89.3	543	88.9
429.mcf	6	505	108	**504**	**109**	504	109	6	505	108	**504**	**109**	504	109
445.gobmk	6	552	114	551	114	**552**	**114**	6	**510**	**123**	510	123	510	123
456.hmmer	6	482	116	482	116	**482**	**116**	6	**380**	**148**	379	148	380	147
458.sjeng	6	641	113	**642**	**113**	643	113	6	**608**	**119**	609	119	608	119
462.libquantum	6	740	168	**739**	**168**	739	168	6	740	168	**739**	**168**	739	168
464.h264ref	6	**688**	**193**	688	193	689	193	6	**658**	**202**	658	202	657	202
471.omnetpp	6	661	56.8	**663**	**56.6**	664	56.5	6	**626**	**59.9**	626	59.9	630	59.5
473.astar	6	578	72.9	587	71.7	**578**	**72.9**	6	**511**	**82.4**	509	82.8	511	82.4
483.xalancbmk	6	330	125	**329**	**126**	329	126	6	330	125	**329**	**126**	329	126

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The config file option 'submit' was used.
 taskset was used to bind processes to cores
```

Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
```

Platform Notes

```
Bios settings:
Hardware Prefetcher:              Enabled
Adjacent Cache Line Prefetch:    Disabled
FSB High Bandwidth Optimization: Disabled
```

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
462.libquantum: `-DSPEC_CPU_LINUX`
483.xalancbmk: `-DSPEC_CPU_LINUX`

Base Optimization Flags

C benchmarks:
`-xSSSE3 -ipo -O3 -no-prec-div -static -inline-calloc -opt-malloc-options=3 -opt-prefetch`

C++ benchmarks:
`-xSSSE3 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs -L/opt/SmartHeap_8.1/lib -lsmartheap`

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks (except as noted below):
`icc`

401.bzip2: `/opt/intel/Compiler/11.0/074/bin/intel64/icc -L/opt/intel/Compiler/11.0/074/ipp/em64t/lib -I/opt/intel/Compiler/11.0/074/ipp/em64t/include`

456.hmmer: `/opt/intel/Compiler/11.0/074/bin/intel64/icc -L/opt/intel/Compiler/11.0/074/ipp/em64t/lib -I/opt/intel/Compiler/11.0/074/ipp/em64t/include`

C++ benchmarks:
`icpc`

Peak Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
401.bzip2: `-DSPEC_CPU_LP64`
456.hmmer: `-DSPEC_CPU_LP64`

Continued on next page

Peak Portability Flags (Continued)

462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3
-no-prec-div -static -ansi-alias -opt-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3
-no-prec-div -static -opt-prefetch -ansi-alias

403.gcc: basepeak = yes

429.mcf: basepeak = yes

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -O2 -ipo
-no-prec-div -ansi-alias

456.hmmer: -xSSSE3 -ipo -O3 -no-prec-div -static -unroll2
-ansi-alias

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3
-no-prec-div -static -unroll4

462.libquantum: basepeak = yes

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3
-no-prec-div -static -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3
-no-prec-div -ansi-alias -opt-ra-region-strategy=block
-Wl,-z,muldefs -L/opt/SmartHeap_8.1/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine
-Wl,-z,muldefs -L/opt/SmartHeap_8.1/lib -lsmartheap

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags files that were used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revE.html`
`http://www.spec.org/cpu2006/flags/NEC-Intel-Linux-Settings-flags-revB.html`

You can also download the XML flags sources by saving the following links:
`http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revE.xml`
`http://www.spec.org/cpu2006/flags/NEC-Intel-Linux-Settings-flags-revB.xml`

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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