SPEC® CFP2006 Result

Dell Inc.

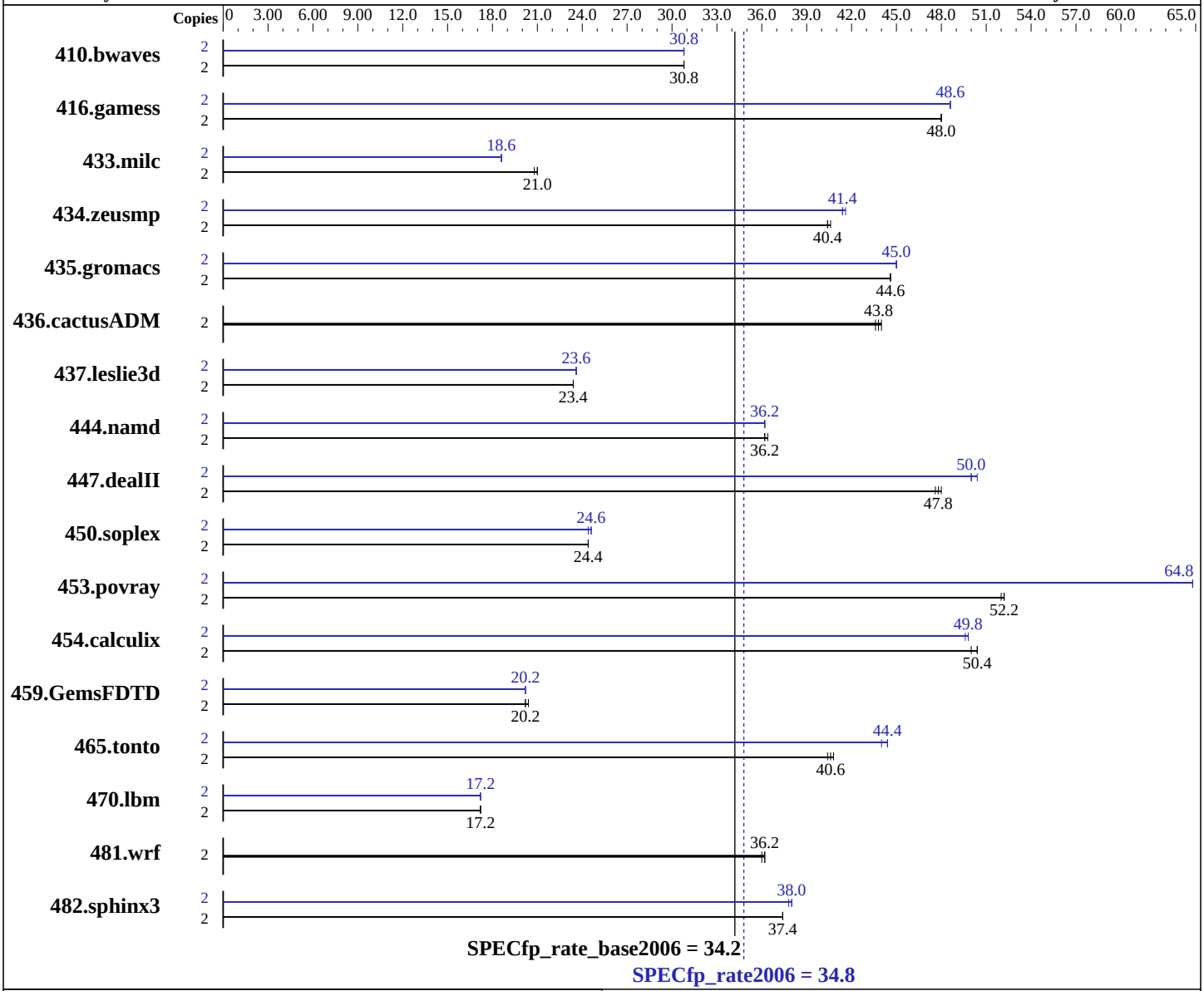
Dell Precision T3400 (Intel E8600, 3.33 GHz)

SPECfp®_rate2006 = 34.8

SPECfp_rate_base2006 = 34.2

CPU2006 license: 55
Test sponsor: Dell Inc.
Tested by: Dell Inc.

Test date: Nov-2008
Hardware Availability: Jan-2009
Software Availability: Nov-2008



Hardware

CPU Name:	Intel Core 2 Duo E8600
CPU Characteristics:	1333 MHz Bus Speed
CPU MHz:	3333
FPU:	Integrated
CPU(s) enabled:	2 cores, 1 chip, 2 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	6 MB I+D on chip per chip

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Software

Operating System:	Windows Vista Ultimate SP1 (64-bit)
Compiler:	Intel C++ Compiler for Intel 64, Version 11.0 Build 20080930 Package ID: w_cproc_p_11.0.061 Intel Visual Fortran Compiler for Intel 64, Version 11.0 Build 20080930 Package ID: w_cprof_p_11.0.061 Microsoft Visual Studio 2008 SP1
Auto Parallel:	No
File System:	NTFS

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L3 Cache:	None	System State:	Default
Other Cache:	None	Base Pointers:	32/64-bit
Memory:	8 GB (4x2 GB DDR2-800, ECC, CL6)	Peak Pointers:	32/64-bit
Disk Subsystem:	1 x 160 GB SATA 7200 RPM	Other Software:	MicroQuill SmartHeap Library 8.1 for x64
Other Hardware:	None		

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	2	881	30.8	882	30.8	**881**	**30.8**	2	882	30.8	881	30.8	**881**	**30.8**
416.gamess	2	**817**	**48.0**	817	48.0	817	48.0	2	806	48.6	**806**	**48.6**	806	48.6
433.milc	2	879	20.8	**878**	**21.0**	877	21.0	2	**989**	**18.6**	988	18.6	991	18.6
434.zeusmp	2	449	40.6	450	40.4	**450**	**40.4**	2	438	41.6	439	41.4	**439**	**41.4**
435.gromacs	2	**320**	**44.6**	320	44.6	320	44.6	2	317	45.0	**317**	**45.0**	317	45.0
436.cactusADM	2	547	43.6	**547**	**43.8**	544	44.0	2	547	43.6	**547**	**43.8**	544	44.0
437.leslie3d	2	802	23.4	**803**	**23.4**	803	23.4	2	793	23.6	**794**	**23.6**	794	23.6
444.namd	2	442	36.4	**443**	**36.2**	444	36.2	2	**444**	**36.2**	444	36.2	444	36.2
447.dealII	2	**479**	**47.8**	481	47.6	476	48.0	2	**458**	**50.0**	459	50.0	453	50.4
450.soplex	2	**682**	**24.4**	685	24.4	682	24.4	2	681	24.4	**680**	**24.6**	680	24.6
453.povray	2	204	52.0	**204**	**52.2**	204	52.2	2	164	64.8	164	64.8	**164**	**64.8**
454.calculix	2	329	50.0	**328**	**50.4**	327	50.4	2	332	49.8	**332**	**49.8**	332	49.6
459.GemsFDTD	2	1046	20.2	**1046**	**20.2**	1043	20.4	2	1055	20.2	**1053**	**20.2**	1053	20.2
465.tonto	2	487	40.4	482	40.8	**484**	**40.6**	2	444	44.4	**444**	**44.4**	447	44.0
470.lbm	2	1590	17.2	**1591**	**17.2**	1592	17.2	2	**1591**	**17.2**	1590	17.2	1591	17.2
481.wrf	2	**618**	**36.2**	617	36.2	620	36.0	2	**618**	**36.2**	617	36.2	620	36.0
482.sphinx3	2	1044	37.4	**1043**	**37.4**	1042	37.4	2	1025	38.0	1029	37.8	**1027**	**38.0**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

```
Binaries were built on Windows Vista Ultimate (64-bit)
```

Base Compiler Invocation

C benchmarks:

```
icl -Qvc9 -Qstd=c99
```

C++ benchmarks:

```
icl -Qvc9
```

Fortran benchmarks:

```
ifort
```

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Base Compiler Invocation (Continued)

Benchmarks using both Fortran and C:
icl -Qvc9 -Qstd=c99 ifort

Base Portability Flags

410.bwaves: -DSPEC_CPU_P64 /assume:underscore
416.gamess: -DSPEC_CPU_P64
433.milc: -DSPEC_CPU_P64
434.zeusmp: -DSPEC_CPU_P64
435.gromacs: -DSPEC_CPU_P64
436.cactusADM: -DSPEC_CPU_P64 -Qlowercase /assume:underscore
437.leslie3d: -DSPEC_CPU_P64
444.namd: -DSPEC_CPU_P64 /TP
447.dealII: -DSPEC_CPU_P64 -DDEAL_II_MEMBER_VAR_SPECIALIZATION_BUG
450.soplex: -DSPEC_CPU_P64
453.povray: -DSPEC_CPU_P64 -DSPEC_CPU_WINDOWS_ICL
454.calculix: -DSPEC_CPU_P64 -DSPEC_CPU_NOZMODIFIER -Qlowercase
459.GemsFDTD: -DSPEC_CPU_P64
465.tonto: -DSPEC_CPU_P64
470.lbm: -DSPEC_CPU_P64
481.wrf: -DSPEC_CPU_P64 -DSPEC_CPU_WINDOWS_ICL
482.sphinx3: -DSPEC_CPU_P64

Base Optimization Flags

C benchmarks:
-QxSSE4.1 -Qauto-ilp32 -Qipo -O3 -Qprec-div- -Qopt-prefetch
/F512000000

C++ benchmarks:
-QxSSE4.1 -Qauto-ilp32 -Qipo -O3 -Qprec-div- -Qopt-prefetch
-Qcxx_features /F512000000 shlw64m.lib
-link /FORCE:MULTIPLE

Fortran benchmarks:
-QxSSE4.1 -Qauto-ilp32 -Qipo -O3 -Qprec-div- -Qopt-prefetch
/F1000000000

Benchmarks using both Fortran and C:
-QxSSE4.1 -Qauto-ilp32 -Qipo -O3 -Qprec-div- -Qopt-prefetch
/F1000000000

Peak Compiler Invocation

C benchmarks:
icl -Qvc9 -Qstd=c99

C++ benchmarks:
icl -Qvc9

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icl -Qvc9 -Qstd=c99 ifort

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: -Qprof_gen(pass 1) -QxSSE4.1(pass 2) -Qauto-ilp32(pass 2)
-Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Oa /F512000000

470.lbm: -QxSSE4.1 -Qipo -O3 -Qprec-div- -Qopt-prefetch
/F512000000

482.sphinx3: -QxSSE4.1 -Qauto-ilp32 -Qipo -O3 -Qprec-div- -Qunroll2
/F512000000

C++ benchmarks:

444.namd: -Qprof_gen(pass 1) -QxSSE4.1(pass 2) -Qauto-ilp32(pass 2)
-Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Oa /F512000000
shlw64m.lib -link /FORCE:MULTIPLE

447.dealII: -Qprof_gen(pass 1) -QxSSE4.1(pass 2) -Qauto-ilp32(pass 2)
-Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qunroll2
-Qansi-alias -Qscalar-rep- /F512000000 shlw64m.lib
-link /FORCE:MULTIPLE

450.soplex: -Qprof_gen(pass 1) -QxSSE4.1 -Qauto-ilp32
-Qprof_use(pass 2) -Qipo -O3 -Qprec-div- /F512000000
shlw64m.lib -link /FORCE:MULTIPLE

453.povray: -Qprof_gen(pass 1) -QxSSE4.1(pass 2) -Qauto-ilp32(pass 2)
-Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qunroll4
-Qansi-alias /F512000000 shlw64m.lib

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Peak Optimization Flags (Continued)

453.povray (continued):
-link /FORCE:MULTIPLE

Fortran benchmarks:

410.bwaves: -QxSSE4.1 -Qauto-ilp32 -Qipo -O3 -Qprec-div- -Qopt-prefetch /F1000000000

416.gamess: -Qprof_gen(pass 1) -QxSSE4.1(pass 2) -Qauto-ilp32(pass 2) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qunroll2 -Ob0 -Qansi-alias -Qscalar-rep- /F1000000000

434.zeusmp: -Qprof_gen(pass 1) -QxSSE4.1 -Qauto-ilp32 -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- /F1000000000

437.leslie3d: -Qprof_gen(pass 1) -QxSSE4.1(pass 2) -Qauto-ilp32(pass 2) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qopt-prefetch /F1000000000

459.GemsFDTD: -Qprof_gen(pass 1) -QxSSE4.1(pass 2) -Qauto-ilp32(pass 2) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qunroll2 -Ob0 -Qopt-prefetch /F1000000000

465.tonto: -Qprof_gen(pass 1) -QxSSE4.1(pass 2) -Qauto-ilp32(pass 2) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qunroll4 -Qauto /F1000000000

Benchmarks using both Fortran and C:

435.gromacs: -Qprof_gen(pass 1) -QxSSE4.1(pass 2) -Qauto-ilp32(pass 2) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qopt-prefetch /F1000000000

436.cactusADM: basepeak = yes

454.calculix: -QxSSE4.1 -Qauto-ilp32 -Qipo -O3 -Qprec-div- /F1000000000

481.wrf: basepeak = yes

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/dell.ic11.0.windows.flags.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/dell.ic11.0.windows.flags.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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