SPEC® CFP2006 Result

Dell Inc.

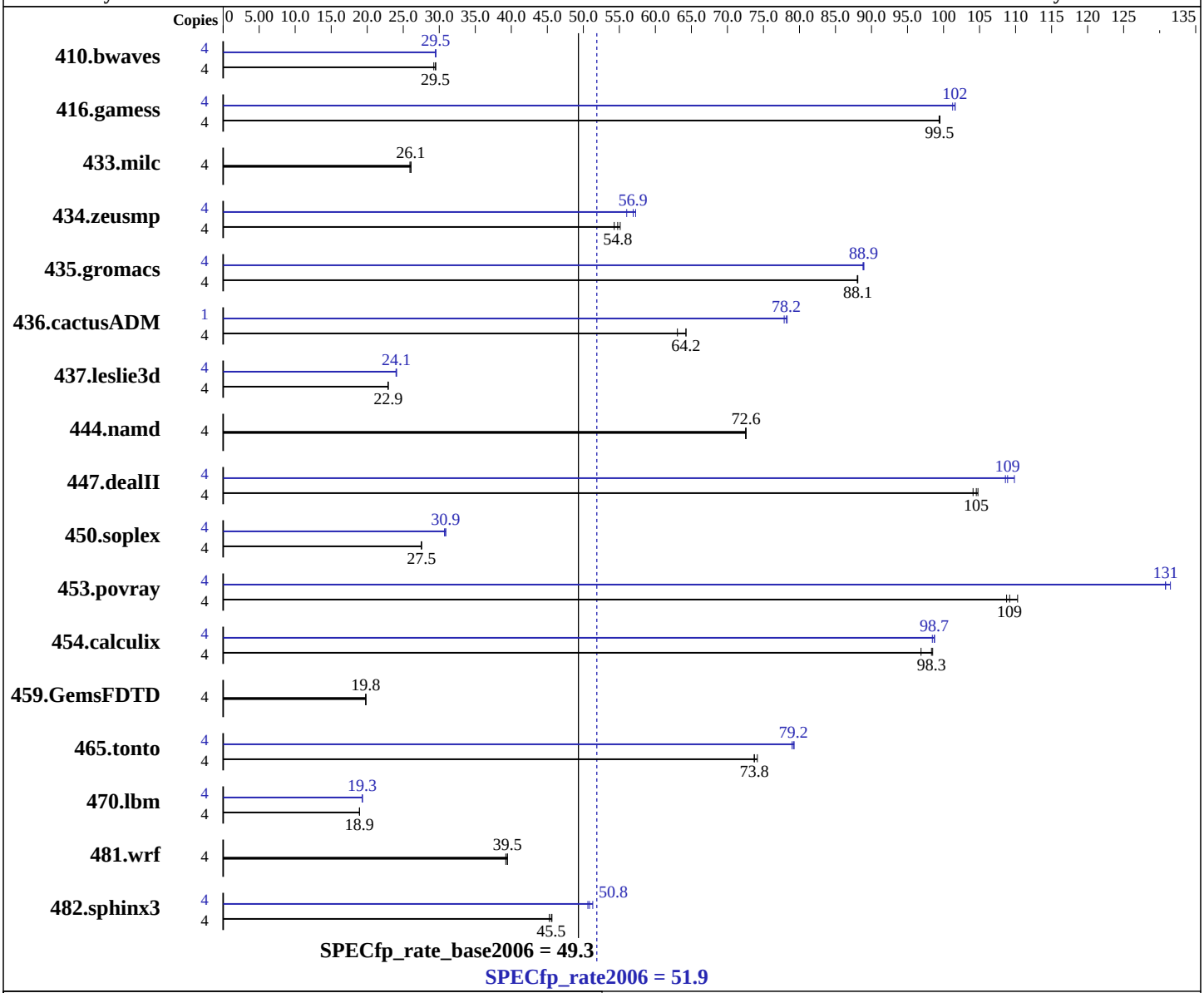
PowerEdge R300 (Intel Xeon X5470, 3.33 GHz)

SPECfp®_rate2006 = 51.9

SPECfp_rate_base2006 = 49.3

CPU2006 license: 55
Test sponsor: Dell Inc.
Tested by: Dell Inc.

Test date: Oct-2008
Hardware Availability: Sep-2008
Software Availability: Oct-2008



Hardware

CPU Name:	Intel Xeon X5470
CPU Characteristics:	
CPU MHz:	3333
FPU:	Integrated
CPU(s) enabled:	4 cores, 1 chip, 4 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	12 MB I+D on chip per chip, 6 MB shared / 2 cores

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Software

Operating System:	SUSE Linux Enterprise Server 10 (x86_64) SP2, Kernel 2.6.16-60.0.21-smp
Compiler:	Intel C++ and Fortran Compiler 11.0 for Linux Build 20080730 Package ID: l_cproc_b_11.0.042, l_fproc_b_11.0.042
Auto Parallel:	Yes
File System:	ReiserFS
System State:	Run level 3 (multi-user)
Base Pointers:	64-bit

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L3 Cache: None
Other Cache: None
Memory: 16 GB (4 x 4 GB DDR2-667)
Disk Subsystem: 1 x 160 GB SATA, 7200 RPM
Other Hardware: None

Peak Pointers: 32/64-bit
Other Software: Binutils 2.18.50.0.7.20080502

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	4	1859	29.2	**1845**	**29.5**	1842	29.5	4	1841	29.5	1844	29.5	**1843**	**29.5**
416.gamess	4	788	99.4	787	99.5	**787**	**99.5**	4	773	101	**771**	**102**	771	102
433.milc	4	1416	25.9	**1409**	**26.1**	1409	26.1	4	1416	25.9	**1409**	**26.1**	1409	26.1
434.zeusmp	4	671	54.3	660	55.1	**665**	**54.8**	4	**640**	**56.9**	650	56.0	636	57.3
435.gromacs	4	**324**	**88.1**	324	88.0	324	88.1	4	321	88.9	**321**	**88.9**	322	88.8
436.cactusADM	4	758	63.1	**744**	**64.2**	744	64.3	1	**153**	**78.2**	153	78.3	153	77.9
437.leslie3d	4	**1641**	**22.9**	1644	22.9	1640	22.9	4	1567	24.0	**1563**	**24.1**	1562	24.1
444.namd	4	442	72.5	442	72.6	**442**	**72.6**	4	442	72.5	442	72.6	**442**	**72.6**
447.dealII	4	440	104	**438**	**105**	437	105	4	421	109	417	110	**420**	**109**
450.soplex	4	**1212**	**27.5**	1213	27.5	1211	27.5	4	1086	30.7	1080	30.9	**1080**	**30.9**
453.povray	4	196	109	**195**	**109**	193	110	4	**163**	**131**	163	131	162	131
454.calculix	4	**336**	**98.3**	341	96.9	335	98.5	4	**334**	**98.7**	335	98.4	334	98.7
459.GemsFDTD	4	2144	19.8	**2143**	**19.8**	2142	19.8	4	2144	19.8	**2143**	**19.8**	2142	19.8
465.tonto	4	531	74.2	534	73.7	**534**	**73.8**	4	**497**	**79.2**	497	79.3	498	79.0
470.lbm	4	2905	18.9	**2903**	**18.9**	2902	18.9	4	2848	19.3	**2844**	**19.3**	2843	19.3
481.wrf	4	1139	39.2	**1132**	**39.5**	1132	39.5	4	1139	39.2	**1132**	**39.5**	1132	39.5
482.sphinx3	4	**1712**	**45.5**	1722	45.3	1708	45.6	4	**1533**	**50.8**	1519	51.3	1540	50.6

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The config file option 'submit' was used.
```

General Notes

```
taskset was used to bind processes to cores except
for 436.cactusADM peak
OMP_NUM_THREADS set to number of processors
KMP_AFFINITY set to "physical,0"
KMP_STACKSIZE set to 64M
```

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icc ifort

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch

C++ benchmarks:
-xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch

Fortran benchmarks:
-xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch

Benchmarks using both Fortran and C:
-xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch

Peak Compiler Invocation

C benchmarks (except as noted below):
```
icc
```

482.sphinx3:
```
/opt/intel/Compiler/11.0/042/bin/ia32/icc
-L/opt/intel/Compiler/11.0/042/ipp/ia32/lib
-I/opt/intel/Compiler/11.0/042/ipp/ia32/include
```

C++ benchmarks (except as noted below):
```
icpc
```

450.soplex:
```
/opt/intel/Compiler/11.0/042/bin/ia32/icpc
-L/opt/intel/Compiler/11.0/042/ipp/ia32/lib
-I/opt/intel/Compiler/11.0/042/ipp/ia32/include
```

Fortran benchmarks (except as noted below):
```
ifort
```

437.leslie3d:
```
/opt/intel/Compiler/11.0/042/bin/ia32/ifort
-L/opt/intel/Compiler/11.0/042/ipp/ia32/lib
-I/opt/intel/Compiler/11.0/042/ipp/ia32/include
```

Benchmarks using both Fortran and C:
```
icc ifort
```

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

Continued on next page

Peak Optimization Flags (Continued)

433.milc: basepeak = yes

470.lbm: -xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch -auto-ilp32

482.sphinx3: -xSSE4.1 -ipo -O3 -no-prec-div -static -unroll2

C++ benchmarks:

444.namd: basepeak = yes

447.dealII: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3 -no-prec-div -static -unroll2 -ansi-alias -scalar-rep-

450.soplex: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3 -no-prec-div -static -opt-malloc-options=3

453.povray: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3 -no-prec-div -static -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch

416.gamess: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3 -no-prec-div -static -unroll2 -Ob0 -ansi-alias -scalar-rep-

434.zeusmp: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3 -no-prec-div -static

437.leslie3d: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3 -no-prec-div -static -opt-malloc-options=3 -opt-prefetch

459.GemsFDTD: basepeak = yes

465.tonto: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3 -no-prec-div -static -unroll4 -auto

Benchmarks using both Fortran and C:

435.gromacs: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch -auto-ilp32

436.cactusADM: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3 -no-prec-div -static -unroll2 -opt-prefetch -parallel -auto-ilp32

454.calculix: -xSSE4.1 -ipo -O3 -no-prec-div -static -auto-ilp32

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Peak Optimization Flags (Continued)

481.wrf: `basepeak = yes`

The flags files that were used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic11.0-fp-linux64-revA.20090713.05.html`
`http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20090713.11.html`

You can also download the XML flags sources by saving the following links:
`http://www.spec.org/cpu2006/flags/Intel-ic11.0-fp-linux64-revA.20090713.05.xml`
`http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20090713.11.xml`

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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