



SPEC® CFP2006 Result

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Acer Incorporated

SPECfp®_rate2006 = 52.7

Acer Altos G330 Mk2 (Xeon X3370)

SPECfp_rate_base2006 = 50.7

CPU2006 license: 97

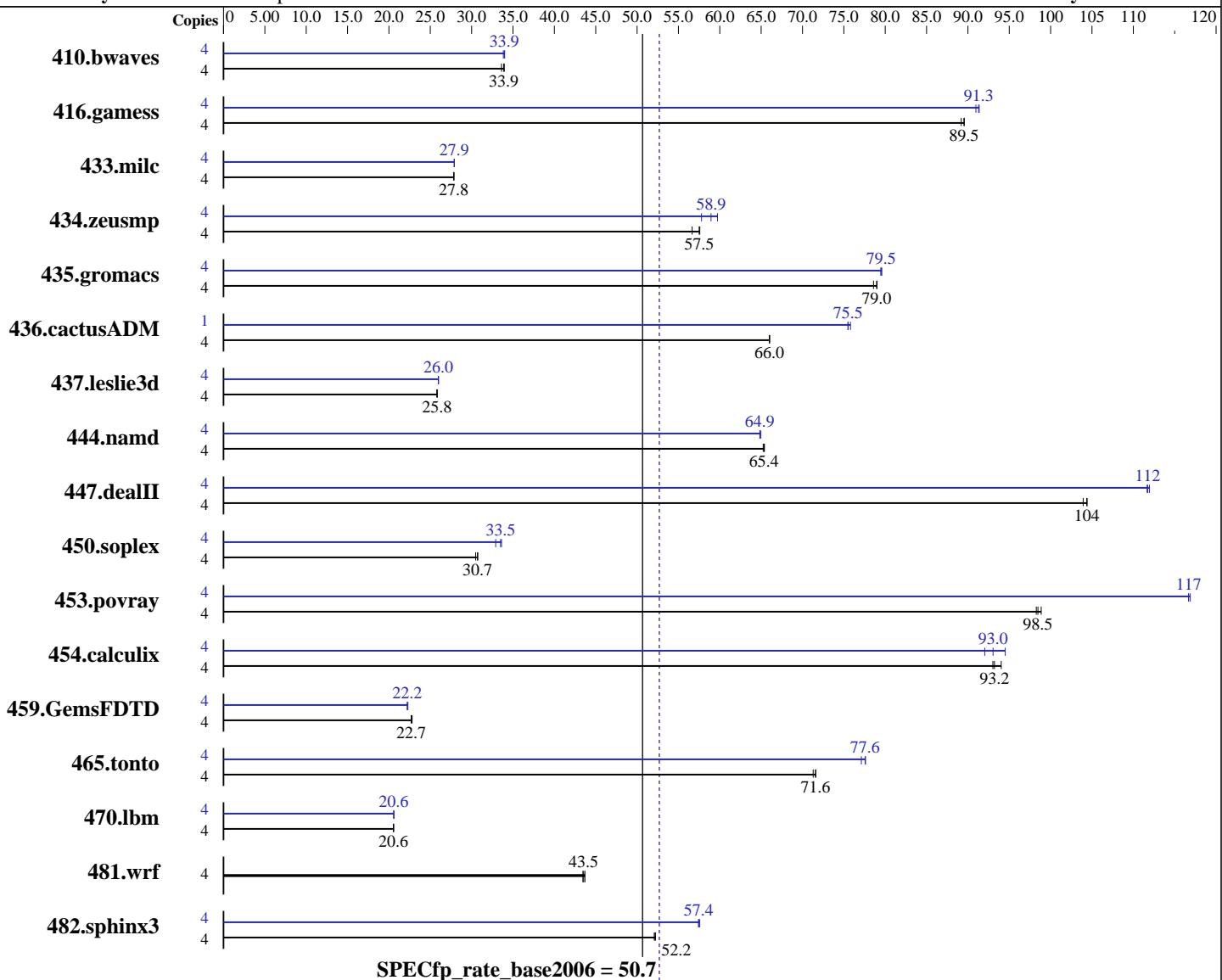
Test date: Oct-2008

Test sponsor: Acer Incorporated

Hardware Availability: Aug-2008

Tested by: Acer Incorporated

Software Availability: Nov-2008



Hardware

CPU Name: Intel Xeon X3370
CPU Characteristics: 1333 MHz system bus
CPU MHz: 3000
FPU: Integrated
CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip
CPU(s) orderable: 1 chip
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores

Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smp
Compiler: Intel C++ and Fortran Compiler 11.0 for Linux Build 20080730 Package ID: l_cproc_b_11.0.042, l_fproc_b_11.0.042
Auto Parallel: Yes
File System: ReiserFS
System State: Multi-user, level 3
Base Pointers: 64-bit

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L3 Cache: None
 Other Cache: None
 Memory: 8 GB (4 x 2048 MB ECC Unbuffered DDR2-800 CL5)
 Disk Subsystem: 1 x 160 GB 7200RPM SATA HDD
 Other Hardware: None

Peak Pointers: 32/64-bit
 Other Software: Binutils 2.18.50.0.7.20080502

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	4	1617	33.6	1601	33.9	1604	33.9	4	1607	33.8	1599	34.0	1601	33.9
416.gamess	4	875	89.5	874	89.6	878	89.2	4	857	91.4	861	91.0	858	91.3
433.milc	4	1319	27.8	1318	27.9	1319	27.8	4	1316	27.9	1315	27.9	1316	27.9
434.zeusmp	4	632	57.6	642	56.7	633	57.5	4	618	58.9	609	59.7	630	57.8
435.gromacs	4	361	79.0	362	79.0	363	78.6	4	359	79.5	359	79.6	359	79.5
436.cactusADM	4	724	66.0	724	66.0	724	66.0	1	158	75.5	158	75.5	158	75.8
437.leslie3d	4	1456	25.8	1458	25.8	1456	25.8	4	1447	26.0	1447	26.0	1447	26.0
444.namd	4	491	65.4	492	65.2	491	65.4	4	495	64.8	494	65.0	495	64.9
447.dealII	4	440	104	438	104	438	104	4	410	112	409	112	410	112
450.soplex	4	1094	30.5	1085	30.7	1086	30.7	4	1014	32.9	992	33.6	996	33.5
453.povray	4	217	98.3	215	98.9	216	98.5	4	182	117	182	117	182	117
454.calculix	4	354	93.2	351	94.0	355	93.0	4	349	94.5	359	92.0	355	93.0
459.GemsFDTD	4	1861	22.8	1870	22.7	1870	22.7	4	1903	22.3	1910	22.2	1910	22.2
465.tonto	4	550	71.6	550	71.6	552	71.3	4	510	77.1	507	77.6	507	77.6
470.lbm	4	2673	20.6	2674	20.6	2673	20.6	4	2669	20.6	2670	20.6	2669	20.6
481.wrf	4	1022	43.7	1028	43.5	1026	43.5	4	1022	43.7	1028	43.5	1026	43.5
482.sphinx3	4	1494	52.2	1493	52.2	1497	52.1	4	1357	57.4	1354	57.6	1357	57.4

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
 taskset was used to bind processes to cores except
 for 436.cactusADM peak

Platform Notes

BIOS Settings
 Adjacent Cache Line Prefetch disabled
 Hardware Prefetch enabled



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General Notes

OMP_NUM_THREADS set to number of processors

KMP_AFFINITY set to "physical,0"

KMP_STACKSIZE set to 64M

'ulimit -s unlimited' was set for stacksize unlimited

Base Compiler Invocation

C benchmarks:

icc

C++ benchmarks:

icpc

Fortran benchmarks:

ifort

Benchmarks using both Fortran and C:

icc ifort

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:

-xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch

C++ benchmarks:

-xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch

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Base Optimization Flags (Continued)

Fortran benchmarks:

```
-xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch
```

Benchmarks using both Fortran and C:

```
-xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch
```

Peak Compiler Invocation

C benchmarks (except as noted below):

```
/opt/intel/Compiler/11.0/042/bin/ia32/icc  
-L/opt/intel/Compiler/11.0/042/ipp/ia32/lib  
-I/opt/intel/Compiler/11.0/042/ipp/ia32/include
```

433.milc: icc

C++ benchmarks (except as noted below):

icpc

```
450.soplex: /opt/intel/Compiler/11.0/042/bin/ia32/icpc  
-L/opt/intel/Compiler/11.0/042/ipp/ia32/lib  
-I/opt/intel/Compiler/11.0/042/ipp/ia32/include
```

Fortran benchmarks (except as noted below):

ifort

```
437.leslie3d: /opt/intel/Compiler/11.0/042/bin/ia32/ifort  
-L/opt/intel/Compiler/11.0/042/ipp/ia32/lib  
-I/opt/intel/Compiler/11.0/042/ipp/ia32/include
```

Benchmarks using both Fortran and C:

icc ifort

Peak Portability Flags

```
410.bwaves: -DSPEC_CPU_LP64  
416.gamess: -DSPEC_CPU_LP64  
433.milc: -DSPEC_CPU_LP64  
434.zeusmp: -DSPEC_CPU_LP64  
435.gromacs: -DSPEC_CPU_LP64 -nofor_main  
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main  
444.namd: -DSPEC_CPU_LP64  
447.dealII: -DSPEC_CPU_LP64  
453.povray: -DSPEC_CPU_LP64  
454.calculix: -DSPEC_CPU_LP64 -nofor_main  
459.GemsFDTD: -DSPEC_CPU_LP64
```

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Peak Portability Flags (Continued)

465.tonto: -DSPEC_CPU_LP64

481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

433.milc: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -fno-alias

470.lbm: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -unroll2 -scalar-rep- -opt-prefetch
-opt-malloc-options=3

482.sphinx3: -xSSE4.1 -ipo -O3 -no-prec-div -static -unroll2

C++ benchmarks:

444.namd: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -fno-alias -auto-ilp32

447.dealII: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -unroll2 -ansi-alias -scalar-rep-

450.soplex: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -opt-malloc-options=3

453.povray: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch

416.gamess: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -unroll2 -Obo -ansi-alias
-scalar-rep-

434.zeusmp: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static

437.leslie3d: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -opt-malloc-options=3 -opt-prefetch

459.GemsFDTD: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -unroll2 -Obo -opt-prefetch

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Peak Optimization Flags (Continued)

465.tonto: -prof-gen(pass 1) -prof-use(pass 2) -xsse4.1 -ipo -O3
-no-prec-div -static -unroll4 -auto

Benchmarks using both Fortran and C:

435.gromacs: -prof-gen(pass 1) -prof-use(pass 2) -xsse4.1 -ipo -O3
-no-prec-div -static -opt-prefetch -auto-ilp32

436.cactusADM: -prof-gen(pass 1) -prof-use(pass 2) -xsse4.1 -ipo -O3
-no-prec-div -static -unroll2 -opt-prefetch -parallel
-auto-ilp32

454.calculix: -xsse4.1 -ipo -O3 -no-prec-div -static -auto-ilp32

481.wrf: basepeak = yes

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-fp-linux64-revA.20090713.15.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-fp-linux64-revA.20090713.15.xml>

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For other inquiries, please contact webmaster@spec.org.

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