



SPEC® CFP2006 Result

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Bull SAS

NovaScale B280
(Intel Xeon E5440, 2.83 GHz)

SPECfp®_rate2006 = 73.0

SPECfp_rate_base2006 = 64.8

CPU2006 license: 20

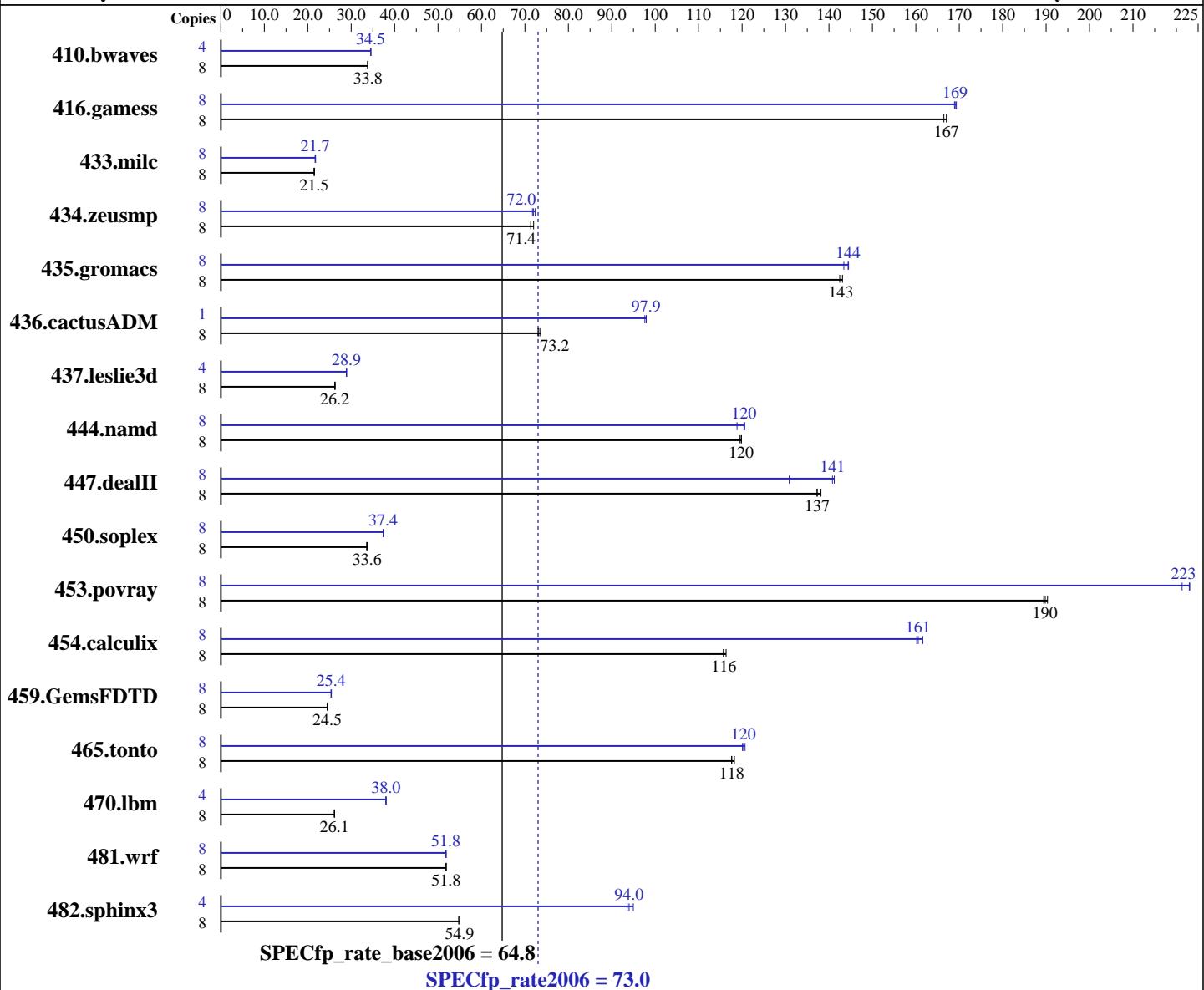
Test date: Sep-2008

Test sponsor: Bull SAS

Hardware Availability: Jan-2008

Tested by: Bull SAS

Software Availability: Nov-2007



Hardware

CPU Name: Intel Xeon E5440
CPU Characteristics: 1333 MHz system bus
CPU MHz: 2833
FPU: Integrated
CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
CPU(s) orderable: 1,2 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores

Software

Operating System: SUSE LINUX Enterprise Server 10 (x86_64) SP1
Compiler: Kernel 2.6.16.46-0.12-smp
Intel C++ and Fortran Compiler 10.1 for Linux
Build 20070913 Package ID: l_cc_p_10.1.008,
l_fc_p_10.1.008
Auto Parallel: Yes
File System: ext2
System State: Run level 3 (multi-user)
Base Pointers: 64-bit

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L3 Cache: None
Other Cache: None
Memory: 16 GB (8x2 GB) FB-DIMM PC2-5300F ECC CL5
Disk Subsystem: 1x73 GB SAS, 15000 RPM
Other Hardware: None

Peak Pointers: 32/64-bit
Other Software: Binutils 2.17.50.0.15

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	8	3216	33.8	3214	33.8	3214	33.8	4	1575	34.5	1575	34.5	1575	34.5
416.gamess	8	937	167	941	166	938	167	8	927	169	927	169	925	169
433.milc	8	3421	21.5	3418	21.5	3412	21.5	8	3369	21.8	3382	21.7	3381	21.7
434.zeusmp	8	1020	71.4	1021	71.3	1011	72.0	8	1006	72.4	1015	71.7	1011	72.0
435.gromacs	8	399	143	400	143	401	142	8	398	143	395	145	396	144
436.cactusADM	8	1306	73.2	1309	73.0	1299	73.6	1	122	97.6	122	97.9	122	97.9
437.leslie3d	8	2859	26.3	2869	26.2	2865	26.2	4	1299	28.9	1301	28.9	1301	28.9
444.namd	8	535	120	537	120	535	120	8	532	121	533	120	540	119
447.dealII	8	662	138	667	137	667	137	8	650	141	648	141	699	131
450.soplex	8	1983	33.6	1983	33.6	1987	33.6	8	1783	37.4	1785	37.4	1786	37.4
453.povray	8	224	190	225	189	224	190	8	191	223	191	223	192	221
454.calculix	8	567	116	571	116	570	116	8	412	160	411	161	408	162
459.GemsFDTD	8	3454	24.6	3471	24.5	3467	24.5	8	3347	25.4	3336	25.4	3345	25.4
465.tonto	8	666	118	669	118	670	118	8	654	120	656	120	652	121
470.lbm	8	4205	26.1	4204	26.1	4210	26.1	4	1445	38.0	1448	38.0	1445	38.0
481.wrf	8	1724	51.8	1724	51.8	1723	51.9	8	1722	51.9	1726	51.8	1727	51.8
482.sphinx3	8	2836	55.0	2838	54.9	2849	54.7	4	829	94.0	833	93.5	821	95.0

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
taskset was used to bind processes to cores except
for 436.cactusADM peak

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
OMP_NUM_THREADS set to number of cores
KMP_AFFINITY set to physical,0
KMP_STACKSIZE set to 64M



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Platform Notes

BIOS configuration:

Hardware Prefetcher Enabled

Adjacent Cache-Line Prefetch Disabled

General Notes

All benchmarks compiled in 64-bit mode except 437.leslie3d, 450.soplex, 470.lbm and 482.sphinx3, at peak, are compiled in 32-bit mode

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icc ifort

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64



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Base Optimization Flags

C benchmarks:
-fast

C++ benchmarks:
-fast

Fortran benchmarks:
-fast

Benchmarks using both Fortran and C:
-fast

Peak Compiler Invocation

C benchmarks (except as noted below):

```
/opt/intel/cc/10.1.008/bin/icc -L/opt/intel/cc/10.1.008/lib  
-I/opt/intel/cc/10.1.008/include
```

433.milc: icc

C++ benchmarks (except as noted below):

```
icpc
```

```
450.soplex: /opt/intel/cc/10.1.008/bin/icpc -L/opt/intel/cc/10.1.008/lib  
-I/opt/intel/cc/10.1.008/include
```

Fortran benchmarks (except as noted below):

```
ifort
```

```
437.leslie3d: /opt/intel/fc/10.1.008/bin/ifort -L/opt/intel/fc/10.1.008/lib  
-I/opt/intel/fc/10.1.008/include
```

Benchmarks using both Fortran and C:

```
icc ifort
```

Peak Portability Flags

```
410.bwaves: -DSPEC_CPU_LP64  
416.gamess: -DSPEC_CPU_LP64  
    433.milc: -DSPEC_CPU_LP64  
434.zeusmp: -DSPEC_CPU_LP64  
435.gromacs: -DSPEC_CPU_LP64 -nofor_main  
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main  
    444.namd: -DSPEC_CPU_LP64  
447.dealII: -DSPEC_CPU_LP64
```

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Peak Portability Flags (Continued)

```
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
```

Peak Optimization Flags

C benchmarks:

```
433.milc: -prof-gen(pass 1) -prof-use(pass 2) -fast -fno-alias
           -auto-ilp32
```

```
470.lbm: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
           -scalar-rep -prefetch -opt-malloc-options=3
```

```
482.sphinx3: -fast -unroll2
```

C++ benchmarks:

```
444.namd: -prof-gen(pass 1) -prof-use(pass 2) -fast -fno-alias
           -auto-ilp32
```

```
447.dealII: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
           -ansi-alias -scalar-rep-
```

```
450.soplex: -prof-gen(pass 1) -prof-use(pass 2) -fast
           -opt-malloc-options=3
```

```
453.povray: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4
           -ansi-alias
```

Fortran benchmarks:

```
410.bwaves: -fast -prefetch
```

```
416.gamess: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2 -O0
           -ansi-alias -scalar-rep-
```

```
434.zeusmp: -prof-gen(pass 1) -prof-use(pass 2) -fast
```

```
437.leslie3d: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch
           -opt-malloc-options=3
```

```
459.GemsFDTD: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2 -O0
           -prefetch
```

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Peak Optimization Flags (Continued)

465.tonto: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll14 -auto

Benchmarks using both Fortran and C:

435.gromacs: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch
-auto-ilp32

436.cactusADM: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll12
-prefetch -parallel -auto-ilp32

454.calculix: -fast -unroll-aggressive -auto-ilp32

481.wrf: -fast -auto-ilp32

The flags file that was used to format this result can be browsed at

http://www.spec.org/cpu2006/flags/EM64T_Intel101_fp_flags.20090713.html

You can also download the XML flags source by saving the following link:

http://www.spec.org/cpu2006/flags/EM64T_Intel101_fp_flags.20090713.xml

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For other inquiries, please contact webmaster@spec.org.

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