



# SPEC® CFP2006 Result

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## Fujitsu Siemens Computers

### SPECfp®\_rate2006 = 59.2

### CELSIUS R650, Intel Xeon X5270 processor

### SPECfp\_rate\_base2006 = 56.8

CPU2006 license: 22

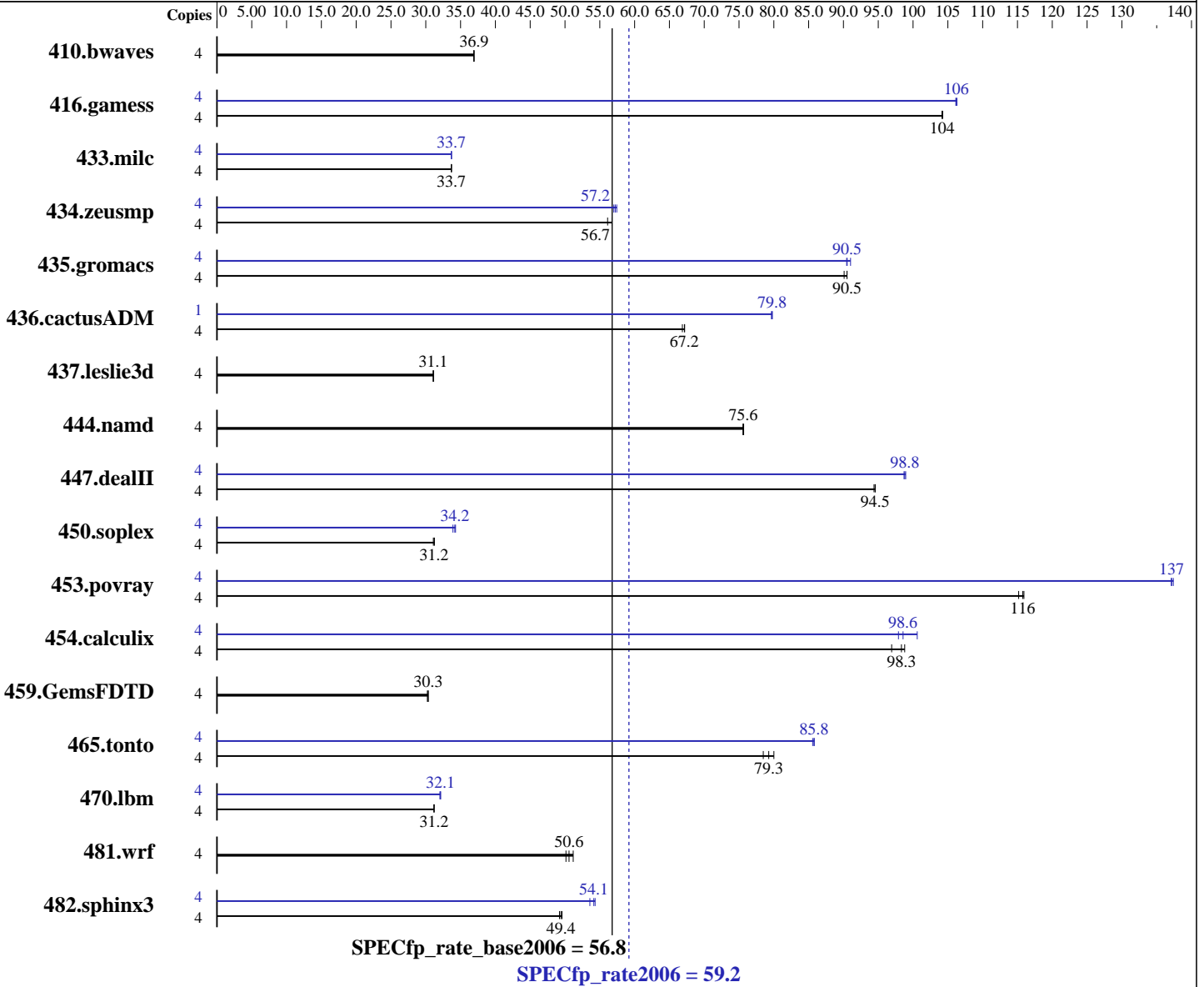
Test sponsor: Fujitsu Siemens Computers

Tested by: Fujitsu Siemens Computers

Test date: Sep-2008

Hardware Availability: Oct-2008

Software Availability: Nov-2008



#### Hardware

CPU Name: Intel Xeon X5270  
 CPU Characteristics:  
 CPU MHz: 3500  
 FPU: Integrated  
 CPU(s) enabled: 4 cores, 2 chips, 2 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 6 MB I+D on chip per chip

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#### Software

Operating System: SuSE Linux Enterprise Server 10 (x86\_64) SP2  
 kernel 2.6.16.60-0.21-smp  
 Compiler: Intel C++ and Fortran Compiler 11.0 for Linux  
 Build 20080730  
 Package ID l\_cproc\_b\_11.0.042, l\_fproc\_b\_11.0.042  
 Auto Parallel: Yes  
 File System: ext3  
 System State: Multi-User, Run Level 3  
 Base Pointers: 64-bit

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L3 Cache: None  
Other Cache: None  
Memory: 8 GB (8x1 GB DDR2 5300F, 2 rank, CL5-5-5, ECC)  
Disk Subsystem: 1 x SATA II, 400 GB, 7200 rpm  
Other Hardware: None

Peak Pointers: 32/64-bit  
Other Software: Binutils 2.18.50.0.7.20080502

### Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	4	1474	36.9	1472	36.9	<u>1472</u>	<u>36.9</u>	4	1474	36.9	1472	36.9	<u>1472</u>	<u>36.9</u>
416.gamess	4	751	104	<u>752</u>	<u>104</u>	752	104	4	737	106	738	106	<u>737</u>	<u>106</u>
433.milc	4	<u>1090</u>	<u>33.7</u>	1091	33.7	1090	33.7	4	1091	33.7	1090	33.7	<u>1090</u>	<u>33.7</u>
434.zeusmp	4	<u>641</u>	<u>56.7</u>	641	56.8	649	56.1	4	638	57.0	<u>636</u>	<u>57.2</u>	634	57.4
435.gromacs	4	<u>316</u>	<u>90.5</u>	317	90.1	316	90.5	4	314	91.0	316	90.5	<u>315</u>	<u>90.5</u>
436.cactusADM	4	715	66.9	711	67.2	<u>712</u>	<u>67.2</u>	1	150	79.8	<u>150</u>	<u>79.8</u>	150	79.7
437.leslie3d	4	<u>1210</u>	<u>31.1</u>	1209	31.1	1210	31.1	4	<u>1210</u>	<u>31.1</u>	1209	31.1	1210	31.1
444.namd	4	424	75.6	424	75.7	<u>424</u>	<u>75.6</u>	4	424	75.6	424	75.7	<u>424</u>	<u>75.6</u>
447.dealII	4	<u>484</u>	<u>94.5</u>	485	94.4	484	94.6	4	464	98.7	462	98.9	<u>463</u>	<u>98.8</u>
450.soplex	4	1073	31.1	<u>1069</u>	<u>31.2</u>	1068	31.2	4	984	33.9	973	34.3	<u>976</u>	<u>34.2</u>
453.povray	4	185	115	184	116	<u>184</u>	<u>116</u>	4	155	137	155	137	<u>155</u>	<u>137</u>
454.calculix	4	<u>336</u>	<u>98.3</u>	340	96.9	334	98.8	4	<u>335</u>	<u>98.6</u>	328	101	337	97.9
459.GemsFDTD	4	1398	30.3	1404	30.2	<u>1398</u>	<u>30.3</u>	4	1398	30.3	1404	30.2	<u>1398</u>	<u>30.3</u>
465.tonto	4	501	78.5	492	80.0	<u>497</u>	<u>79.3</u>	4	460	85.6	459	85.8	<u>459</u>	<u>85.8</u>
470.lbm	4	1762	31.2	<u>1762</u>	<u>31.2</u>	1762	31.2	4	<u>1711</u>	<u>32.1</u>	1711	32.1	1716	32.0
481.wrf	4	873	51.2	<u>883</u>	<u>50.6</u>	891	50.1	4	873	51.2	<u>883</u>	<u>50.6</u>	891	50.1
482.sphinx3	4	1573	49.6	1584	49.2	<u>1578</u>	<u>49.4</u>	4	1435	54.3	<u>1440</u>	<u>54.1</u>	1455	53.6

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

### Submit Notes

The config file option 'submit' was used.  
taskset was used to bind processes to cores except  
for 436.cactusADM peak

### Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run  
OMP\_NUM\_THREADS set to number of processors  
KMP\_AFFINITY set to "physical,0"  
KMP\_STACKSIZE set to 64M



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## Platform Notes

BIOS configuration:  
Enhanced Speedstep Technology = Disable  
C1 Enhanced Mode = Disable  
Adjacent Sector Prefetch = Disable  
SnoopFilter = Enable

## General Notes

For information about Fujitsu Siemens Computers please see:  
<http://www.fujitsu-siemens.com>

## Base Compiler Invocation

C benchmarks:

icc

C++ benchmarks:

icpc

Fortran benchmarks:

ifort

Benchmarks using both Fortran and C:

icc ifort

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.leslie3d: -DSPEC\_CPU\_LP64  
444.namd: -DSPEC\_CPU\_LP64  
447.dealII: -DSPEC\_CPU\_LP64  
450.soplex: -DSPEC\_CPU\_LP64  
453.povray: -DSPEC\_CPU\_LP64  
454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
459.GemsFDTD: -DSPEC\_CPU\_LP64  
465.tonto: -DSPEC\_CPU\_LP64  
470.lbm: -DSPEC\_CPU\_LP64  
481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
482.sphinx3: -DSPEC\_CPU\_LP64



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## Base Optimization Flags

C benchmarks:

-xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch

C++ benchmarks:

-xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch

Fortran benchmarks:

-xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch

Benchmarks using both Fortran and C:

-xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc

482.sphinx3: /opt/intel/Compiler/11.0/042/bin/ia32/icc  
-L/opt/intel/Compiler/11.0/042/ipp/ia32/lib  
-I/opt/intel/Compiler/11.0/042/ipp/ia32/include

C++ benchmarks (except as noted below):

icpc

450.soplex: /opt/intel/Compiler/11.0/042/bin/ia32/icpc  
-L/opt/intel/Compiler/11.0/042/ipp/ia32/lib  
-I/opt/intel/Compiler/11.0/042/ipp/ia32/include

Fortran benchmarks:

ifort

Benchmarks using both Fortran and C:

icc ifort

## Peak Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.leslie3d: -DSPEC\_CPU\_LP64  
444.namd: -DSPEC\_CPU\_LP64  
447.dealII: -DSPEC\_CPU\_LP64

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## Peak Portability Flags (Continued)

453.povray: -DSPEC\_CPU\_LP64  
 454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
 459.GemsFDTD: -DSPEC\_CPU\_LP64  
 465.tonto: -DSPEC\_CPU\_LP64  
 470.lbm: -DSPEC\_CPU\_LP64  
 481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

433.milc: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3  
 -no-prec-div -static -fno-alias

470.lbm: -xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch  
 -auto-ilp32

482.sphinx3: -xSSE4.1 -ipo -O3 -no-prec-div -static -unroll2

C++ benchmarks:

444.namd: basepeak = yes

447.dealIII: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3  
 -no-prec-div -static -unroll2 -ansi-alias -scalar-rep-

450.soplex: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3  
 -no-prec-div -static -opt-malloc-options=3

453.povray: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3  
 -no-prec-div -static -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3  
 -no-prec-div -static -unroll2 -Ob0 -ansi-alias  
 -scalar-rep-

434.zeusmp: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3  
 -no-prec-div -static

437.leslie3d: basepeak = yes

459.GemsFDTD: basepeak = yes

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## Peak Optimization Flags (Continued)

465.tonto: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3  
-no-prec-div -static -unroll4 -auto

Benchmarks using both Fortran and C:

435.gromacs: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3  
-no-prec-div -static -opt-prefetch -auto-ilp32

436.cactusADM: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3  
-no-prec-div -static -unroll2 -opt-prefetch -parallel  
-auto-ilp32

454.calculix: -xSSE4.1 -ipo -O3 -no-prec-div -static -auto-ilp32

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-fp-linux64-revA.20090713.11.html>

<http://www.spec.org/cpu2006/flags/FSC-SLES10-Platform.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-fp-linux64-revA.20090713.11.xml>

<http://www.spec.org/cpu2006/flags/FSC-SLES10-Platform.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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