



SPEC® CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Intel Corporation
Lenovo Thinkpad T60

SPECfp®_rate2006 = 16.6
SPECfp_rate_base2006 = 16.4

CPU2006 license: 13

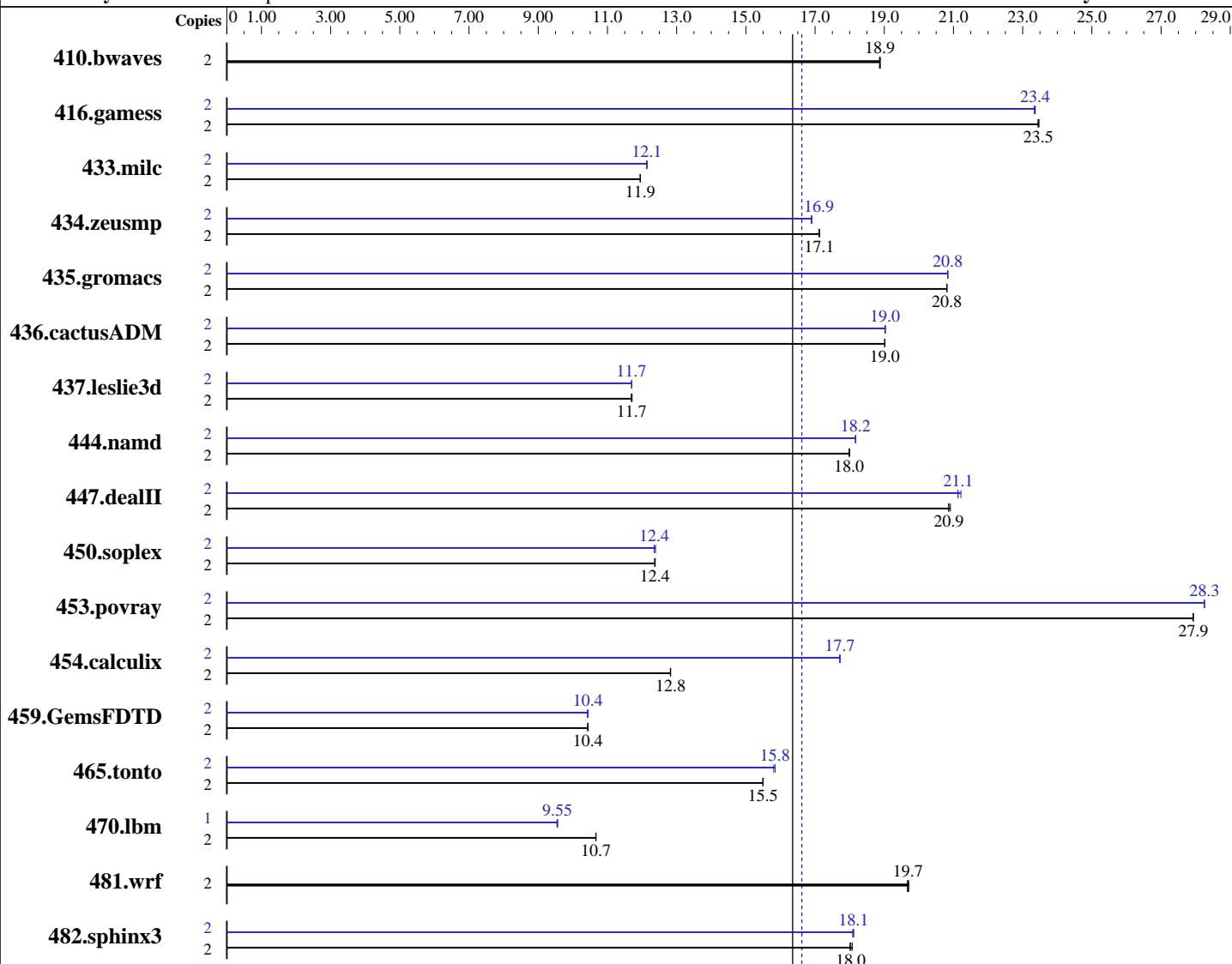
Test sponsor: Intel Corporation

Tested by: Intel Corporation

Test date: Aug-2008

Hardware Availability: Sep-2006

Software Availability: Nov-2007



SPECfp_rate_base2006 = 16.4

SPECfp_rate2006 = 16.6

Hardware

CPU Name: Intel Core Duo T2700
CPU Characteristics:
CPU MHz: 2333
FPU: Integrated
CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip
CPU(s) orderable: 1 chip
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 2 MB I+D on chip per chip

Software

Operating System: Windows XP Professional SP2
Compiler: Intel C++ Compiler for IA32 version 10.1
Build 20070913 Package ID: w_cc_p_10.1.011
Intel Fortran Compiler for IA32 version 10.1
Build 20070913 Package ID: w_fc_p_10.1.011
Microsoft Visual Studio 2005 SP1 (for libraries)
Auto Parallel: No
File System: NTFS
System State: Default

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L3 Cache: None
 Other Cache: None
 Memory: 2 GB (2x1GB Micron PC2-5300 DDR2-667 CL5)
 Disk Subsystem: Hitachi Travelstar HTS721010G9SA00 SATA 100GB 7200RPM
 Other Hardware: None

Base Pointers: 32-bit
 Peak Pointers: 32-bit
 Other Software: SmartHeap Library Version 8.1 from <http://www.microquill.com/>

Results Table

Benchmark	Base								Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	2	1440	18.9	1441	18.9	1439	18.9	2	1440	18.9	1441	18.9	1439	18.9		
416.gamess	2	1670	23.4	1670	23.5	1668	23.5	2	1676	23.4	1676	23.4	1678	23.3		
433.milc	2	1536	12.0	1537	11.9	1536	11.9	2	1512	12.1	1512	12.1	1513	12.1		
434.zeusmp	2	1063	17.1	1063	17.1	1063	17.1	2	1077	16.9	1077	16.9	1077	16.9		
435.gromacs	2	686	20.8	686	20.8	686	20.8	2	685	20.8	685	20.8	686	20.8		
436.cactusADM	2	1257	19.0	1257	19.0	1256	19.0	2	1256	19.0	1256	19.0	1256	19.0		
437.leslie3d	2	1606	11.7	1608	11.7	1605	11.7	2	1607	11.7	1607	11.7	1606	11.7		
444.namd	2	891	18.0	892	18.0	892	18.0	2	882	18.2	883	18.2	883	18.2		
447.dealII	2	1097	20.9	1094	20.9	1097	20.9	2	1083	21.1	1078	21.2	1083	21.1		
450.soplex	2	1349	12.4	1348	12.4	1347	12.4	2	1346	12.4	1350	12.4	1350	12.4		
453.povray	2	381	27.9	381	27.9	381	27.9	2	377	28.3	377	28.3	377	28.3		
454.calculix	2	1286	12.8	1287	12.8	1287	12.8	2	931	17.7	931	17.7	931	17.7		
459.GemsFDTD	2	2034	10.4	2034	10.4	2033	10.4	2	2033	10.4	2036	10.4	2034	10.4		
465.tonto	2	1270	15.5	1271	15.5	1270	15.5	2	1242	15.8	1242	15.8	1245	15.8		
470.lbm	2	2576	10.7	2576	10.7	2576	10.7	1	1438	9.55	1440	9.54	1437	9.56		
481.wrf	2	1136	19.7	1135	19.7	1134	19.7	2	1136	19.7	1135	19.7	1134	19.7		
482.sphinx3	2	2157	18.1	2165	18.0	2161	18.0	2	2155	18.1	2152	18.1	2152	18.1		

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

Binaries were built on Windows Vista32

The following VS 2005 SP1 updates were applied: KB926601 and KB932232

Base Compiler Invocation

C benchmarks:

 icl -Qvc8 -Qc99

C++ benchmarks:

 icl -Qvc8

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Base Compiler Invocation (Continued)

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icl -Qvc8 -Qc99 ifort

Base Portability Flags

436.cactusADM: -Qlowercase /assume:underscore
444.namd: -TP
447.deallII: -DDEAL_I_I_MEMBER_VAR_SPECIALIZATION_BUG
453.povray: -DSPEC_CPU_WINDOWS_ICL
454.calculix: -DSPEC_CPU_NOZMODIFIER -Qlowercase
481.wrf: -DSPEC_CPU_WINDOWS_ICL

Base Optimization Flags

C benchmarks:
-QxP -Qipo -O3 -Qprec-div- /F1000000000

C++ benchmarks:
-QxP -Qipo -O3 -Qprec-div- -Qcxx_features /F1000000000 shlw32m.lib
-link /FORCE:MULTIPLE

Fortran benchmarks:
-QxP -Qipo -O3 -Qprec-div- /F1000000000

Benchmarks using both Fortran and C:
-QxP -Qipo -O3 -Qprec-div- /F1000000000

Peak Compiler Invocation

C benchmarks:
icl -Qvc8 -Qc99

C++ benchmarks:
icl -Qvc8

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icl -Qvc8 -Qc99 ifort



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Peak Portability Flags

```
436.cactusADM: -Qlowercase /assume:underscore
 444.namd: -TP
 447.dealII: -DDEAL_II_MEMBER_VAR_SPECIALIZATION_BUG
 453.povray: -DSPEC_CPU_WINDOWS_ICL
 454.calculix: -DSPEC_CPU_NOZMODIFIER -Qlowercase
 481.wrf: -DSPEC_CPU_WINDOWS_ICL
```

Peak Optimization Flags

C benchmarks:

```
433.milc: -QxP -Qipo -O3 -Qprec-div- -Qunroll12 -Oa /F1000000000
470.lbm: -QxP -Qipo -O3 -Qprec-div- -Qunroll12 -Qscalar-rep-
          -Qprefetch /F1000000000
482.sphinx3: -QxP -Qipo -O3 -Qprec-div- -Qunroll12 /F1000000000
```

C++ benchmarks:

```
444.namd: -QxP -Qipo -O3 -Qprec-div- -Oa -Qcxx_features
          /F1000000000 shlw32m.lib           -link /FORCE:MULTIPLE
447.dealII: -QxP -Qipo -O3 -Qprec-div- -Qunroll12 -Qprefetch
          -Qcxx_features /F1000000000 shlw32m.lib
          -link /FORCE:MULTIPLE
450.soplex: -QxP -Qipo -O3 -Qprec-div- -Qcxx_features /F1000000000
          shlw32m.lib           -link /FORCE:MULTIPLE
453.povray: -QxP -Qipo -O3 -Qprec-div- -Qunroll14 -Qansi-alias
          -Qcxx_features /F1000000000 shlw32m.lib
          -link /FORCE:MULTIPLE
```

Fortran benchmarks:

```
410.bwaves: basepeak = yes
416.gamess: -QxP -Qipo -O3 -Qprec-div- -Qunroll12 -Ob0 -Qansi-alias
          -Qscalar-rep- /F1000000000
434.zeusmp: -QxP -O2 -Qprec-div- -Qunroll10 -Qscalar-rep- /F1000000000
437.leslie3d: -QxP -Qipo -O3 -Qprec-div- -Qprefetch /F1000000000
459.GemsFDTD: -QxP -Qipo -O3 -Qprec-div- -Qunroll12 -Ob0 -Qprefetch
          /F1000000000
```

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Peak Optimization Flags (Continued)

465.tonto: -QxP -Qipo -O3 -Qprec-div- -Qunroll14 -Qauto /F1000000000

Benchmarks using both Fortran and C:

435.gromacs: -QxP -Qipo -O3 -Qprec-div- -Oa -Qprefetch /F1000000000

436.cactusADM: -QxP -Qipo -O3 -Qprec-div- -Qunroll12 -Qprefetch
/F1000000000

454.calculix: -QxP -Qipo -O3 -Qprec-div- -Qunroll-aggressive
/F1000000000

481.wrf: basepeak = yes

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic10.1-win32-revC.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic10.1-win32-revC.xml>

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For other inquiries, please contact webmaster@spec.org.

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