



# SPEC® CFP2006 Result

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Intel Corporation

**SPECfp®2006 = 16.4**

Lenovo Thinkpad T61

**SPECfp\_base2006 = 15.9**

CPU2006 license: 13

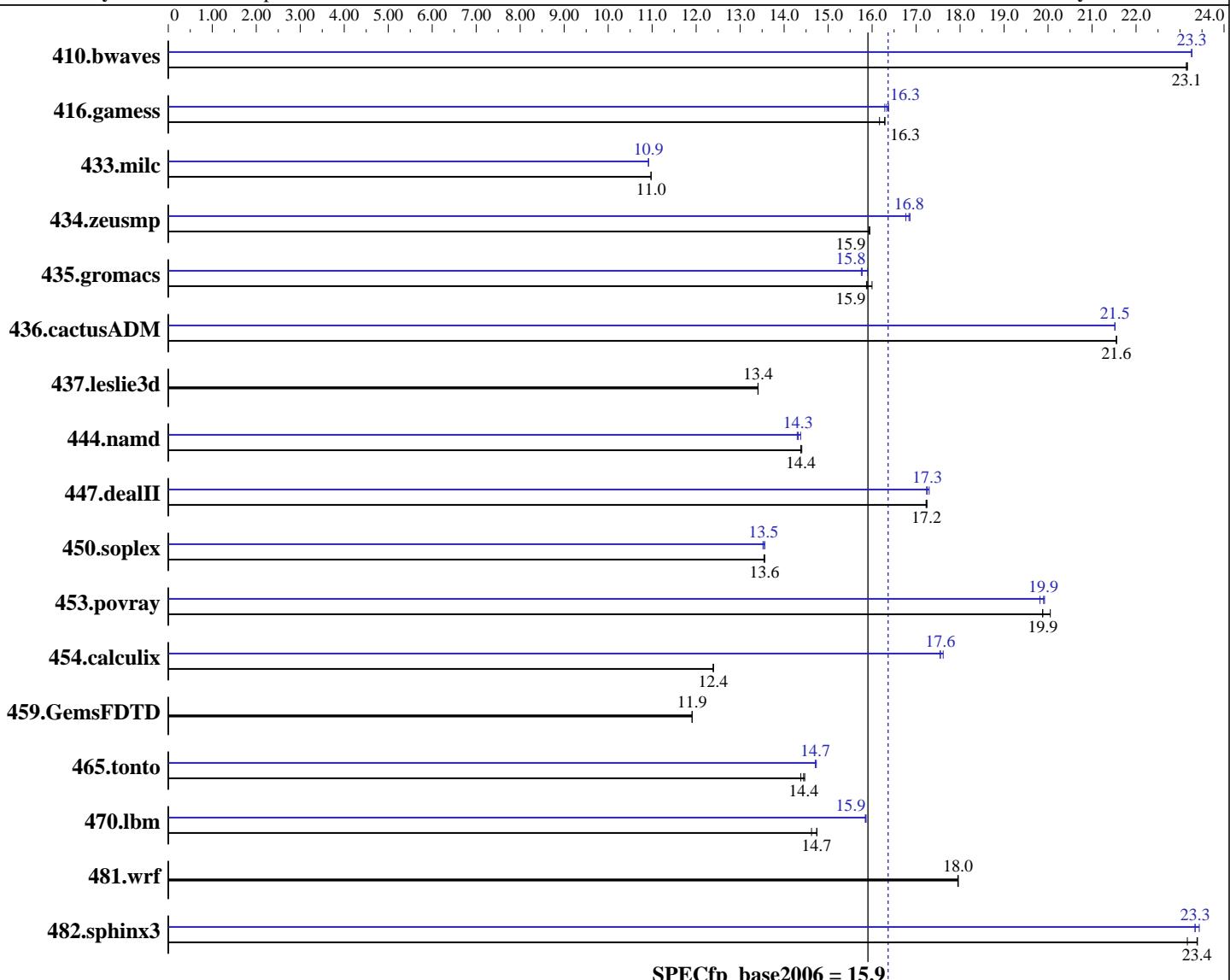
Test date: Aug-2008

Test sponsor: Intel Corporation

Hardware Availability: Sep-2007

Tested by: Intel Corporation

Software Availability: Nov-2007



## Hardware

CPU Name: Intel Core 2 Duo T7800  
 CPU Characteristics:  
 CPU MHz: 2600  
 FPU: Integrated  
 CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip  
 CPU(s) orderable: 1 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 4 MB I+D on chip per chip

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## Software

Operating System: Windows XP Professional SP2  
 Compiler: Intel C++ Compiler for IA32 version 10.1  
 Build 20070913 Package ID: w\_cc\_p\_10.1.011  
 Intel Fortran Compiler for IA32 version 10.1  
 Build 20070913 Package ID: w\_fc\_p\_10.1.011  
 Microsoft Visual Studio 2005 SP1 (for libraries)  
 Auto Parallel: Yes  
 File System: NTFS  
 System State: Default

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L3 Cache: None  
 Other Cache: None  
 Memory: 2 GB (2x1GB Micron PC2-6400 DDR2-800 CL5)  
 Disk Subsystem: Hitachi Travelstar HTS721010G9SA00 SATA 100GB 7200RPM  
 Other Hardware: None

Base Pointers: 32-bit  
 Peak Pointers: 32-bit  
 Other Software: SmartHeap Library Version 8.1 from <http://www.microquill.com/>

## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	587	23.2	<b>587</b>	<b>23.1</b>	587	23.1	584	23.3	<b>584</b>	<b>23.3</b>	584	23.3
416.gamess	<b>1202</b>	<b>16.3</b>	1211	16.2	1202	16.3	1202	16.3	<b>1199</b>	<b>16.3</b>	1196	16.4
433.milc	<b>836</b>	<b>11.0</b>	836	11.0	837	11.0	841	10.9	<b>841</b>	<b>10.9</b>	841	10.9
434.zeusmp	<b>571</b>	<b>15.9</b>	571	15.9	571	15.9	543	16.8	540	16.9	<b>540</b>	<b>16.8</b>
435.gromacs	450	15.9	<b>449</b>	<b>15.9</b>	446	16.0	449	15.9	453	15.8	<b>453</b>	<b>15.8</b>
436.cactusADM	554	21.6	<b>554</b>	<b>21.6</b>	554	21.6	555	21.5	555	21.5	<b>555</b>	<b>21.5</b>
437.leslie3d	701	13.4	<b>701</b>	<b>13.4</b>	701	13.4	701	13.4	<b>701</b>	<b>13.4</b>	701	13.4
444.namd	<b>558</b>	<b>14.4</b>	558	14.4	557	14.4	558	14.4	<b>560</b>	<b>14.3</b>	561	14.3
447.dealII	664	17.2	663	17.2	<b>664</b>	<b>17.2</b>	661	17.3	<b>663</b>	<b>17.3</b>	664	17.2
450.soplex	<b>615</b>	<b>13.6</b>	616	13.5	615	13.6	<b>615</b>	<b>13.6</b>	617	13.5	<b>616</b>	<b>13.5</b>
453.povray	<b>268</b>	<b>19.9</b>	268	19.9	265	20.1	268	19.8	<b>267</b>	<b>19.9</b>	267	19.9
454.calculix	<b>666</b>	<b>12.4</b>	666	12.4	666	12.4	<b>468</b>	<b>17.6</b>	470	17.5	<b>470</b>	<b>17.6</b>
459.GemsFDTD	891	11.9	<b>891</b>	<b>11.9</b>	891	11.9	891	11.9	<b>891</b>	<b>11.9</b>	891	11.9
465.tonto	684	14.4	<b>681</b>	<b>14.4</b>	680	14.5	<b>668</b>	<b>14.7</b>	669	14.7	<b>669</b>	<b>14.7</b>
470.lbm	940	14.6	932	14.7	<b>932</b>	<b>14.7</b>	867	15.8	<b>866</b>	<b>15.9</b>	866	15.9
481.wrf	622	17.9	622	18.0	<b>622</b>	<b>18.0</b>	622	17.9	622	18.0	<b>622</b>	<b>18.0</b>
482.sphinx3	841	23.2	833	23.4	<b>833</b>	<b>23.4</b>	832	23.4	835	23.3	<b>835</b>	<b>23.3</b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## General Notes

Binaries were built on Windows Vista32

The following VS 2005 SP1 updates were applied: KB926601 and KB932232

## Base Compiler Invocation

C benchmarks:

icl -Qvc8 -Qc99

C++ benchmarks:

icl -Qvc8

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## Peak Portability Flags

436.cactusADM: -Qlowercase /assume:underscore  
444.namd: -TP  
447.dealII: -DDEAL\_II\_MEMBER\_VAR\_SPECIALIZATION\_BUG  
453.povray: -DSPEC\_CPU\_WINDOWS\_ICL  
454.calculix: -DSPEC\_CPU\_NOZMODIFIER -Qlowercase  
481.wrf: -DSPEC\_CPU\_WINDOWS\_ICL

## Peak Optimization Flags

C benchmarks:

433.milc: -fast -Qunroll12 -Oa /F1000000000 libguide40.lib  
470.lbm: -fast -Qunroll12 -Qscalar-rep- -Qprefetch /F1000000000 libguide40.lib  
482.sphinx3: -fast -Qunroll12 /F1000000000 libguide40.lib

C++ benchmarks:

444.namd: -fast -Oa -Qcxx\_features /F1000000000 shlw32m.lib  
libguide40.lib -link /FORCE:MULTIPLE  
447.dealII: -fast -Qunroll12 -Qprefetch -Qcxx\_features /F1000000000  
shlw32m.lib libguide40.lib -link /FORCE:MULTIPLE  
450.soplex: -fast -Qparallel -Qcxx\_features /F1000000000 shlw32m.lib  
libguide40.lib -link /FORCE:MULTIPLE  
453.povray: -fast -Qunroll14 -Qcxx\_features /F1000000000 shlw32m.lib  
libguide40.lib -link /FORCE:MULTIPLE

Fortran benchmarks:

410.bwaves: -fast -Qparallel -Qprefetch /F1000000000 libguide40.lib  
416.gamess: -fast -Qunroll12 -Ob0 -Qansi-alias -Qscalar-rep-  
/F1000000000 libguide40.lib  
434.zeusmp: -QxT -O2 -Qprec-div- -Qunroll10 -Qscalar-rep- /F1000000000  
libguide40.lib  
437.leslie3d: basepeak = yes  
459.GemsFDTD: basepeak = yes  
465.tonto: -fast -Qunroll14 -Qauto /F1000000000 libguide40.lib

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## Peak Optimization Flags (Continued)

Benchmarks using both Fortran and C:

435.gromacs: -fast -Oa -Qprefetch /F1000000000 libguide40.lib

436.cactusADM: -fast -Qunroll12 -Qparallel -Qprefetch /F1000000000 libguide40.lib

454.calculix: -fast -Qunroll-aggressive /F1000000000 libguide40.lib

481.wrf: basepeak = yes

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic10.1-win32-revC.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic10.1-win32-revC.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.0.

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