SPEC® CINT2006 Result

Hewlett-Packard Company

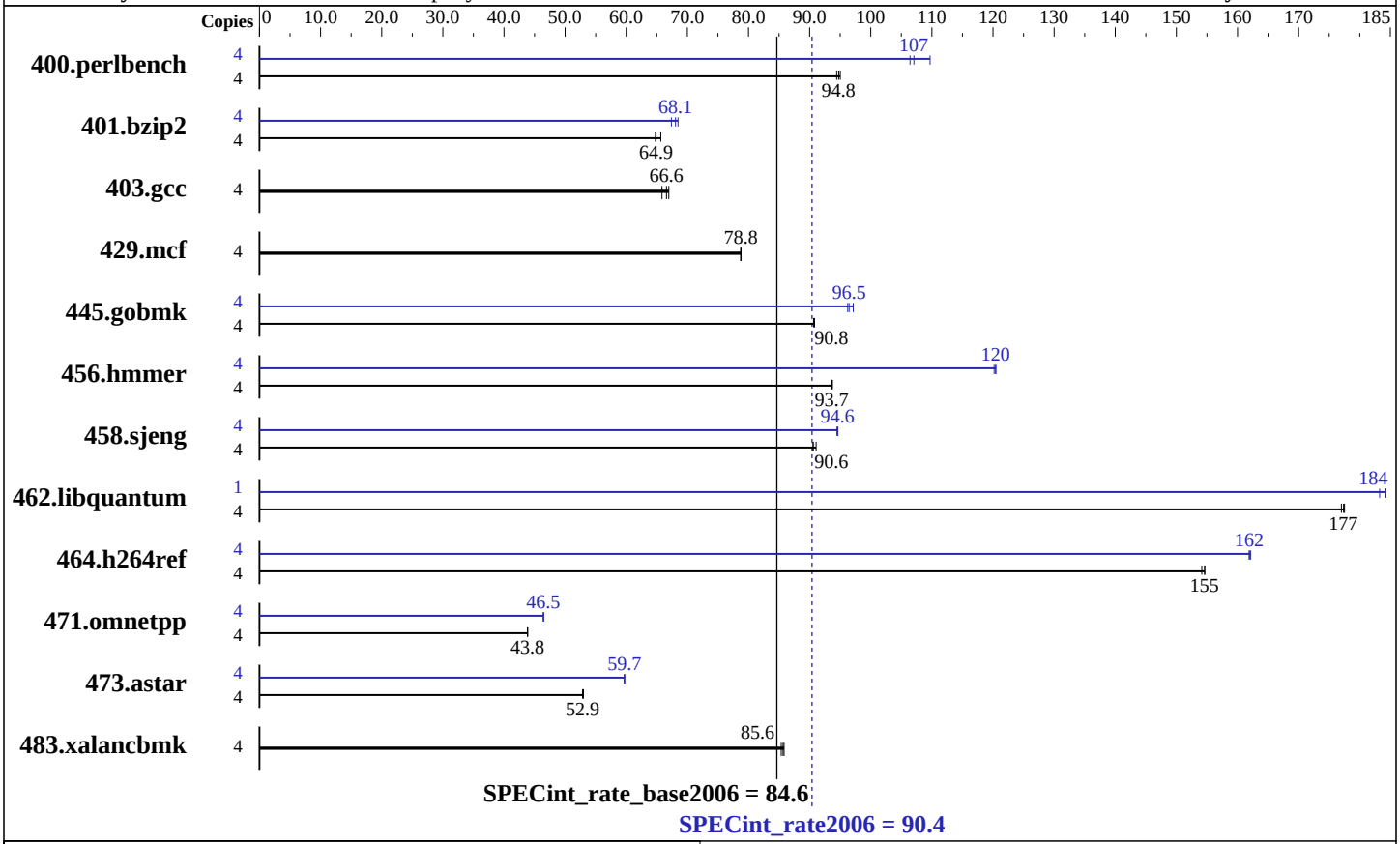
ProLiant BL460c
(3.5 GHz, Intel Xeon X5270)

SPECint®_rate2006 = 90.4

SPECint_rate_base2006 = 84.6

CPU2006 license: 3
Test sponsor: Hewlett-Packard Company
Tested by: Hewlett-Packard Company

Test date: Aug-2008
Hardware Availability: Sep-2008
Software Availability: Nov-2008



Hardware

CPU Name:	Intel Xeon X5270
CPU Characteristics:	3.5 GHz, 6 MB L2 shared, 1333 MHz system bus
CPU MHz:	3500
FPU:	Integrated
CPU(s) enabled:	4 cores, 2 chips, 2 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	6 MB I+D on chip per chip
L3 Cache:	None
Other Cache:	None
Memory:	16 GB (8x2 GB PC2-5300F CL5)
Disk Subsystem:	2x72 GB 10 K SAS
Other Hardware:	None

Software

Operating System:	SUSE Linux Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smp
Compiler:	Intel C++ Compiler 11.0 for Linux Build 20080730 Package ID: l_cproc_b_11.0.042
Auto Parallel:	Yes
File System:	ext3
System State:	Run level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	MicroQuill SmartHeap Library 8.1 Binutils 2.18.50.0.7.20080502

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	**412**	**94.8**	414	94.4	411	95.0	4	**365**	**107**	367	106	356	110
401.bzip2	4	596	64.8	**595**	**64.9**	588	65.7	4	**567**	**68.1**	573	67.4	564	68.5
403.gcc	4	481	67.0	**484**	**66.6**	489	65.9	4	481	67.0	**484**	**66.6**	489	65.9
429.mcf	4	463	78.7	463	78.8	**463**	**78.8**	4	463	78.7	463	78.8	**463**	**78.8**
445.gobmk	4	**462**	**90.8**	462	90.8	463	90.6	4	**435**	**96.5**	436	96.2	432	97.1
456.hmmer	4	399	93.6	**398**	**93.7**	398	93.7	4	**310**	**120**	311	120	310	120
458.sjeng	4	534	90.6	**534**	**90.6**	532	91.1	4	512	94.6	**512**	**94.6**	512	94.5
462.libquantum	4	467	178	468	177	**467**	**177**	1	113	183	**112**	**184**	112	184
464.h264ref	4	**572**	**155**	574	154	572	155	4	**547**	**162**	547	162	546	162
471.omnetpp	4	570	43.9	570	43.8	**570**	**43.8**	4	**538**	**46.5**	538	46.5	538	46.4
473.astar	4	530	53.0	**531**	**52.9**	531	52.9	4	470	59.7	470	59.7	**470**	**59.7**
483.xalancbmk	4	322	85.8	323	85.4	**322**	**85.6**	4	322	85.8	323	85.4	**322**	**85.6**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The config file option 'submit' was used.
 taskset was used to bind processes to cores except
 for 462.libquantum peak
```

Operating System Notes

```
 'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
 OMP_NUM_THREADS set to number of cores
 KMP_AFFINITY set to physical,0
 KMP_STACKSIZE set to 64M
```

Platform Notes

```
BIOS configuration:
  Power Regulator set to Static High Performance Mode
  Adjacent Sector Prefetch Disabled
```

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-xSSE4.1 -ipo -O3 -no-prec-div -static -inline-calloc
-opt-malloc-options=3 -opt-prefetch

C++ benchmarks:
-xSSE4.1 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/spec/cpu2006.1.1/lib -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc

401.bzip2: /opt/intel/Compiler/11.0/042/bin/intel64/icc
-L/opt/intel/Compiler/11.0/042/ipp/em64t/lib
-I/opt/intel/Compiler/11.0/042/ipp/em64t/include

456.hmmer: /opt/intel/Compiler/11.0/042/bin/intel64/icc
-L/opt/intel/Compiler/11.0/042/ipp/em64t/lib
-I/opt/intel/Compiler/11.0/042/ipp/em64t/include

C++ benchmarks:
icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64

Continued on next page

Peak Portability Flags (Continued)

462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -ansi-alias -opt-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -opt-prefetch -ansi-alias

403.gcc: basepeak = yes

429.mcf: basepeak = yes

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -O2 -ipo
-no-prec-div -ansi-alias

456.hmmer: -xSSE4.1 -ipo -O3 -no-prec-div -static -unroll2
-ansi-alias

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -unroll4

462.libquantum: -xSSE4.1 -ipo -O3 -no-prec-div -static
-opt-malloc-options=3 -parallel -par-runtime-control
-opt-prefetch

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -ansi-alias -opt-ra-region-strategy=block
-Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine
-Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags files that were used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/HP-Intel-Linux-Settings-flags.20090713.00.html`
`http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revD.20090713.00.html`

You can also download the XML flags sources by saving the following links:
`http://www.spec.org/cpu2006/flags/HP-Intel-Linux-Settings-flags.20090713.00.xml`
`http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revD.20090713.00.xml`

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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