SPEC® CFP2006 Result

Fujitsu Siemens Computers

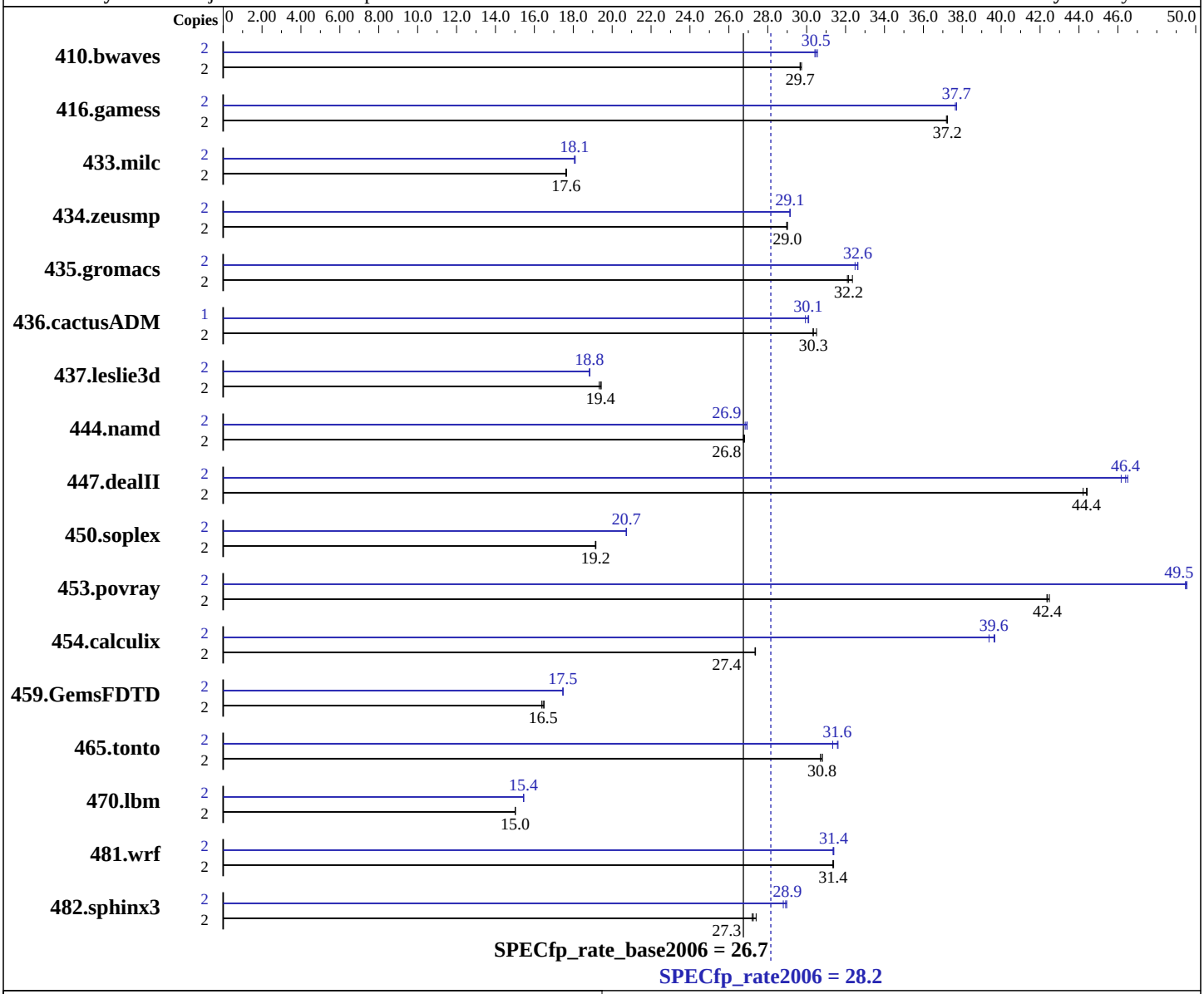
PRIMERGY RX100 S5, Intel Core 2 Duo E7200, 2.53 GHz

SPECfp®_rate2006 = 28.2

SPECfp_rate_base2006 = 26.7

CPU2006 license: 22
Test sponsor: Fujitsu Siemens Computers
Tested by: Fujitsu Siemens Computers

Test date: Aug-2008
Hardware Availability: Aug-2008
Software Availability: May-2008



Hardware

CPU Name:	Intel Core 2 Duo E7200
CPU Characteristics:	1067 MHz system bus
CPU MHz:	2533
FPU:	Integrated
CPU(s) enabled:	2 cores, 1 chip, 2 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	3 MB I+D on chip per chip

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Software

Operating System:	SuSE Linux Enterprise Server 10 (x86_64) with SP2, kernel 2.6.16.60-0.21-smp
Compiler:	Intel C++ and Fortran Compiler for Linux32 and Linux64 Version 10.1 - Build 20070913
Auto Parallel:	Yes
File System:	ext3
System State:	Multi-User Run Level 3
Base Pointers:	64-bit

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L3 Cache: None
Other Cache: None
Memory: 8 GB (4x2 GB PC2-6400E, 2 rank, CL 6-6-6, ECC)
Disk Subsystem: 1x SATA, 160 GB, 7200 rpm
Other Hardware: None

Peak Pointers: 32/64-bit
Other Software: binutils-2.17.50.0.5-0.1.x86_64

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	2	**916**	**29.7**	914	29.7	916	29.7	2	**892**	**30.5**	890	30.6	893	30.4
416.gamess	2	1052	37.2	1053	37.2	**1052**	**37.2**	2	1039	37.7	1040	37.6	**1039**	**37.7**
433.milc	2	**1041**	**17.6**	1040	17.7	1042	17.6	2	1014	18.1	**1016**	**18.1**	1016	18.1
434.zeusmp	2	628	29.0	627	29.0	**628**	**29.0**	2	624	29.1	625	29.1	**624**	**29.1**
435.gromacs	2	441	32.4	**444**	**32.2**	445	32.1	2	438	32.6	**438**	**32.6**	440	32.5
436.cactusADM	2	788	30.3	783	30.5	**788**	**30.3**	1	397	30.1	399	29.9	**397**	**30.1**
437.leslie3d	2	967	19.4	**969**	**19.4**	972	19.3	2	997	18.9	**998**	**18.8**	999	18.8
444.namd	2	600	26.8	598	26.8	**599**	**26.8**	2	598	26.8	**596**	**26.9**	595	26.9
447.dealII	2	517	44.2	**515**	**44.4**	515	44.4	2	496	46.2	**493**	**46.4**	492	46.5
450.soplex	2	872	19.1	**871**	**19.2**	870	19.2	2	**805**	**20.7**	804	20.7	805	20.7
453.povray	2	250	42.5	**251**	**42.4**	251	42.4	2	**215**	**49.5**	215	49.5	215	49.5
454.calculix	2	603	27.4	603	27.3	**603**	**27.4**	2	416	39.7	**416**	**39.6**	419	39.4
459.GemsFDTD	2	1286	16.5	1295	16.4	**1290**	**16.5**	2	1215	17.5	**1215**	**17.5**	1214	17.5
465.tonto	2	641	30.7	**639**	**30.8**	639	30.8	2	623	31.6	**623**	**31.6**	628	31.3
470.lbm	2	1830	15.0	1830	15.0	**1830**	**15.0**	2	1779	15.4	**1779**	**15.4**	1779	15.4
481.wrf	2	**712**	**31.4**	713	31.4	712	31.4	2	**712**	**31.4**	712	31.4	712	31.4
482.sphinx3	2	1433	27.2	1423	27.4	**1430**	**27.3**	2	1345	29.0	**1348**	**28.9**	1354	28.8

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Compiler Invocation Notes

```
All binaries were built with 64-bit Intel compiler except:
450.soplex, 470.lbm and 482.sphinx3 in peak were built with 32-bit Intel compiler.
```

Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
OMP_NUM_THREADS set to number of cores (default)
```

Platform Notes

```
BIOS configuration:
Hardware Prefetch = Enable, Adjacent Sector Prefetch = Disable
```

General Notes

```
For information about Fujitsu Siemens Computers please see:
http://www.fujitsu-siemens.com
```

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icc ifort

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-fast

C++ benchmarks:
-fast

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Base Optimization Flags (Continued)

Fortran benchmarks:
`-fast`

Benchmarks using both Fortran and C:
`-fast`

Peak Compiler Invocation

C benchmarks (except as noted below):
`/opt/intel/cc/10.1.008/bin/icc -L/opt/intel/cc/10.1.008/lib -I/opt/intel/cc/10.1.008/include`

433.milc: `icc`

C++ benchmarks (except as noted below):
`icpc`

450.soplex: `/opt/intel/cc/10.1.008/bin/icpc -L/opt/intel/cc/10.1.008/lib -I/opt/intel/cc/10.1.008/include`

Fortran benchmarks (except as noted below):
`ifort`

437.leslie3d: `/opt/intel/fc/10.1.008/bin/ifort -L/opt/intel/fc/10.1.008/lib -I/opt/intel/fc/10.1.008/include`

Benchmarks using both Fortran and C:
`icc ifort`

Peak Portability Flags

410.bwaves: `-DSPEC_CPU_LP64`
416.gamess: `-DSPEC_CPU_LP64`
433.milc: `-DSPEC_CPU_LP64`
434.zeusmp: `-DSPEC_CPU_LP64`
435.gromacs: `-DSPEC_CPU_LP64 -nofor_main`
436.cactusADM: `-DSPEC_CPU_LP64 -nofor_main`
444.namd: `-DSPEC_CPU_LP64`
447.dealII: `-DSPEC_CPU_LP64`
453.povray: `-DSPEC_CPU_LP64`
454.calculix: `-DSPEC_CPU_LP64 -nofor_main`
459.GemsFDTD: `-DSPEC_CPU_LP64`
465.tonto: `-DSPEC_CPU_LP64`
481.wrf: `-DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

433.milc: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -fno-alias -auto-ilp32`

470.lbm: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -unroll2 -scalar-rep- -prefetch -opt-malloc-options=3`

482.sphinx3: `-fast -unroll2`

C++ benchmarks:

444.namd: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -fno-alias -auto-ilp32`

447.dealII: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -unroll2 -ansi-alias -scalar-rep-`

450.soplex: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -opt-malloc-options=3`

453.povray: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -unroll4 -ansi-alias`

Fortran benchmarks:

410.bwaves: `-fast -prefetch`

416.gamess: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -unroll2 -Ob0 -ansi-alias -scalar-rep-`

434.zeusmp: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast`

437.leslie3d: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -prefetch -opt-malloc-options=3`

459.GemsFDTD: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -unroll2 -Ob0 -prefetch`

465.tonto: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -unroll4 -auto`

Benchmarks using both Fortran and C:

435.gromacs: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -prefetch -auto-ilp32`

436.cactusADM: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -unroll2 -prefetch -parallel -auto-ilp32`

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Peak Optimization Flags (Continued)

454.calculix: `-fast -unroll-aggressive -auto-ilp32`

481.wrf: `-fast -auto-ilp32`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/flags-ic101-linux-intel64.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/flags-ic101-linux-intel64.xml`

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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