



SPEC® CFP2006 Result

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Bull SAS

NovaScale R480 E1
(Intel Xeon E7330, 2.40 GHz)

SPECfp®2006 = 18.6

SPECfp_base2006 = 16.2

CPU2006 license: 20

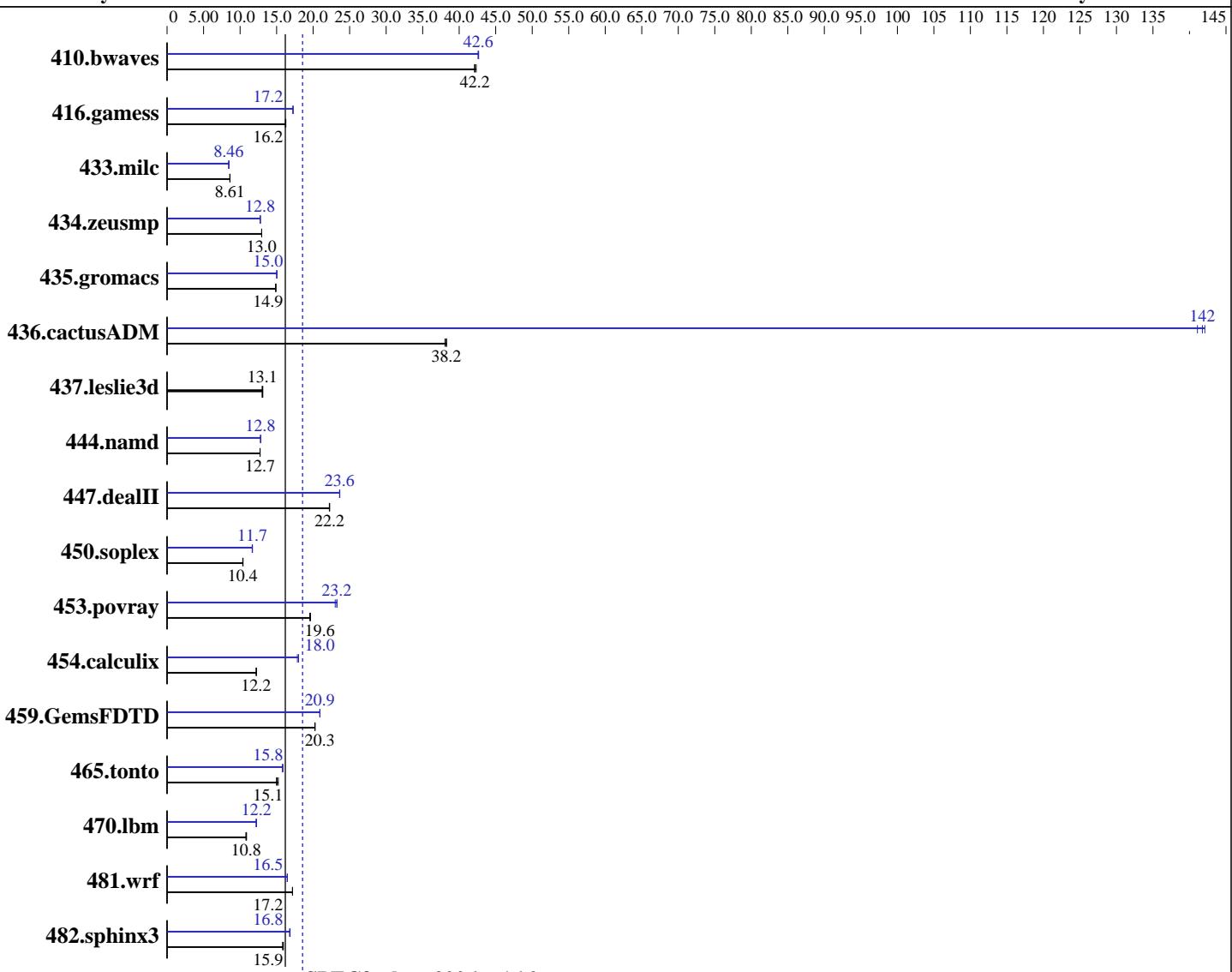
Test sponsor: Bull SAS

Tested by: Bull SAS

Test date: Aug-2008

Hardware Availability: Jan-2008

Software Availability: Nov-2007



SPECfp_base2006 = 16.2

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Hardware

CPU Name: Intel Xeon E7330
CPU Characteristics: 1066 MHz system bus
CPU MHz: 2400
FPU: Integrated
CPU(s) enabled: 16 cores, 4 chips, 4 cores/chip
CPU(s) orderable: 1 to 4 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 6 MB I+D on chip per chip, 3 MB shared / 2 cores

Software

Operating System: SUSE LINUX Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smp
Compiler: Intel C++ and Fortran Compiler 10.1 for Linux Build 20070913 Package ID: 1_cc_p_10.1.008, 1_fc_p_10.1.008
Auto Parallel: Yes
File System: ext2
System State: Run level 3 (multi-user)
Base Pointers: 64-bit

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L3 Cache: None
Other Cache: None
Memory: 32 GB (16x2 GB) FB-DIMM PC2-5300F ECC CL5
Disk Subsystem: 1x146 GB SAS, 10000 RPM
Other Hardware: None

Peak Pointers: 32/64-bit
Other Software: Binutils 2.17.50.0.15

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	323	42.1	322	42.2	321	42.3	319	42.7	319	42.6	319	42.6
416.gamess	1204	16.3	1207	16.2	1206	16.2	1136	17.2	1136	17.2	1134	17.3
433.milc	1067	8.61	1067	8.61	1066	8.61	1086	8.45	1086	8.46	1085	8.46
434.zeusmp	703	13.0	702	13.0	702	13.0	713	12.8	712	12.8	713	12.8
435.gromacs	479	14.9	480	14.9	479	14.9	475	15.0	475	15.0	475	15.0
436.cactusADM	314	38.1	313	38.2	312	38.3	84.7	141	84.1	142	84.3	142
437.leslie3d	717	13.1	719	13.1	722	13.0	717	13.1	719	13.1	722	13.0
444.namd	629	12.7	630	12.7	630	12.7	626	12.8	626	12.8	626	12.8
447.dealII	515	22.2	514	22.3	514	22.2	484	23.6	484	23.6	484	23.7
450.soplex	801	10.4	804	10.4	805	10.4	713	11.7	716	11.7	714	11.7
453.povray	271	19.6	272	19.5	271	19.6	229	23.2	231	23.0	229	23.3
454.calculix	674	12.2	675	12.2	676	12.2	458	18.0	458	18.0	462	17.8
459.GemsFDTD	524	20.3	524	20.2	524	20.3	507	20.9	507	20.9	507	20.9
465.tonto	646	15.2	653	15.1	657	15.0	620	15.9	622	15.8	623	15.8
470.lbm	1272	10.8	1268	10.8	1262	10.9	1129	12.2	1122	12.2	1121	12.3
481.wrf	650	17.2	650	17.2	650	17.2	678	16.5	679	16.5	678	16.5
482.sphinx3	1224	15.9	1232	15.8	1226	15.9	1162	16.8	1157	16.8	1157	16.8

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
OMP_NUM_THREADS set to number of cores
KMP_AFFINITY set to physical,0
KMP_STACKSIZE set to 200M

General Notes

All benchmarks compiled in 64-bit mode except 450.soplex,
470.lbm and 482.sphinx3, at peak, are compiled in 32-bit mode
BIOS settings :

Hardware Prefetcher : Enabled
Adjacent Cache-Line Prefetch : Enabled



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Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icc ifort

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-fast -parallel

C++ benchmarks:
-fast -parallel

Fortran benchmarks:
-fast -parallel

Benchmarks using both Fortran and C:
-fast -parallel



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Peak Compiler Invocation

C benchmarks (except as noted below):

```
/opt/intel/cc/10.1.008/bin/icc -L/opt/intel/cc/10.1.008/lib
-I/opt/intel/cc/10.1.008/include
```

433.milc: icc

C++ benchmarks (except as noted below):

icpc

```
450.soplex: /opt/intel/cc/10.1.008/bin/icpc -L/opt/intel/cc/10.1.008/lib
-I/opt/intel/cc/10.1.008/include
```

Fortran benchmarks:

ifort

Benchmarks using both Fortran and C:

icc ifort

Peak Portability Flags

```
410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
    433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
    444.namd: -DSPEC_CPU_LP64
    447.dealII: -DSPEC_CPU_LP64
    453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
    481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
```

Peak Optimization Flags

C benchmarks:

```
433.milc: -prof-gen(pass 1) -prof-use(pass 2) -fast -fno-alias
    -auto-ilp32
```

```
470.lbm: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll12
    -scalar-rep -prefetch -opt-malloc-options=3
```

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Peak Optimization Flags (Continued)

482.sphinx3: -fast -unroll12

C++ benchmarks:

444.namd: -prof-gen(pass 1) -prof-use(pass 2) -fast -fno-alias
-auto-ilp32

447.dealII: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll12
-ansi-alias -scalar-rep-

450.soplex: -prof-gen(pass 1) -prof-use(pass 2) -fast
-opt-malloc-options=3

453.povray: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll14
-ansi-alias

Fortran benchmarks:

410.bwaves: -fast -prefetch -parallel

416.gamess: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll12 -O0
-ansi-alias -scalar-rep-

434.zeusmp: -prof-gen(pass 1) -prof-use(pass 2) -fast

437.leslie3d: basepeak = yes

459.GemsFDTD: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll12 -O0
-prefetch -parallel

465.tonto: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll14 -auto

Benchmarks using both Fortran and C:

435.gromacs: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch
-auto-ilp32

436.cactusADM: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll12
-prefetch -parallel -auto-ilp32

454.calculix: -fast -unroll-aggressive -auto-ilp32

481.wrf: -fast -parallel -prefetch -auto-ilp32

The flags file that was used to format this result can be browsed at

http://www.spec.org/cpu2006/flags/EM64T_Intel101_fp_flags.20090713.00.html

You can also download the XML flags source by saving the following link:

http://www.spec.org/cpu2006/flags/EM64T_Intel101_fp_flags.20090713.00.xml



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