SPEC® CINT2006 Result

Dell Inc.

PowerEdge M600 (Intel Xeon E5205, 1.86 GHz)

SPECint®2006 = 16.6

SPECint_base2006 = 13.9

CPU2006 license: 55		**Test date:**	Aug-2008
Test sponsor: Dell Inc.		**Hardware Availability:**	Feb-2008
Tested by: Dell Inc.		**Software Availability:**	May-2008

Graph data (benchmark: peak / base):

- **400.perlbench**: 15.3 / 12.3
- **401.bzip2**: 11.1 / 10.3
- **403.gcc**: 13.4 / 10.7
- **429.mcf**: 17.1 / 15.8
- **445.gobmk**: 13.8 / 12.7
- **456.hmmer**: 17.0 / 9.79
- **458.sjeng**: 12.7 / 11.4
- **462.libquantum**: 83.3 / 49.2
- **464.h264ref**: 20.7 / 19.2
- **471.omnetpp**: 12.1 / 11.1
- **473.astar**: 10.6 / 9.60
- **483.xalancbmk**: 16.7

SPECint_base2006 = 13.9

SPECint2006 = 16.6

Hardware

CPU Name:	Intel Xeon E5205
CPU Characteristics:	
CPU MHz:	1866
FPU:	Integrated
CPU(s) enabled:	4 cores, 2 chips, 2 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	6 MB I+D on chip per chip
L3 Cache:	None
Other Cache:	None
Memory:	16 GB (8 x 2 GB 667 MHz ECC CL5 FB-DIMM)
Disk Subsystem:	1 x 80 GB 5400 RPM SATA
Other Hardware:	None

Software

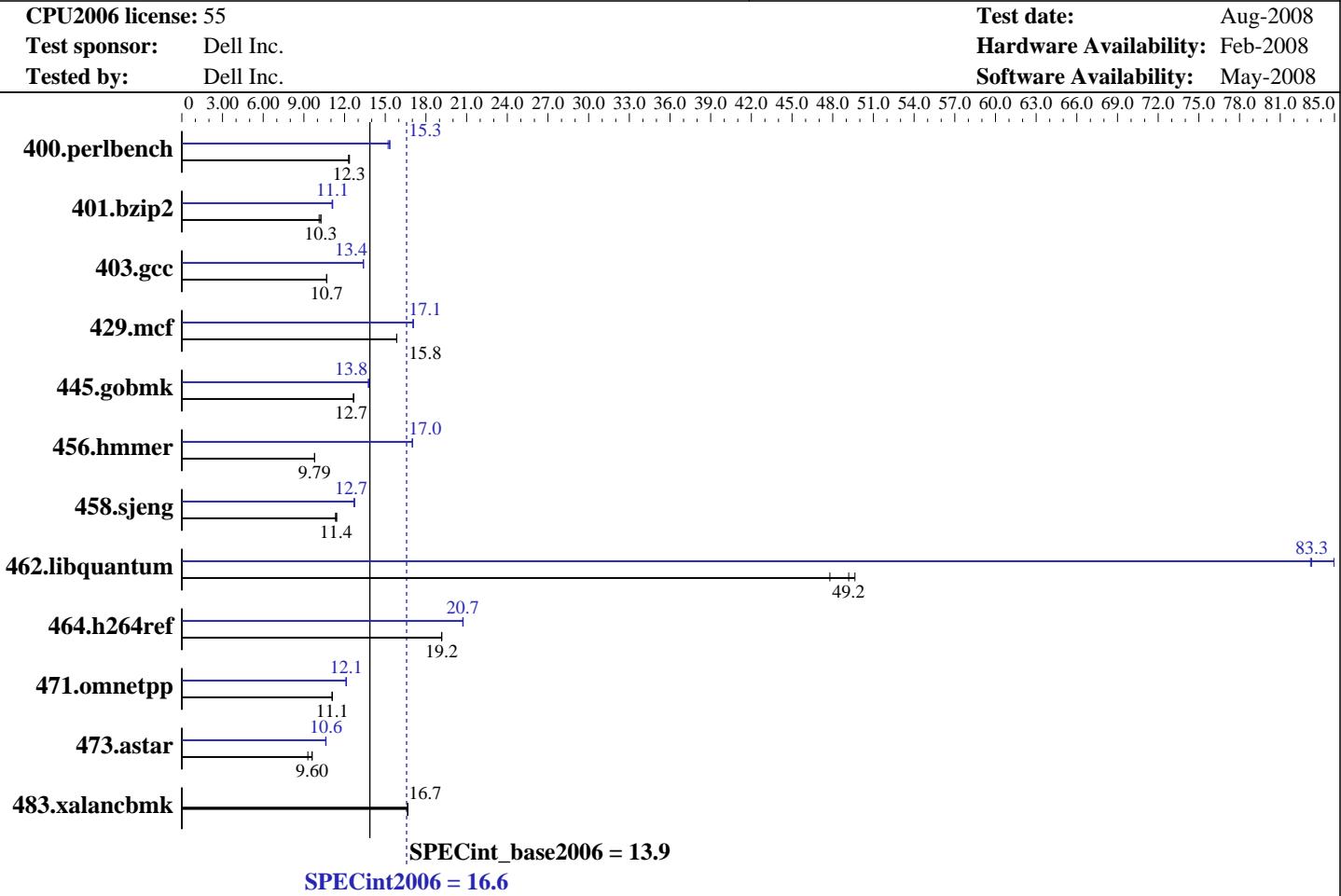|	
Operating System:	SUSE Linux Enterprise Server 10 (x86_64) SP2, Kernel 2.6.16-60.0.21-smp
Compiler:	Intel C++ Compiler 10.1 for Linux Build 20080312 Package ID: l_cc_p_10.1.015
Auto Parallel:	Yes
File System:	ReiserFS
System State:	Run level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502

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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	790	12.4	795	12.3	**792**	**12.3**	**638**	**15.3**	636	15.4	642	15.2
401.bzip2	**941**	**10.3**	941	10.3	953	10.1	**868**	**11.1**	871	11.1	868	11.1
403.gcc	753	10.7	**753**	**10.7**	754	10.7	602	13.4	**601**	**13.4**	601	13.4
429.mcf	**575**	**15.8**	576	15.8	575	15.9	534	17.1	535	17.0	**534**	**17.1**
445.gobmk	**828**	**12.7**	828	12.7	828	12.7	**762**	**13.8**	762	13.8	762	13.8
456.hmmer	**953**	**9.79**	952	9.80	954	9.78	**549**	**17.0**	549	17.0	549	17.0
458.sjeng	1067	11.3	**1064**	**11.4**	1059	11.4	954	12.7	**950**	**12.7**	950	12.7
462.libquantum	434	47.8	**421**	**49.2**	417	49.6	**249**	**83.3**	244	85.0	249	83.2
464.h264ref	**1155**	**19.2**	1155	19.2	1155	19.2	1067	20.7	**1068**	**20.7**	1068	20.7
471.omnetpp	563	11.1	564	11.1	**563**	**11.1**	517	12.1	**515**	**12.1**	515	12.1
473.astar	755	9.29	**731**	**9.60**	730	9.61	662	10.6	**661**	**10.6**	660	10.6
483.xalancbmk	414	16.7	415	16.6	**414**	**16.7**	414	16.7	415	16.6	**414**	**16.7**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stack size to unlimited prior to run

General Notes

All benchmarks compiled in 32-bit mode except 401.bzip2 and 456.hmmer,
for peak, are compiled in 64-bit mode
OMP_NUM_THREADS set to number of processors
KMP_AFFINITY set to "physical,0"

Base Compiler Invocation

C benchmarks:
      icc

C++ benchmarks:
      icpc

Base Portability Flags

    400.perlbench: -DSPEC_CPU_LINUX_IA32
    462.libquantum: -DSPEC_CPU_LINUX
    483.xalancbmk: -DSPEC_CPU_LINUX

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Base Optimization Flags

C benchmarks:
```
-fast -vec-guard-write -parallel -par-runtime-control
```

C++ benchmarks:
```
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/spec/cpu2006.1.1/lib -lsmartheap
```

Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

Peak Compiler Invocation

C benchmarks (except as noted below):
```
icc
```

```
401.bzip2: /opt/intel/cce/10.1.015/bin/icc
           -L/opt/intel/cce/10.1.015/lib
           -I/opt/intel/cce/10.1.015/include
```

```
456.hmmer: /opt/intel/cce/10.1.015/bin/icc
           -L/opt/intel/cce/10.1.015/lib
           -I/opt/intel/cce/10.1.015/include
```

C++ benchmarks:
```
icpc
```

Peak Portability Flags

```
400.perlbench: -DSPEC_CPU_LINUX_IA32
   401.bzip2: -DSPEC_CPU_LP64
   456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX
```

Peak Optimization Flags

C benchmarks:

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Peak Optimization Flags (Continued)

400.perlbench: `-prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias`
`-prefetch`

401.bzip2: `-prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch`
`-auto-ilp32`

403.gcc: `-fast -inline-calloc -opt-malloc-options=3`

429.mcf: `-fast -prefetch`

445.gobmk: `-prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo`
`-no-prec-div -ansi-alias`

456.hmmer: `-fast -unroll2 -ansi-alias -opt-multi-version-aggressive`
`-auto-ilp32`

458.sjeng: `-prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4`

462.libquantum: `-fast -unroll4 -Ob0 -prefetch`
`-opt-streaming-stores always -vec-guard-write`
`-opt-malloc-options=3 -parallel -par-runtime-control`

464.h264ref: `-prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2`
`-ansi-alias`

C++ benchmarks:

471.omnetpp: `-prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo`
`-no-prec-div -ansi-alias -opt-ra-region-strategy=block`
`-Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap`

473.astar: `-prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo`
`-no-prec-div -ansi-alias -opt-ra-region-strategy=routine`
`-Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap`

483.xalancbmk: `basepeak = yes`

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic10.1-int-linux64-revD.20090713.00.html
http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20090713.07.html

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You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic10.1-int-linux64-revD.20090713.00.xml
http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20090713.07.xml

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