



SPEC® CINT2006 Result

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Fujitsu Siemens Computers

SPECint®_rate2006 = 75.6

PRIMERGY RX200 S4, Intel Xeon X5460, 3.16 GHz

SPECint_rate_base2006 = 63.7

CPU2006 license: 22

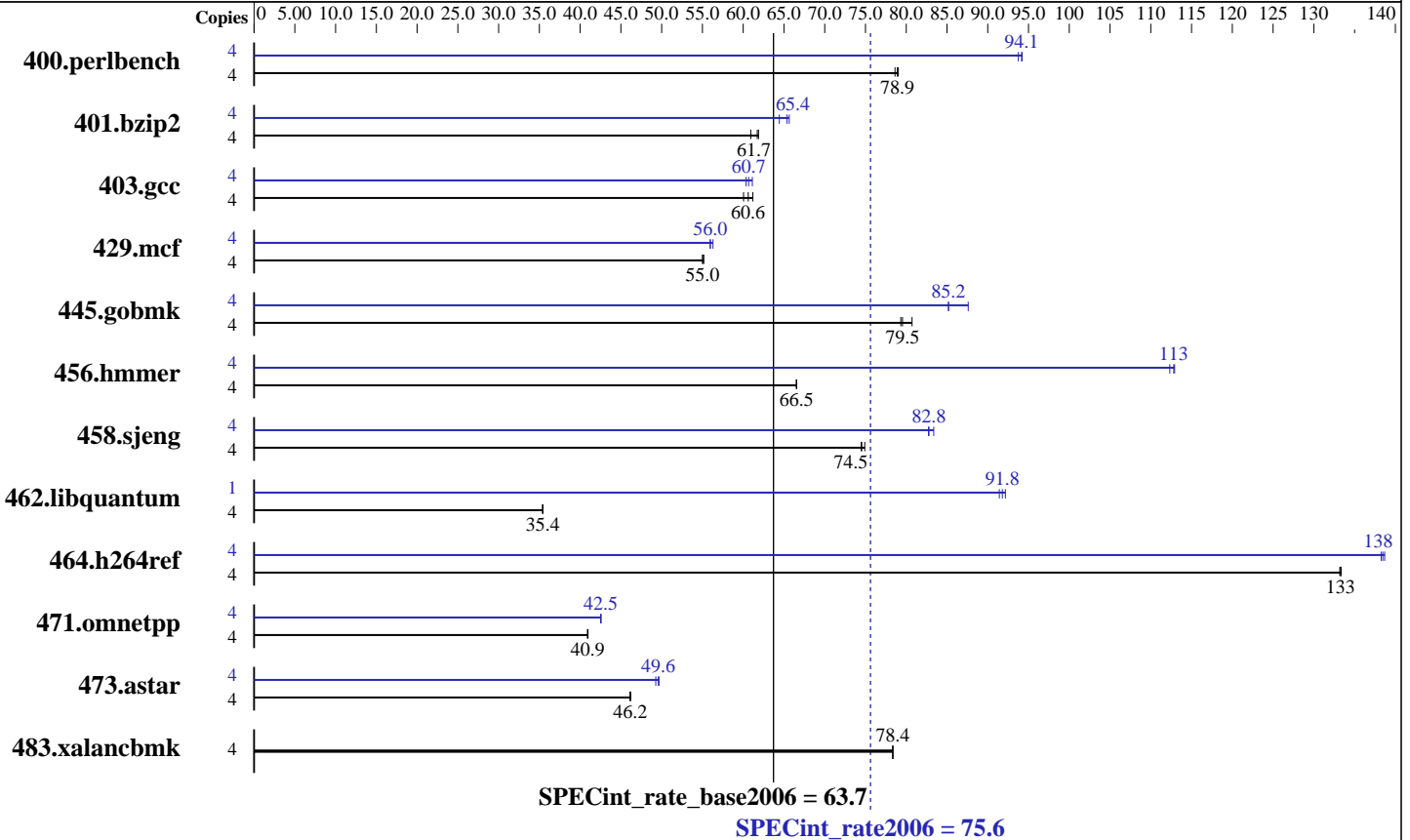
Test sponsor: Fujitsu Siemens Computers

Tested by: Fujitsu Siemens Computers

Test date: Jun-2008

Hardware Availability: Dec-2007

Software Availability: Nov-2007



Hardware

CPU Name: Intel Xeon X5460
 CPU Characteristics: 1333 MHz system bus
 CPU MHz: 3167
 FPU: Integrated
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores
 L3 Cache: None
 Other Cache: None
 Memory: 16 GB (8x2 GB PC2-5300F, 2 rank, CL 5-5-5, ECC)
 Disk Subsystem: 1x SAS, 73 GB, 15000 rpm
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smp
 Compiler: Intel C++ Compiler for Linux32 and Linux64, Version 10.1, Build 20070913
 Auto Parallel: Yes
 File System: ext2
 System State: Multi-User Run Level 3
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: MicroQuill SmartHeap Library, Version 8.1 binutils-2.17.50.0.5-0.1.x86_64



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	<u>495</u>	<u>78.9</u>	495	79.0	497	78.6	4	415	94.2	<u>415</u>	<u>94.1</u>	417	93.7
401.bzip2	4	<u>625</u>	<u>61.7</u>	633	60.9	624	61.9	4	599	64.4	588	65.6	<u>590</u>	<u>65.4</u>
403.gcc	4	<u>531</u>	<u>60.6</u>	526	61.2	536	60.0	4	527	61.1	533	60.4	<u>531</u>	<u>60.7</u>
429.mcf	4	661	55.2	<u>663</u>	<u>55.0</u>	663	55.0	4	648	56.3	652	55.9	<u>652</u>	<u>56.0</u>
445.gobmk	4	520	80.7	<u>528</u>	<u>79.5</u>	529	79.3	4	479	87.6	<u>493</u>	<u>85.2</u>	493	85.2
456.hmmmer	4	561	66.6	561	66.5	<u>561</u>	<u>66.5</u>	4	331	113	<u>331</u>	<u>113</u>	332	112
458.sjeng	4	646	74.9	650	74.5	<u>649</u>	<u>74.5</u>	4	<u>585</u>	<u>82.8</u>	585	82.7	581	83.4
462.libquantum	4	<u>2341</u>	<u>35.4</u>	2341	35.4	2340	35.4	1	227	91.4	225	92.2	<u>226</u>	<u>91.8</u>
464.h264ref	4	<u>664</u>	<u>133</u>	665	133	664	133	4	640	138	638	139	<u>639</u>	<u>138</u>
471.omnetpp	4	610	41.0	<u>611</u>	<u>40.9</u>	611	40.9	4	<u>588</u>	<u>42.5</u>	587	42.6	588	42.5
473.astar	4	609	46.1	608	46.2	<u>608</u>	<u>46.2</u>	4	<u>566</u>	<u>49.6</u>	565	49.7	570	49.3
483.xalancbmk	4	352	78.3	<u>352</u>	<u>78.4</u>	352	78.4	4	352	78.3	<u>352</u>	<u>78.4</u>	352	78.4

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
OMP_NUM_THREADS set to number of cores (default)

Platform Notes

BIOS configuration:
Hardware Prefetch = Enable, Adjacent Sector Prefetch = Disable

General Notes

All binaries were built with 32-bit Intel compiler except:
401.bzip2 and 456.hmmmer in peak were built with 64-bit Intel compiler by changing the path for include and library files.

For information about Fujitsu Siemens Computers please see:
<http://www.fujitsu-siemens.com>

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc



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Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-fast -inline-calloc -opt-malloc-options=3

C++ benchmarks:
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/opt/SmartHeap_8.1/lib -lsmartheap

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc

401.bzip2: /opt/intel/cce/10.1.008/bin/icc
-L/opt/intel/cce/10.1.008/lib
-I/opt/intel/cce/10.1.008/include

456.hmmer: /opt/intel/cce/10.1.008/bin/icc
-L/opt/intel/cce/10.1.008/lib
-I/opt/intel/cce/10.1.008/include

C++ benchmarks:
icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX

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Peak Portability Flags (Continued)

483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias
-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch

403.gcc: -fast -inline-calloc -opt-malloc-options=3

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo
-no-prec-div -ansi-alias

456.hmmer: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll4 -Ob0 -prefetch
-opt-streaming-stores always -vec-guard-write
-opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
-ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=block
-Wl,-z,muldefs -L/opt/SmartHeap_8.1/lib -lsmarheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine
-Wl,-z,muldefs -L/opt/SmartHeap_8.1/lib -lsmarheap

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

Continued on next page



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Peak Other Flags (Continued)

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090713.01.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090713.01.xml>

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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