



SPEC® CINT2006 Result

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Dell Inc.

SPECint®_rate2006 = Not Run

PowerEdge 2900 III (Intel Xeon X5460, 3.16 GHz)

SPECint_rate_base2006 = 113

CPU2006 license: 55

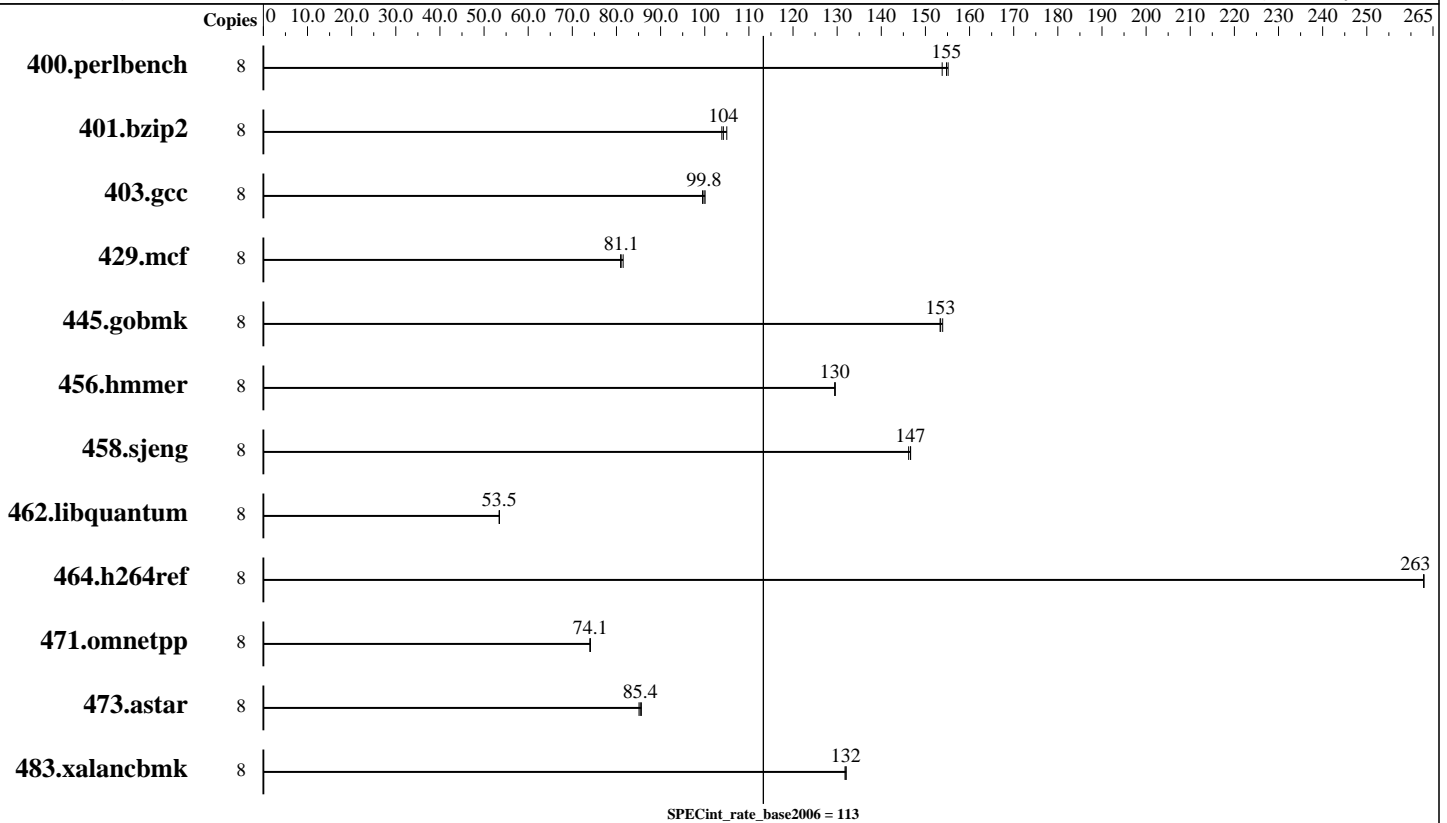
Test date: Jul-2008

Test sponsor: Dell Inc.

Hardware Availability: Nov-2007

Tested by: Dell Inc.

Software Availability: May-2008



Hardware

CPU Name: Intel Xeon X5460
 CPU Characteristics: 3166
 CPU MHz: 3166
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores
 L3 Cache: None
 Other Cache: None
 Memory: 16 GB (8 x 2 GB, DDR2-667 MHz, CL5, Reg, Dual Rank)
 Disk Subsystem: 2 x 73 GB GB SAS, 15000 RPM
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64) SP2
 Kernel 2.6.16-60.0.21-smp
 Compiler: Intel C++ Compiler 10.1 for Linux
 Build 20080312 Package ID: l_cc_p_10.1.015
 Auto Parallel: No
 File System: ReiserFS
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: Not Applicable
 Other Software: Microquill SmartHeap V8.1
 Binutils 2.18.50.0.7.20080502



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	505	155	508	154	504	155							
401.bzip2	8	743	104	741	104	735	105							
403.gcc	8	647	99.5	645	99.8	644	100							
429.mcf	8	895	81.5	902	80.9	900	81.1							
445.gobmk	8	547	153	545	154	547	153							
456.hammer	8	577	129	576	130	576	130							
458.sjeng	8	662	146	660	147	660	147							
462.libquantum	8	3100	53.5	3101	53.4	3100	53.5							
464.h264ref	8	673	263	673	263	673	263							
471.omnetpp	8	676	74.0	675	74.1	675	74.1							
473.astar	8	656	85.6	658	85.4	660	85.1							
483.xalancbmk	8	419	132	418	132	418	132							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.

Platform Notes

BIOS Settings:
Adjacent Cache Line Prefetch = Disabled (default = Enabled)
Hardware Prefetcher = Disabled (default = Enabled)

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX



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Base Optimization Flags

C benchmarks:

`-fast -inline-calloc -opt-malloc-options=3`

C++ benchmarks:

`-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/spec/cpu2006.1.1/lib -lsmartheap`

Base Other Flags

C benchmarks:

`403.gcc: -Dalloca=_alloca`

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic10.1-int-linux64-revD.20090713.00.html>

<http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20090713.07.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic10.1-int-linux64-revD.20090713.00.xml>

<http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20090713.07.xml>

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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