SPEC® CFP2006 Result

Bull SAS

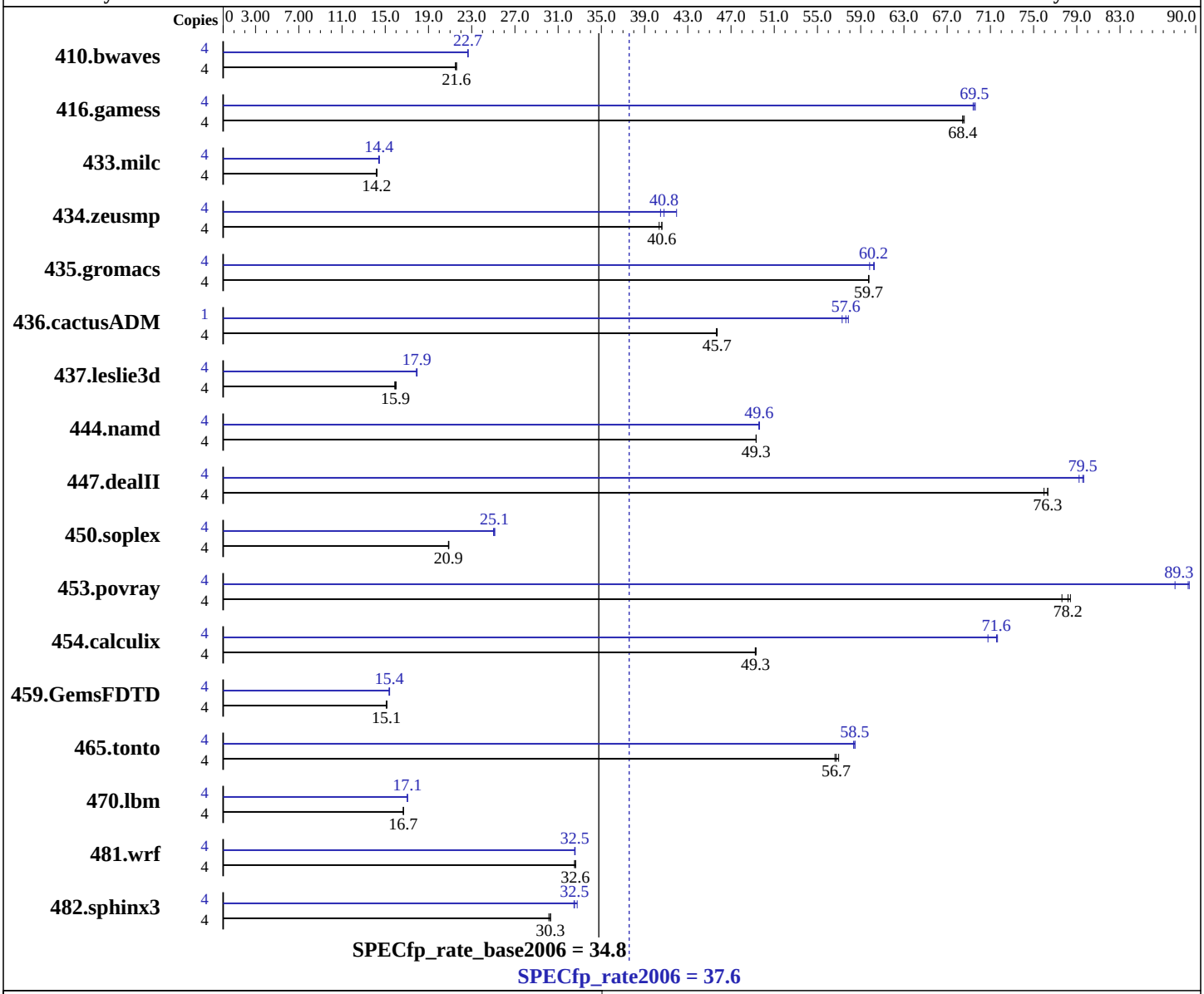
NovaScale R440 E1
(Intel Xeon E5410, 2.33 GHz)

SPECfp®_rate2006 = 37.6

SPECfp_rate_base2006 = 34.8

CPU2006 license: 20
Test sponsor: Bull SAS
Tested by: Bull SAS

Test date: Jun-2008
Hardware Availability: Jan-2008
Software Availability: Nov-2007



Hardware

CPU Name:	Intel Xeon E5410
CPU Characteristics:	1333 MHz system bus
CPU MHz:	2333
FPU:	Integrated
CPU(s) enabled:	4 cores, 1 chip, 4 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	12 MB I+D on chip per chip, 6 MB shared / 2 cores

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Software

Operating System:	SUSE LINUX Enterprise Server 10 SP1 Kernel 2.6.16.46-0.12-smp for x86_64
Compiler:	Intel C++ and Fortran Compiler 10.1 for Linux Build 20070913 Package ID: l_cc_p_10.1.008, l_fc_p_10.1.008
Auto Parallel:	Yes
File System:	ext2
System State:	Run level 3 (multi-user)
Base Pointers:	64-bit

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L3 Cache: None
Other Cache: None
Memory: 16 GB (8x2 GB) FB-DIMM PC2-5300F ECC CL5
Disk Subsystem: 1x73 GB SAS, 15000 RPM
Other Hardware: None

Peak Pointers: 32/64-bit
Other Software: Binutils 2.17.50.0.15

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	4	2530	21.5	**2518**	**21.6**	2518	21.6	4	**2400**	**22.7**	2397	22.7	2401	22.6
416.gamess	4	**1144**	**68.4**	1142	68.6	1144	68.4	4	1128	69.4	1125	69.6	**1127**	**69.5**
433.milc	4	2578	14.2	2590	14.2	**2585**	**14.2**	4	2540	14.5	2551	14.4	**2544**	**14.4**
434.zeusmp	4	902	40.3	**897**	**40.6**	896	40.6	4	867	42.0	**892**	**40.8**	899	40.5
435.gromacs	4	478	59.7	**478**	**59.7**	478	59.8	4	474	60.3	477	59.8	**474**	**60.2**
436.cactusADM	4	1047	45.6	1045	45.7	**1046**	**45.7**	1	**207**	**57.6**	209	57.3	207	57.9
437.leslie3d	4	2368	15.9	2349	16.0	**2359**	**15.9**	4	2095	18.0	2103	17.9	**2098**	**17.9**
444.namd	4	**650**	**49.3**	650	49.3	650	49.3	4	646	49.6	647	49.6	**647**	**49.6**
447.dealII	4	602	76.0	**600**	**76.3**	600	76.3	4	578	79.2	575	79.6	**575**	**79.5**
450.soplex	4	**1599**	**20.9**	1597	20.9	1600	20.8	4	1333	25.0	**1329**	**25.1**	1327	25.1
453.povray	4	271	78.4	**272**	**78.2**	274	77.6	4	238	89.4	**238**	**89.3**	242	88.1
454.calculix	4	670	49.2	**670**	**49.3**	669	49.3	4	466	70.8	**461**	**71.6**	461	71.7
459.GemsFDTD	4	2800	15.2	2810	15.1	**2809**	**15.1**	4	2755	15.4	**2758**	**15.4**	2765	15.4
465.tonto	4	**694**	**56.7**	691	57.0	695	56.6	4	**673**	**58.5**	675	58.3	673	58.5
470.lbm	4	**3295**	**16.7**	3295	16.7	3295	16.7	4	3222	17.1	3225	17.0	**3222**	**17.1**
481.wrf	4	1374	32.5	**1371**	**32.6**	1370	32.6	4	1373	32.5	**1373**	**32.5**	1372	32.6
482.sphinx3	4	2573	30.3	**2575**	**30.3**	2587	30.1	4	**2398**	**32.5**	2401	32.5	2379	32.8

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
'/usr/bin/taskset' used to bind processes to CPUs
OMP_NUM_THREADS set to number of cores
KMP_AFFINITY set to physical,0
KMP_STACKSIZE set to 64M
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
```

General Notes

```
All benchmarks compiled in 64-bit mode except 450.soplex,
470.lbm and 482.sphinx3, at peak, are compiled in 32-bit mode
The Bull NovaScale R440 E1 (Intel Xeon E5410, 2.33 GHz) and
the Bull NovaScale R460 E1 (Intel Xeon E5410, 2.33 GHz) models are electronically equivalent.
The results have been measured on a Bull NovaScale R460 E1 (Intel Xeon E5410, 2.33 GHz) model.
```

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icc ifort

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-fast

C++ benchmarks:
-fast

Fortran benchmarks:
-fast

Benchmarks using both Fortran and C:
-fast

Peak Compiler Invocation

C benchmarks (except as noted below):
`/opt/intel/cc/10.1.008/bin/icc -L/opt/intel/cc/10.1.008/lib -I/opt/intel/cc/10.1.008/include`

433.milc: `icc`

C++ benchmarks (except as noted below):
`icpc`

450.soplex: `/opt/intel/cc/10.1.008/bin/icpc -L/opt/intel/cc/10.1.008/lib -I/opt/intel/cc/10.1.008/include`

Fortran benchmarks (except as noted below):
`ifort`

437.leslie3d: `/opt/intel/fc/10.1.008/bin/ifort -L/opt/intel/fc/10.1.008/lib -I/opt/intel/fc/10.1.008/include`

Benchmarks using both Fortran and C:
`icc ifort`

Peak Portability Flags

410.bwaves: `-DSPEC_CPU_LP64`
416.gamess: `-DSPEC_CPU_LP64`
433.milc: `-DSPEC_CPU_LP64`
434.zeusmp: `-DSPEC_CPU_LP64`
435.gromacs: `-DSPEC_CPU_LP64 -nofor_main`
436.cactusADM: `-DSPEC_CPU_LP64 -nofor_main`
444.namd: `-DSPEC_CPU_LP64`
447.dealII: `-DSPEC_CPU_LP64`
453.povray: `-DSPEC_CPU_LP64`
454.calculix: `-DSPEC_CPU_LP64 -nofor_main`
459.GemsFDTD: `-DSPEC_CPU_LP64`
465.tonto: `-DSPEC_CPU_LP64`
481.wrf: `-DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

433.milc: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -fno-alias -auto-ilp32`

470.lbm: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -unroll2 -scalar-rep- -prefetch -opt-malloc-options=3`

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Peak Optimization Flags (Continued)

482.sphinx3: -fast -unroll2

C++ benchmarks:

444.namd: -prof-gen(pass 1) -prof-use(pass 2) -fast -fno-alias -auto-ilp32

447.dealII: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2 -ansi-alias -scalar-rep-

450.soplex: -prof-gen(pass 1) -prof-use(pass 2) -fast -opt-malloc-options=3

453.povray: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -fast -prefetch

416.gamess: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2 -Ob0 -ansi-alias -scalar-rep-

434.zeusmp: -prof-gen(pass 1) -prof-use(pass 2) -fast

437.leslie3d: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch -opt-malloc-options=3

459.GemsFDTD: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2 -Ob0 -prefetch

465.tonto: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4 -auto

Benchmarks using both Fortran and C:

435.gromacs: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch -auto-ilp32

436.cactusADM: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2 -prefetch -parallel -auto-ilp32

454.calculix: -fast -unroll-aggressive -auto-ilp32

481.wrf: -fast -auto-ilp32

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/EM64T_Intel101_fp_flags.20090713.00.html

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/EM64T_Intel101_fp_flags.20090713.00.xml`

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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