



SPEC[®] CINT2006 Result

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NEC Corporation

SPECint[®]_rate2006 = 74.2

Express5800/i120Ra-e1
(Intel Xeon L5240)

SPECint_rate_base2006 = 63.0

CPU2006 license: 9006

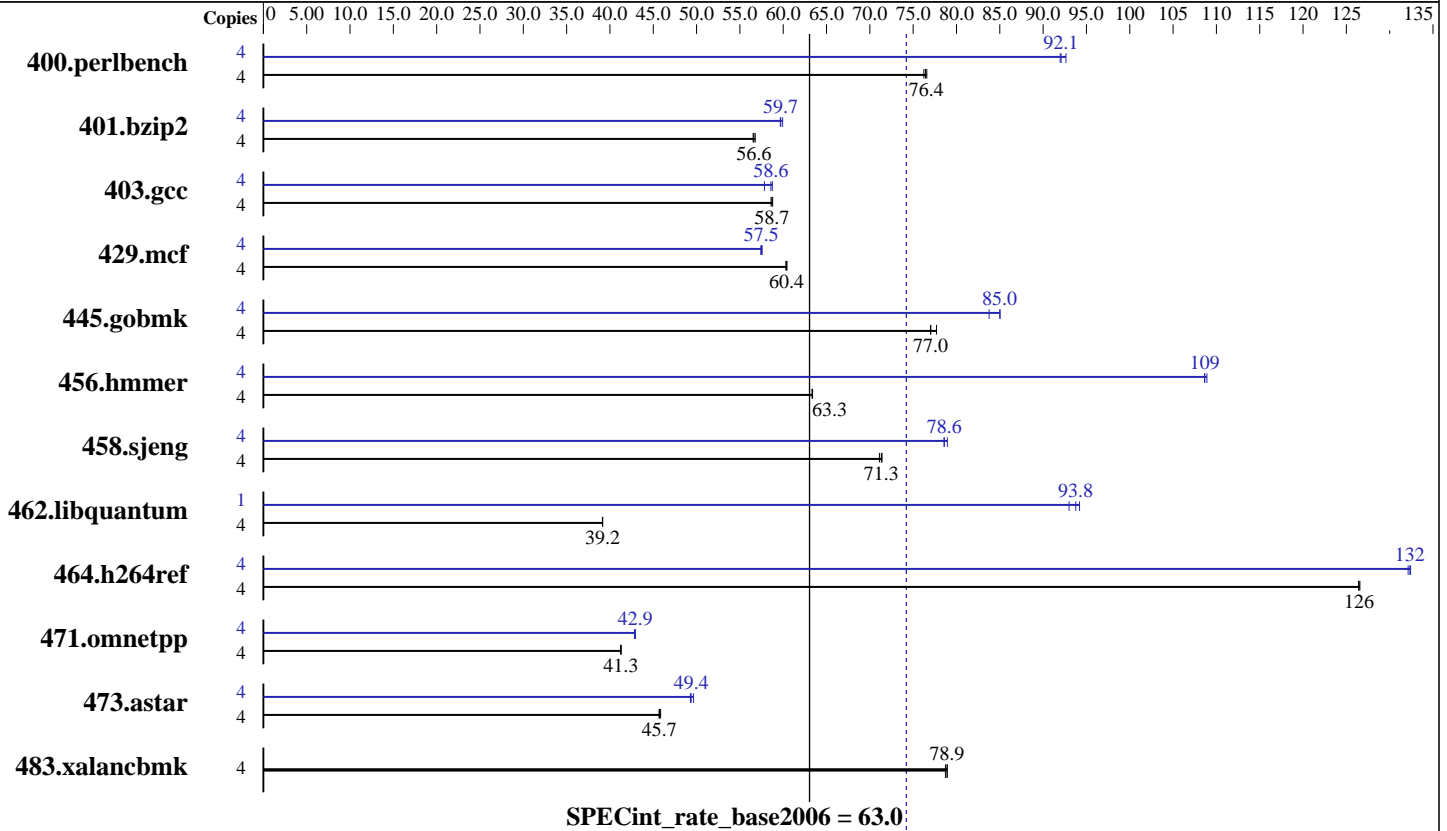
Test date: Jun-2008

Test sponsor: NEC Corporation

Hardware Availability: May-2008

Tested by: NEC Corporation

Software Availability: Nov-2007



Hardware

CPU Name: Intel Xeon L5240
 CPU Characteristics: 3.00 GHz, 6 MB L2, 1333 MHz bus
 CPU MHz: 3000
 FPU: Integrated
 CPU(s) enabled: 4 cores, 2 chips, 2 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 6 MB I+D on chip per chip
 L3 Cache: None
 Other Cache: None
 Memory: 16 GB (4x4 GB PC2-5300P, 2 rank, CL5-5-5, ECC)
 Disk Subsystem: 1x80.0 GB SATAII, 7200RPM
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smp
 Compiler: Intel C++ Compiler for Linux32 and Linux64 version 10.1 Build 20070913 Package ID: L_cc_p_10.1.008
 Auto Parallel: Yes
 File System: ReiserFS
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: MicroQuill SmartHeap library 8.1 binutils-2.17.tar.gz, Version 2.17



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	510	76.6	513	76.2	<u>511</u>	<u>76.4</u>	4	425	92.0	422	92.6	<u>424</u>	<u>92.1</u>
401.bzip2	4	<u>682</u>	<u>56.6</u>	680	56.8	683	56.5	4	644	59.9	<u>646</u>	<u>59.7</u>	647	59.7
403.gcc	4	<u>548</u>	<u>58.7</u>	550	58.6	548	58.8	4	<u>550</u>	<u>58.6</u>	548	58.7	557	57.8
429.mcf	4	<u>604</u>	<u>60.4</u>	604	60.4	605	60.3	4	635	57.4	<u>635</u>	<u>57.5</u>	634	57.6
445.gobmk	4	545	77.0	<u>545</u>	<u>77.0</u>	540	77.7	4	493	85.0	501	83.8	<u>494</u>	<u>85.0</u>
456.hmmer	4	589	63.4	589	63.3	<u>589</u>	<u>63.3</u>	4	343	109	344	109	<u>343</u>	<u>109</u>
458.sjeng	4	<u>679</u>	<u>71.3</u>	681	71.1	678	71.4	4	616	78.6	613	78.9	<u>616</u>	<u>78.6</u>
462.libquantum	4	2117	39.2	2118	39.1	<u>2117</u>	<u>39.2</u>	1	223	93.0	220	94.2	<u>221</u>	<u>93.8</u>
464.h264ref	4	<u>700</u>	<u>126</u>	700	126	699	127	4	670	132	669	132	<u>669</u>	<u>132</u>
471.omnetpp	4	606	41.2	606	41.3	<u>606</u>	<u>41.3</u>	4	584	42.8	<u>582</u>	<u>42.9</u>	582	42.9
473.astar	4	615	45.7	<u>614</u>	<u>45.7</u>	612	45.9	4	<u>569</u>	<u>49.4</u>	566	49.7	570	49.3
483.xalancbmk	4	351	78.7	<u>350</u>	<u>78.9</u>	350	78.9	4	351	78.7	<u>350</u>	<u>78.9</u>	350	78.9

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
OMP_NUM_THREADS set to number of cores

Platform Notes

Bios settings:
Hardware Prefetcher: Enabled
Adjacent Cache Line Prefetch: Enabled
Intel SpeedStep Technology: Disabled

General Notes

All benchmarks compiled in 32-bit mode except 401.bzip2,
456.hmmer, for peak, are compiled in 64-bit mode

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc



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Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-fast -inline-calloc -opt-malloc-options=3

C++ benchmarks:
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/opt/SmartHeap_8.1/lib -lsmartheap

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc

401.bzip2: /opt/intel/cce/10.1.008/bin/icc
-L/opt/intel/cce/10.1.008/lib
-I/opt/intel/cce/10.1.008/include

456.hmmmer: /opt/intel/cce/10.1.008/bin/icc
-L/opt/intel/cce/10.1.008/lib
-I/opt/intel/cce/10.1.008/include

C++ benchmarks:
icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX

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Peak Portability Flags (Continued)

483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias
-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch

403.gcc: -fast -inline-calloc -opt-malloc-options=3

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo
-no-prec-div -ansi-alias

456.hmmer: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll4 -Ob0 -prefetch
-opt-streaming-stores always -vec-guard-write
-opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
-ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=block
-Wl,-z,muldefs -L/opt/SmartHeap_8.1/lib -lsmarheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine
-Wl,-z,muldefs -L/opt/SmartHeap_8.1/lib -lsmarheap

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

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Peak Other Flags (Continued)

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/NEC-Intel-ic10.1-INT-ia32-linux-flags.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/NEC-Intel-ic10.1-INT-ia32-linux-flags.xml>

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For other inquiries, please contact webmaster@spec.org.

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