SPEC® CFP2006 Result

Dell Inc.

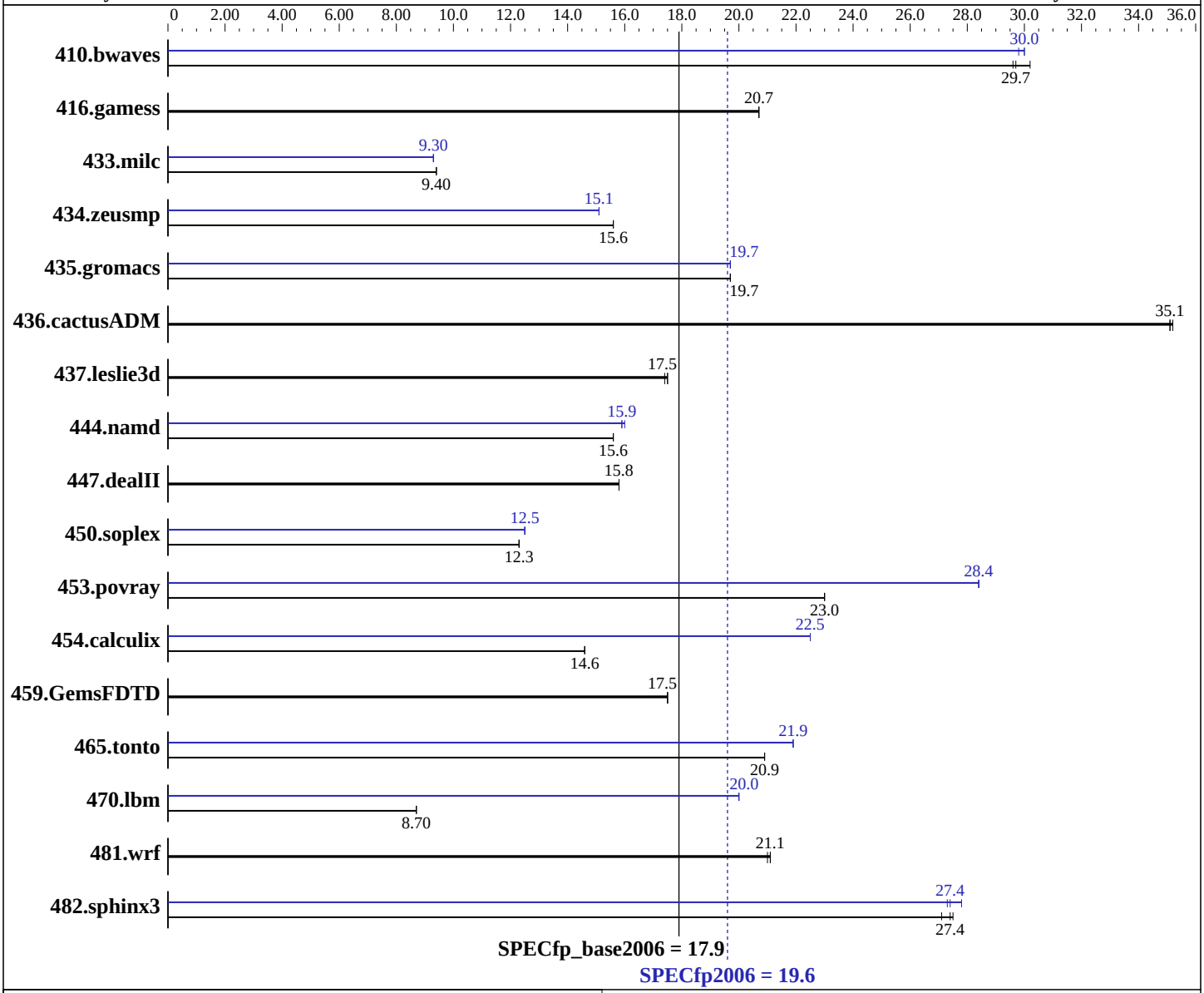
Dell Precision R5400 (Intel Xeon E5450, 3.00 GHz)

SPECfp®2006 = 19.6

SPECfp_base2006 = 17.9

CPU2006 license: 55
Test sponsor: Dell Inc.
Tested by: Dell Inc.

Test date: Jun-2008
Hardware Availability: Jul-2008
Software Availability: Mar-2008



Hardware

CPU Name:	Intel Xeon E5450
CPU Characteristics:	1333 MHz Bus Speed
CPU MHz:	3000
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	12 MB I+D on chip per chip, 6 MB shared / 2 cores

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Software

Operating System:	Windows Vista Ultimate (64-bit)
Compiler:	Intel C++ Compiler for Intel 64, Version 10.1 Build 20080312 Package ID: w_cc_p_10.1.021 Intel Visual Fortran Compiler for Intel 64, Version 10.0 Build 20080312 Package ID: w_fc_p_10.1.021 Microsoft Visual Studio 2005 SP1
Auto Parallel:	Yes
File System:	NTFS

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L3 Cache:	None	System State:	Default
Other Cache:	None	Base Pointers:	32/64-bit
Memory:	16 GB (4x4 GB 667 MHz CL5 FB-DIMM)	Peak Pointers:	32/64-bit
Disk Subsystem:	2 x 320 GB SATA 7200 RPM, RAID0	Other Software:	MicroQuill SmartHeap Library 8.1 for x64
Other Hardware:	None		

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	450	30.2	**458**	**29.7**	459	29.6	454	30.0	456	29.8	**454**	**30.0**
416.gamess	946	20.7	946	20.7	**946**	**20.7**	946	20.7	946	20.7	**946**	**20.7**
433.milc	**976**	**9.40**	976	9.40	976	9.40	985	9.30	984	9.30	**985**	**9.30**
434.zeusmp	**582**	**15.6**	582	15.6	583	15.6	601	15.1	601	15.1	**601**	**15.1**
435.gromacs	363	19.7	363	19.7	**363**	**19.7**	362	19.7	**362**	**19.7**	362	19.7
436.cactusADM	341	35.1	**340**	**35.1**	340	35.2	341	35.1	**340**	**35.1**	340	35.2
437.leslie3d	541	17.4	**538**	**17.5**	537	17.5	541	17.4	**538**	**17.5**	537	17.5
444.namd	514	15.6	514	15.6	**514**	**15.6**	503	15.9	**503**	**15.9**	503	16.0
447.dealII	723	15.8	**723**	**15.8**	723	15.8	723	15.8	**723**	**15.8**	723	15.8
450.soplex	681	12.3	677	12.3	**680**	**12.3**	667	12.5	**667**	**12.5**	668	12.5
453.povray	232	23.0	**232**	**23.0**	232	23.0	187	28.4	**187**	**28.4**	187	28.4
454.calculix	564	14.6	564	14.6	**564**	**14.6**	**366**	**22.5**	366	22.5	366	22.5
459.GemsFDTD	606	17.5	606	17.5	**606**	**17.5**	606	17.5	606	17.5	**606**	**17.5**
465.tonto	471	20.9	471	20.9	**471**	**20.9**	**450**	**21.9**	450	21.9	450	21.9
470.lbm	1581	8.70	**1580**	**8.70**	1579	8.70	687	20.0	689	20.0	**688**	**20.0**
481.wrf	**530**	**21.1**	530	21.1	532	21.0	**530**	**21.1**	530	21.1	532	21.0
482.sphinx3	**711**	**27.4**	718	27.1	708	27.5	**712**	**27.4**	702	27.8	714	27.3

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

```
Binaries were built on Windows Vista Ultimate (64-bit)
```

Base Compiler Invocation

C benchmarks:

```
icl -Qstd=c99
```

C++ benchmarks:

```
icl
```

Fortran benchmarks:

```
ifort
```

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Base Compiler Invocation (Continued)

Benchmarks using both Fortran and C:
icl -Qstd=c99 ifort

Base Portability Flags

410.bwaves: -DSPEC_CPU_P64
416.gamess: -DSPEC_CPU_P64
433.milc: -DSPEC_CPU_P64
434.zeusmp: -DSPEC_CPU_P64
435.gromacs: -DSPEC_CPU_P64
436.cactusADM: -DSPEC_CPU_P64 -Qlowercase /assume:underscore
437.leslie3d: -DSPEC_CPU_P64
444.namd: -DSPEC_CPU_P64 /TP
447.dealII: -DSPEC_CPU_P64 -DDEAL_II_MEMBER_VAR_SPECIALIZATION_BUG
450.soplex: -DSPEC_CPU_P64
453.povray: -DSPEC_CPU_P64 -DSPEC_CPU_WINDOWS_ICL
454.calculix: -DSPEC_CPU_P64 -DSPEC_CPU_NOZMODIFIER -Qlowercase
459.GemsFDTD: -DSPEC_CPU_P64
465.tonto: -DSPEC_CPU_P64
470.lbm: -DSPEC_CPU_P64
481.wrf: -DSPEC_CPU_P64 -DSPEC_CPU_WINDOWS_ICL
482.sphinx3: -DSPEC_CPU_P64

Base Optimization Flags

C benchmarks:
-fast -Qauto-ilp32 -Qparallel /F512000000 libguide40.lib
-link /FORCE:MULTIPLE

C++ benchmarks:
-fast -Qauto-ilp32 -Qparallel -Qcxx_features /F512000000 shlw64m.lib
libguide40.lib -link /FORCE:MULTIPLE

Fortran benchmarks:
-fast -Qparallel /F1000000000 libguide40.lib
-link /FORCE:MULTIPLE

Benchmarks using both Fortran and C:
-fast -Qauto-ilp32 -Qparallel /F1000000000 libguide40.lib
-link /FORCE:MULTIPLE

Peak Compiler Invocation

C benchmarks:
icl -Qstd=c99

C++ benchmarks:
icl

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icl -Qstd=c99 ifort

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qauto-ilp32 -Qunroll2 -Oa /F512000000 libguide40.lib -link /FORCE:MULTIPLE

470.lbm: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qauto-ilp32 -Qunroll2 -Qscalar-rep- -Qprefetch /F512000000 libguide40.lib -link /FORCE:MULTIPLE

482.sphinx3: -fast -Qauto-ilp32 -Qunroll2 /F512000000 libguide40.lib -link /FORCE:MULTIPLE

C++ benchmarks:

444.namd: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qauto-ilp32 -Oa -Qcxx_features /F512000000 shlw64m.lib libguide40.lib -link /FORCE:MULTIPLE

447.dealII: basepeak = yes

450.soplex: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qauto-ilp32 -Qparallel -Qcxx_features /F512000000 shlw64m.lib libguide40.lib -link /FORCE:MULTIPLE

453.povray: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qauto-ilp32 -Qunroll4 -Qansi-alias -Qcxx_features /F512000000 shlw64m.lib libguide40.lib -link /FORCE:MULTIPLE

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Peak Optimization Flags (Continued)

Fortran benchmarks:

410.bwaves: `-fast -Qauto-ilp32 -Qparallel -Qprefetch /F1000000000 libguide40.lib -link /FORCE:MULTIPLE`

416.gamess: `basepeak = yes`

434.zeusmp: `-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-QxT -O2 -Qprec-div- -Qunroll0 -Qscalar-rep- /F1000000000 libguide40.lib -link /FORCE:MULTIPLE`

437.leslie3d: `basepeak = yes`

459.GemsFDTD: `basepeak = yes`

465.tonto: `-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-fast -Qauto-ilp32 -Qunroll4 -Qauto /F1000000000 libguide40.lib -link /FORCE:MULTIPLE`

Benchmarks using both Fortran and C:

435.gromacs: `-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-fast -Qauto-ilp32 -Oa -Qprefetch /F1000000000 libguide40.lib -link /FORCE:MULTIPLE`

436.cactusADM: `basepeak = yes`

454.calculix: `-fast -Qauto-ilp32 -Qunroll-aggressive /F1000000000 libguide40.lib -link /FORCE:MULTIPLE`

481.wrf: `basepeak = yes`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/dell.ic10.1.windows.flags.20090713.00.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/dell.ic10.1.windows.flags.20090713.00.xml`

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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