



# SPEC® CFP2006 Result

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## NEC Corporation

Express5800/110Ri-1  
(Intel Xeon 3065)

SPECfp®2006 = 18.4

SPECfp\_base2006 = 17.3

CPU2006 license: 9006

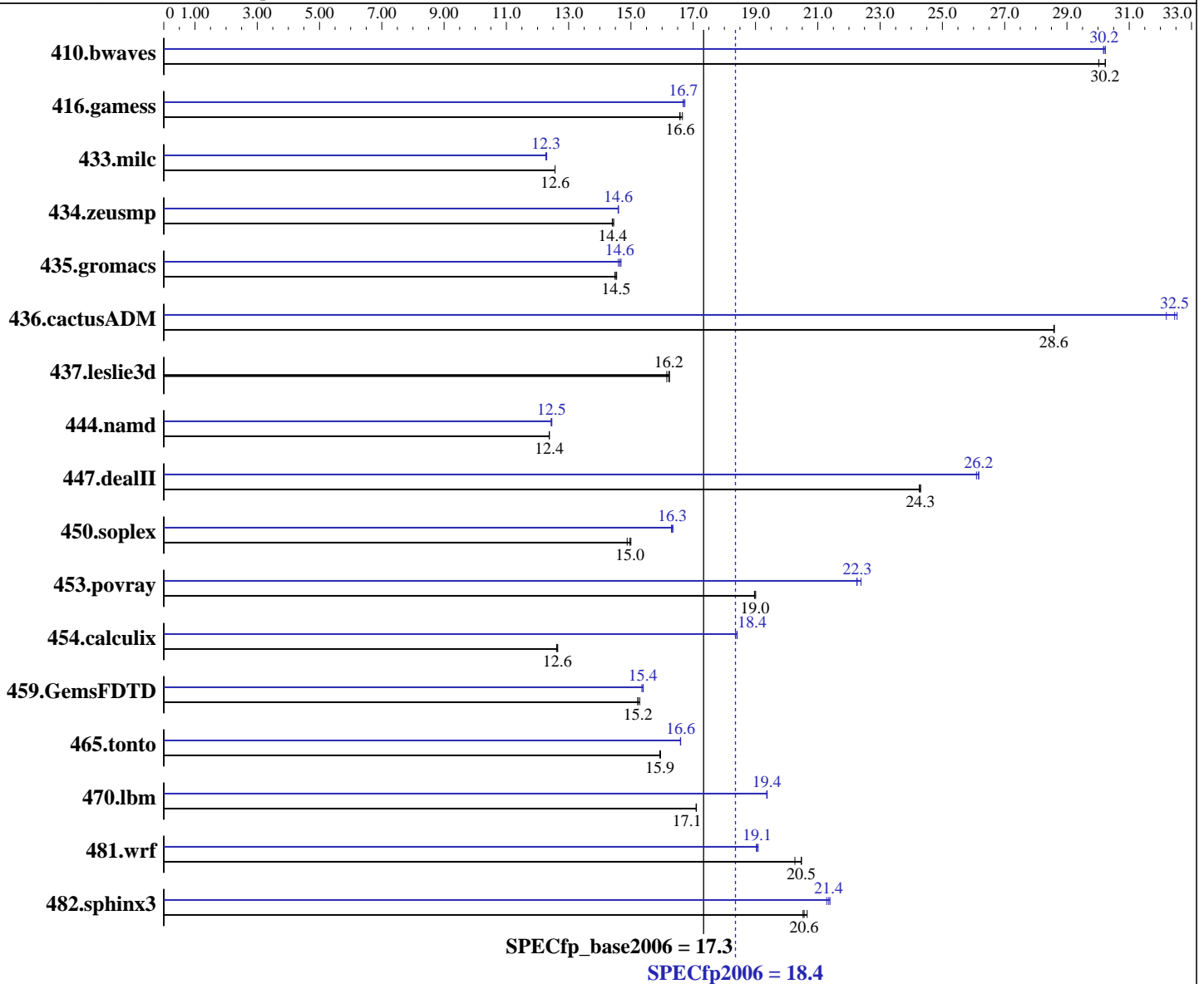
Test sponsor: NEC Corporation

Tested by: NEC Corporation

Test date: Jun-2008

Hardware Availability: Apr-2008

Software Availability: Nov-2007



**Hardware**

CPU Name: Intel Xeon 3065  
 CPU Characteristics: 2.33 GHz, 4 MB L2, 1333 MHz bus  
 CPU MHz: 2333  
 FPU: Integrated  
 CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip  
 CPU(s) orderable: 1 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 4 MB I+D on chip per chip

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**Software**

Operating System: SUSE Linux Enterprise Server 10 (x86\_64) SP1, Kernel 2.6.16.46-0.12-smp  
 Compiler: Intel C++ and Fortran Compiler for Linux version 10.1 Build 20070913 Package ID: l\_cc\_p\_10.1.008, l\_fc\_p\_10.1.008  
 Auto Parallel: Yes  
 File System: ReiserFS  
 System State: Run level 3 (multi-user)

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L3 Cache: None  
Other Cache: None  
Memory: 8 GB (4x2 GB PC2-6400E, 2 rank, CL6-6-6, ECC)  
Disk Subsystem: 1x80.0 GB SATAII, 7200RPM  
Other Hardware: None

Base Pointers: 64-bit  
Peak Pointers: 32/64-bit  
Other Software: binutils 2.17

## Results Table

| Benchmark     | Base       |             |            |             |             |             | Peak        |             |            |             |            |             |
|---------------|------------|-------------|------------|-------------|-------------|-------------|-------------|-------------|------------|-------------|------------|-------------|
|               | Seconds    | Ratio       | Seconds    | Ratio       | Seconds     | Ratio       | Seconds     | Ratio       | Seconds    | Ratio       | Seconds    | Ratio       |
| 410.bwaves    | 453        | 30.0        | <b>450</b> | <b>30.2</b> | 449         | 30.2        | 449         | 30.2        | <b>450</b> | <b>30.2</b> | 450        | 30.2        |
| 416.gamess    | 1176       | 16.7        | 1182       | 16.6        | <b>1181</b> | <b>16.6</b> | <b>1173</b> | <b>16.7</b> | 1174       | 16.7        | 1170       | 16.7        |
| 433.milc      | 731        | 12.6        | <b>731</b> | <b>12.6</b> | 731         | 12.6        | <b>748</b>  | <b>12.3</b> | 747        | 12.3        | 748        | 12.3        |
| 434.zeusmp    | <b>631</b> | <b>14.4</b> | 632        | 14.4        | 630         | 14.4        | 624         | 14.6        | <b>623</b> | <b>14.6</b> | 623        | 14.6        |
| 435.gromacs   | 493        | 14.5        | 491        | 14.5        | <b>492</b>  | <b>14.5</b> | 486         | 14.7        | <b>488</b> | <b>14.6</b> | 489        | 14.6        |
| 436.cactusADM | 418        | 28.6        | 418        | 28.6        | <b>418</b>  | <b>28.6</b> | <b>368</b>  | <b>32.5</b> | 371        | 32.2        | 367        | 32.5        |
| 437.leslie3d  | 582        | 16.2        | 579        | 16.2        | <b>580</b>  | <b>16.2</b> | 582         | 16.2        | 579        | 16.2        | <b>580</b> | <b>16.2</b> |
| 444.namd      | <b>648</b> | <b>12.4</b> | 648        | 12.4        | 648         | 12.4        | 645         | 12.4        | 644        | 12.5        | <b>644</b> | <b>12.5</b> |
| 447.dealII    | 471        | 24.3        | <b>471</b> | <b>24.3</b> | 472         | 24.3        | 437         | 26.2        | <b>437</b> | <b>26.2</b> | 438        | 26.1        |
| 450.soplex    | 561        | 14.9        | 556        | 15.0        | <b>557</b>  | <b>15.0</b> | 510         | 16.3        | <b>511</b> | <b>16.3</b> | 512        | 16.3        |
| 453.povray    | 280        | 19.0        | 281        | 19.0        | <b>280</b>  | <b>19.0</b> | 238         | 22.4        | <b>239</b> | <b>22.3</b> | 239        | 22.3        |
| 454.calculix  | 652        | 12.6        | <b>653</b> | <b>12.6</b> | 654         | 12.6        | 448         | 18.4        | 449        | 18.4        | <b>448</b> | <b>18.4</b> |
| 459.GemsFDTD  | 694        | 15.3        | <b>697</b> | <b>15.2</b> | 697         | 15.2        | 689         | 15.4        | <b>689</b> | <b>15.4</b> | 691        | 15.4        |
| 465.tonto     | <b>618</b> | <b>15.9</b> | 618        | 15.9        | 617         | 16.0        | 593         | 16.6        | 593        | 16.6        | <b>593</b> | <b>16.6</b> |
| 470.lbm       | 803        | 17.1        | 804        | 17.1        | <b>804</b>  | <b>17.1</b> | 710         | 19.4        | 709        | 19.4        | <b>709</b> | <b>19.4</b> |
| 481.wrf       | 551        | 20.3        | <b>546</b> | <b>20.5</b> | 545         | 20.5        | 587         | 19.0        | <b>586</b> | <b>19.1</b> | 585        | 19.1        |
| 482.sphinx3   | 950        | 20.5        | <b>948</b> | <b>20.6</b> | 943         | 20.7        | 911         | 21.4        | <b>913</b> | <b>21.4</b> | 915        | 21.3        |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run  
OMP\_NUM\_THREADS set to number of cores

## Platform Notes

Bios settings:  
Hardware Prefetcher: Enabled  
Adjacent Cache Line Prefetch: Enabled  
Intel SpeedStep Technology: Disabled



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**Hardware Availability:** Apr-2008

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## General Notes

All benchmarks compiled in 64-bit mode except 450.soplex, 470.lbm and 482.sphinx3, for peak, are compiled in 32-bit mode

## Base Compiler Invocation

C benchmarks:

icc

C++ benchmarks:

icpc

Fortran benchmarks:

ifort

Benchmarks using both Fortran and C:

icc ifort

## Base Portability Flags

```

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

```

## Base Optimization Flags

C benchmarks:

-fast -parallel

C++ benchmarks:

-fast -parallel

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## Base Optimization Flags (Continued)

Fortran benchmarks:

-fast -parallel

Benchmarks using both Fortran and C:

-fast -parallel

## Peak Compiler Invocation

C benchmarks (except as noted below):

/opt/intel/cc/10.1.008/bin/icc -L/opt/intel/cc/10.1.008/lib  
-I/opt/intel/cc/10.1.008/include

433.milc: icc

C++ benchmarks (except as noted below):

icpc

450.soplex: /opt/intel/cc/10.1.008/bin/icpc -L/opt/intel/cc/10.1.008/lib  
-I/opt/intel/cc/10.1.008/include

Fortran benchmarks:

ifort

Benchmarks using both Fortran and C:

icc ifort

## Peak Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.leslie3d: -DSPEC\_CPU\_LP64  
444.namd: -DSPEC\_CPU\_LP64  
447.dealII: -DSPEC\_CPU\_LP64  
453.povray: -DSPEC\_CPU\_LP64  
454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
459.GemsFDTD: -DSPEC\_CPU\_LP64  
465.tonto: -DSPEC\_CPU\_LP64  
481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX



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## Peak Optimization Flags

C benchmarks:

433.milc: -prof-gen(pass 1) -prof-use(pass 2) -fast -fno-alias  
-auto-ilp32

470.lbm: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2  
-scalar-rep- -prefetch -opt-malloc-options=3

482.sphinx3: -fast -unroll2

C++ benchmarks:

444.namd: -prof-gen(pass 1) -prof-use(pass 2) -fast -fno-alias  
-auto-ilp32

447.dealII: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2  
-ansi-alias -scalar-rep-

450.soplex: -prof-gen(pass 1) -prof-use(pass 2) -fast  
-opt-malloc-options=3

453.povray: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4  
-ansi-alias

Fortran benchmarks:

410.bwaves: -fast -prefetch -parallel

416.gamess: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2 -Ob0  
-ansi-alias -scalar-rep-

434.zeusmp: -prof-gen(pass 1) -prof-use(pass 2) -fast

437.leslie3d: basepeak = yes

459.GemsFDTD: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2 -Ob0  
-prefetch -parallel

465.tonto: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4 -auto

Benchmarks using both Fortran and C:

435.gromacs: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch  
-auto-ilp32

436.cactusADM: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2  
-prefetch -parallel -auto-ilp32

454.calculix: -fast -unroll-aggressive -auto-ilp32

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## Peak Optimization Flags (Continued)

481.wrf: -fast -parallel -prefetch -auto-ilp32

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/NEC-Intel-ic10.1-FP-intel64-linux-flags.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/NEC-Intel-ic10.1-FP-intel64-linux-flags.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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