



# SPEC® CINT2006 Result

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## Hewlett-Packard Company

ProLiant DL120 G5  
(2.66 GHz, Intel Xeon X3350)

**SPECint\_rate2006 = Not Run**

**SPECint\_rate\_base2006 = 61.1**

CPU2006 license: 3

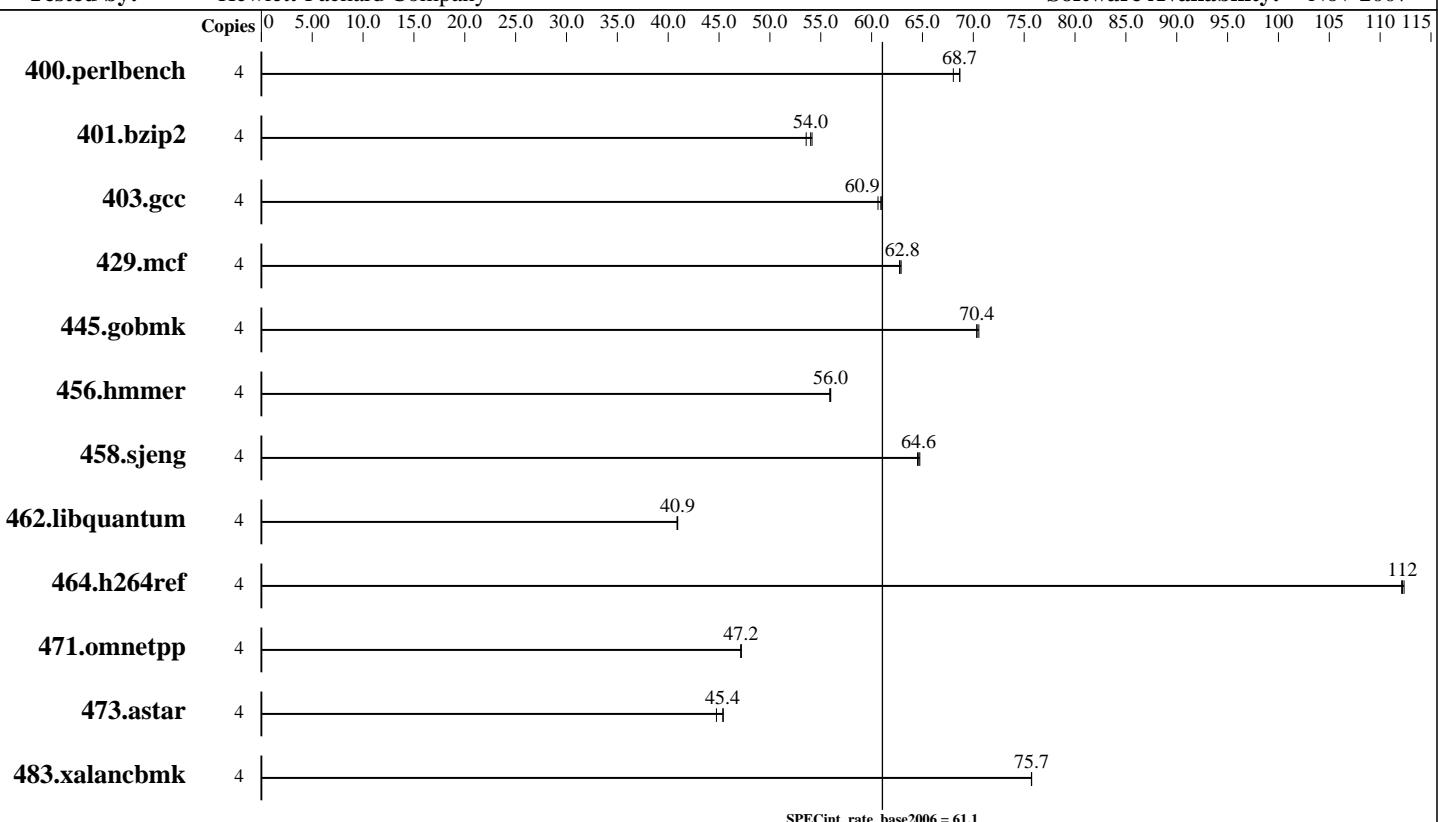
Test sponsor: Hewlett-Packard Company

Tested by: Hewlett-Packard Company

**Test date:** Jun-2008

**Hardware Availability:** Sep-2008

**Software Availability:** Nov-2007



### Hardware

CPU Name: Intel Xeon X3350  
CPU Characteristics: 2.66 GHz, 2x6 MB L2 shared, 1333 MHz system bus  
CPU MHz: 2666  
FPU: Integrated  
CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip  
CPU(s) orderable: 1 chip  
Primary Cache: 32 KB I + 32 KB D on chip per core  
Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores  
L3 Cache: None  
Other Cache: None  
Memory: 8 GB (4x2 GB PC2-6400E CL6)  
Disk Subsystem: 1x160 GB 7.2 K SATA  
Other Hardware: None

### Software

Operating System: SUSE Linux Enterprise Server 10 (x86\_64) SP1, Kernel 2.6.16.46-0.12-smp  
Compiler: Intel C++ Compiler 10.1 for Linux Build 20070913 Package ID: l\_cc\_p\_10.1.008  
Auto Parallel: No  
File System: ext2  
System State: Run level 3 (multi-user)  
Base Pointers: 32-bit  
Peak Pointers: Not Applicable  
Other Software: MicroQuill SmartHeap Library 8.1 binutils-2.17.50



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	574	68.0	569	68.7	<b>569</b>	<b>68.7</b>							
401.bzip2	4	713	54.1	<b>715</b>	<b>54.0</b>	721	53.6							
403.gcc	4	528	60.9	531	60.6	<b>529</b>	<b>60.9</b>							
429.mcf	4	580	62.9	<b>581</b>	<b>62.8</b>	581	62.7							
445.gobmk	4	595	70.5	<b>596</b>	<b>70.4</b>	597	70.3							
456.hmmer	4	<b>667</b>	<b>56.0</b>	668	55.9	667	56.0							
458.sjeng	4	748	64.7	750	64.5	<b>749</b>	<b>64.6</b>							
462.libquantum	4	2025	40.9	2028	40.9	<b>2025</b>	<b>40.9</b>							
464.h264ref	4	790	112	<b>789</b>	<b>112</b>	788	112							
471.omnetpp	4	530	47.1	530	47.2	<b>530</b>	<b>47.2</b>							
473.astar	4	<b>619</b>	<b>45.4</b>	628	44.7	618	45.4							
483.xalancbmk	4	365	75.7	<b>364</b>	<b>75.7</b>	364	75.7							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

## Platform Notes

BIOS configuration:

Hardware Prefetcher Disabled  
Adjacent Cache Line Prefetch Disabled

## Base Compiler Invocation

C benchmarks:

icc

C++ benchmarks:

icpc

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32

462.libquantum: -DSPEC\_CPU\_LINUX

483.xalancbmk: -DSPEC\_CPU\_LINUX



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## Base Optimization Flags

C benchmarks:

```
-fast -inline-calloc -opt-malloc-options=3
```

C++ benchmarks:

```
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs  
-L/cpu2006/SmartHeap_8.1/lib -lsmartheap
```

## Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/HP-Intel-ic10.1-linux-int-flags.20090713.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/HP-Intel-ic10.1-linux-int-flags.20090713.xml>

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For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.0.

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