



SPEC® CFP2006 Result

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IBM Corporation

SPECfp®_rate2006 = Not Run

IBM BladeCenter HS21 (Intel Xeon L5408)

SPECfp_rate_base2006 = 52.3

CPU2006 license: 11

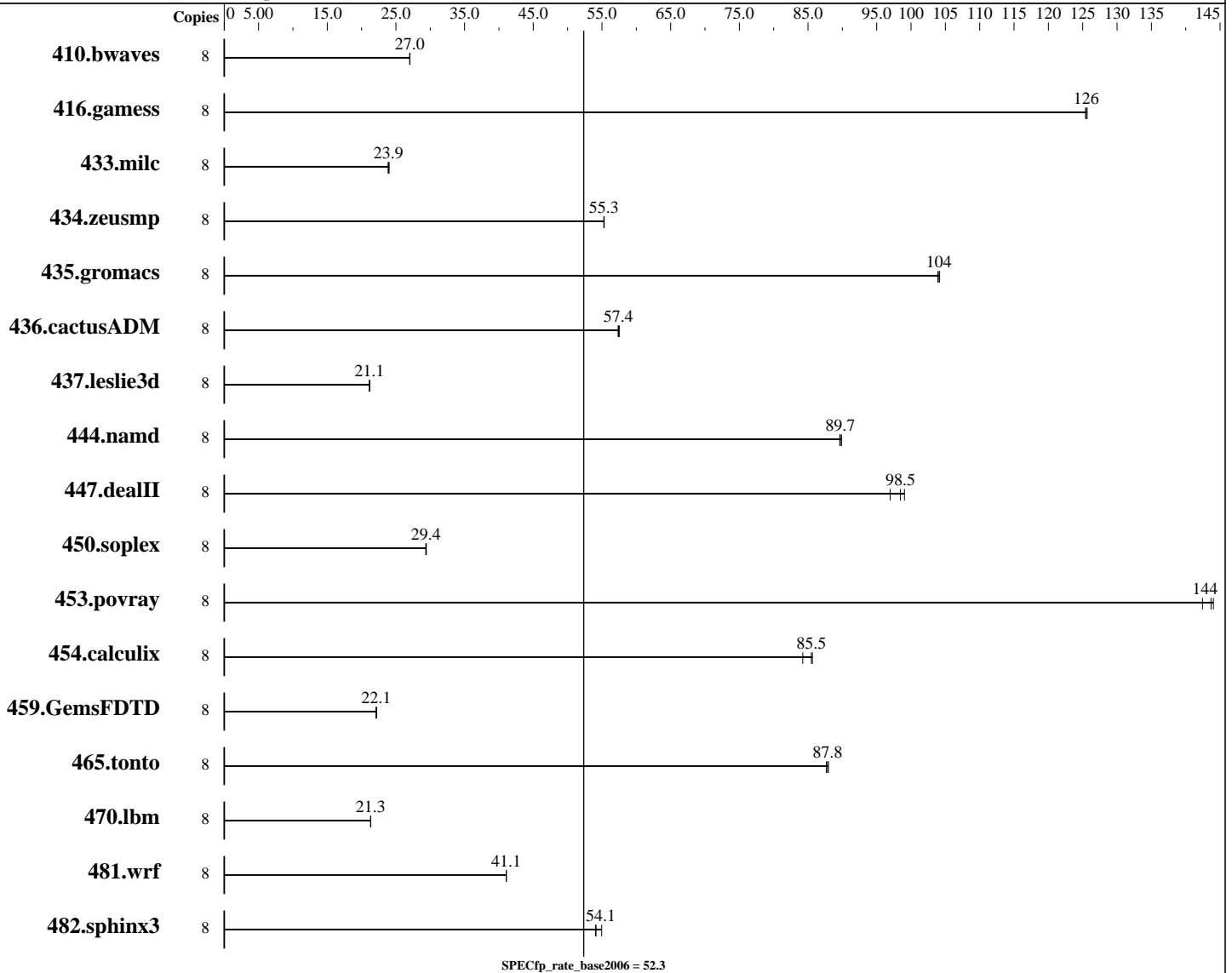
Test sponsor: IBM Corporation

Tested by: IBM Corporation

Test date: Apr-2008

Hardware Availability: Jul-2008

Software Availability: Nov-2007



Hardware

CPU Name: Intel Xeon L5408
 CPU Characteristics: 1066MHz system bus
 CPU MHz: 2133
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores

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Software

Operating System: SuSE Linux Enterprise Server 10 (x86_64), kernel 2.6.16.21-0.8-smp
 Compiler: Intel C++ and Fortran Compiler 10.1 for Linux Build 20070913 Package ID: l_cc_p_10.1.008, l_fc_p_10.1.008
 Auto Parallel: No
 File System: ReiserFS
 System State: Multi-user, run level 3
 Base Pointers: 64-bit

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L3 Cache: None
Other Cache: None
Memory: 16 GB (8 x 2 GB DDR2-5300F ECC)
Disk Subsystem: 1 x 36 GB SAS, 10000 RPM
Other Hardware: None

Peak Pointers: Not Applicable
Other Software: Binutils 2.17.50.0.15

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	8	4022	27.0	4023	27.0	4025	27.0							
416.gamess	8	1248	126	1249	125	1247	126							
433.milc	8	3077	23.9	3058	24.0	3072	23.9							
434.zeusmp	8	1317	55.3	1317	55.3	1316	55.3							
435.gromacs	8	548	104	550	104	549	104							
436.cactusADM	8	1662	57.5	1665	57.4	1667	57.4							
437.leslie3d	8	3550	21.2	3559	21.1	3563	21.1							
444.namd	8	715	89.7	716	89.6	714	89.9							
447.dealII	8	924	99.0	944	97.0	930	98.5							
450.soplex	8	2267	29.4	2270	29.4	2273	29.3							
453.povray	8	296	144	296	144	299	142							
454.calculix	8	771	85.6	784	84.2	772	85.5							
459.GemsFDTD	8	3827	22.2	3841	22.1	3835	22.1							
465.tonto	8	895	88.0	898	87.7	896	87.8							
470.lbm	8	5156	21.3	5154	21.3	5153	21.3							
481.wrf	8	2176	41.1	2176	41.1	2176	41.1							
482.sphinx3	8	2884	54.1	2837	55.0	2880	54.1							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

All benchmarks compiled in 64-bit mode
Hardware Sector Prefetch Disabled and Adjacent Sector Prefetch Disabled
taskset utility used to bind CPU(s) to processes

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

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Base Compiler Invocation (Continued)

Fortran benchmarks:

ifort

Benchmarks using both Fortran and C:

icc ifort

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
 416.gamess: -DSPEC_CPU_LP64
 433.milc: -DSPEC_CPU_LP64
 434.zeusmp: -DSPEC_CPU_LP64
 435.gromacs: -DSPEC_CPU_LP64 -nofor_main
 436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
 437.leslie3d: -DSPEC_CPU_LP64
 444.namd: -DSPEC_CPU_LP64
 447.dealII: -DSPEC_CPU_LP64
 450.soplex: -DSPEC_CPU_LP64
 453.povray: -DSPEC_CPU_LP64
 454.calculix: -DSPEC_CPU_LP64 -nofor_main
 459.GemsFDTD: -DSPEC_CPU_LP64
 465.tonto: -DSPEC_CPU_LP64
 470.lbm: -DSPEC_CPU_LP64
 481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
 482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:

-fast

C++ benchmarks:

-fast

Fortran benchmarks:

-fast

Benchmarks using both Fortran and C:

-fast

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic10.1-fp-linux64-revC.20090713.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic10.1-fp-linux64-revC.20090713.xml>



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For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.
Report generated on Tue Jul 22 17:46:50 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 25 June 2008.