SPEC® CFP2006 Result
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IBM Corporation

$SPECfp_{®}\_rate2006 =$ Not Run

IBM BladeCenter HS21 XM (Intel Xeon L5408)
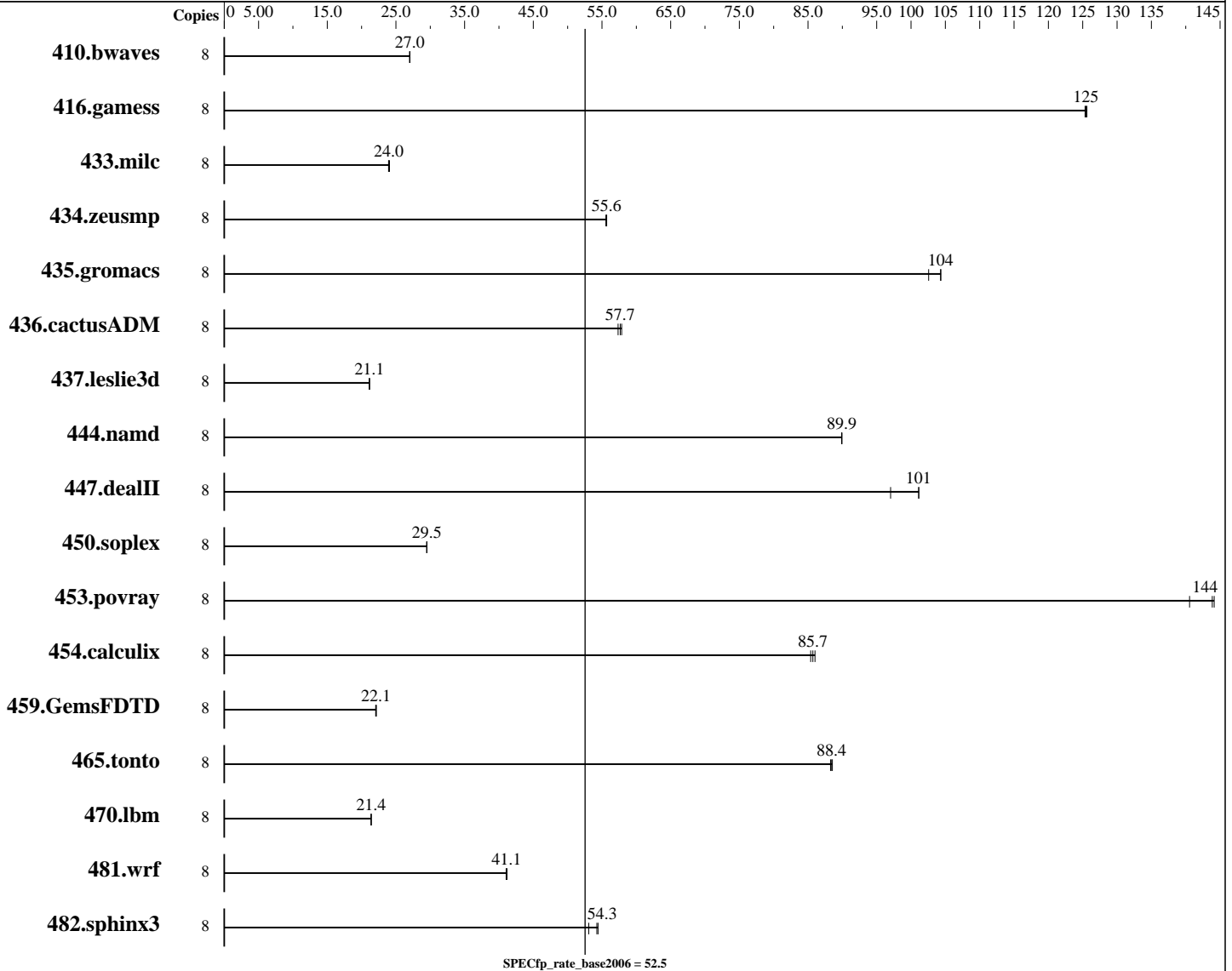
$SPECfp\_rate\_base2006 =$ 52.5

CPU2006 license: 11
Test sponsor: IBM Corporation
Tested by: IBM Corporation

Test date: May-2008
Hardware Availability: Jul-2008
Software Availability: Nov-2007

Benchmark	Copies	Result
410.bwaves	8	27.0
416.gamess	8	125
433.milc	8	24.0
434.zeusmp	8	55.6
435.gromacs	8	104
436.cactusADM	8	57.7
437.leslie3d	8	21.1
444.namd	8	89.9
447.dealII	8	101
450.soplex	8	29.5
453.povray	8	144
454.calculix	8	85.7
459.GemsFDTD	8	22.1
465.tonto	8	88.4
470.lbm	8	21.4
481.wrf	8	41.1
482.sphinx3	8	54.3

SPECfp_rate_base2006 = 52.5

Hardware

CPU Name: Intel Xeon L5408
CPU Characteristics: 1066MHz system bus
CPU MHz: 2133
FPU: Integrated
CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
CPU(s) orderable: 1,2 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores

Continued on next page

Software

Operating System: SuSE Linux Enterprise Server 10 (x86_64), kernel 2.6.16.21-0.8-smp
Compiler: Intel C++ and Fortran Compiler 10.1 for Linux Build 20070913 Package ID: l_cc_p_10.1.008, l_fc_p_10.1.008
Auto Parallel: No
File System: ReiserFS
System State: Multi-user, run level 3
Base Pointers: 64-bit

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L3 Cache:	None	Peak Pointers:	Not Applicable
Other Cache:	None	Other Software:	Binutils 2.17.50.0.15
Memory:	16 GB (8 x 2 GB DDR2-5300F ECC)		
Disk Subsystem:	1 x 36 GB SAS, 10000 RPM		
Other Hardware:	None		

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	8	4024	27.0	**4024**	**27.0**	4026	27.0							
416.gamess	8	1247	126	1250	125	**1248**	**125**							
433.milc	8	3070	23.9	3052	24.1	**3064**	**24.0**							
434.zeusmp	8	**1308**	**55.6**	1307	55.7	1310	55.6							
435.gromacs	8	557	103	**548**	**104**	547	104							
436.cactusADM	8	1652	57.9	**1658**	**57.7**	1667	57.3							
437.leslie3d	8	**3558**	**21.1**	3562	21.1	3553	21.2							
444.namd	8	713	90.0	714	89.9	**714**	**89.9**							
447.dealII	8	905	101	943	97.0	**905**	**101**							
450.soplex	8	**2262**	**29.5**	2263	29.5	2260	29.5							
453.povray	8	295	144	**296**	**144**	303	141							
454.calculix	8	**770**	**85.7**	767	86.0	773	85.4							
459.GemsFDTD	8	**3840**	**22.1**	3834	22.1	3849	22.1							
465.tonto	8	**890**	**88.4**	889	88.5	892	88.3							
470.lbm	8	5140	21.4	5138	21.4	**5138**	**21.4**							
481.wrf	8	2177	41.0	2170	41.2	**2174**	**41.1**							
482.sphinx3	8	2937	53.1	**2874**	**54.3**	2865	54.4							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

```
All benchmarks compiled in 64-bit mode
Hardware Sector Prefetch Disabled and Adjacent Sector Prefetch Disabled
taskset utility used to bind CPU(s) to processes
```

Base Compiler Invocation

C benchmarks:
```
icc
```

C++ benchmarks:
```
icpc
```

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Base Compiler Invocation (Continued)

Fortran benchmarks:
        ifort

Benchmarks using both Fortran and C:
        icc ifort

Base Portability Flags

        410.bwaves: -DSPEC_CPU_LP64
        416.gamess: -DSPEC_CPU_LP64
          433.milc: -DSPEC_CPU_LP64
        434.zeusmp: -DSPEC_CPU_LP64
       435.gromacs: -DSPEC_CPU_LP64 -nofor_main
      436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
        437.leslie3d: -DSPEC_CPU_LP64
          444.namd: -DSPEC_CPU_LP64
         447.dealII: -DSPEC_CPU_LP64
         450.soplex: -DSPEC_CPU_LP64
        453.povray: -DSPEC_CPU_LP64
       454.calculix: -DSPEC_CPU_LP64 -nofor_main
      459.GemsFDTD: -DSPEC_CPU_LP64
         465.tonto: -DSPEC_CPU_LP64
          470.lbm: -DSPEC_CPU_LP64
           481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
        482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
        -fast

C++ benchmarks:
        -fast

Fortran benchmarks:
        -fast

Benchmarks using both Fortran and C:
        -fast

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic10.1-fp-linux64-revC.20090713.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic10.1-fp-linux64-revC.20090713.xml

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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