



# SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## Itautec

### SPECint®\_rate2006 = 67.5

### Servidor Itautec LX211 (Intel Xeon X5450)

### SPECint\_rate\_base2006 = 59.5

CPU2006 license: 9001

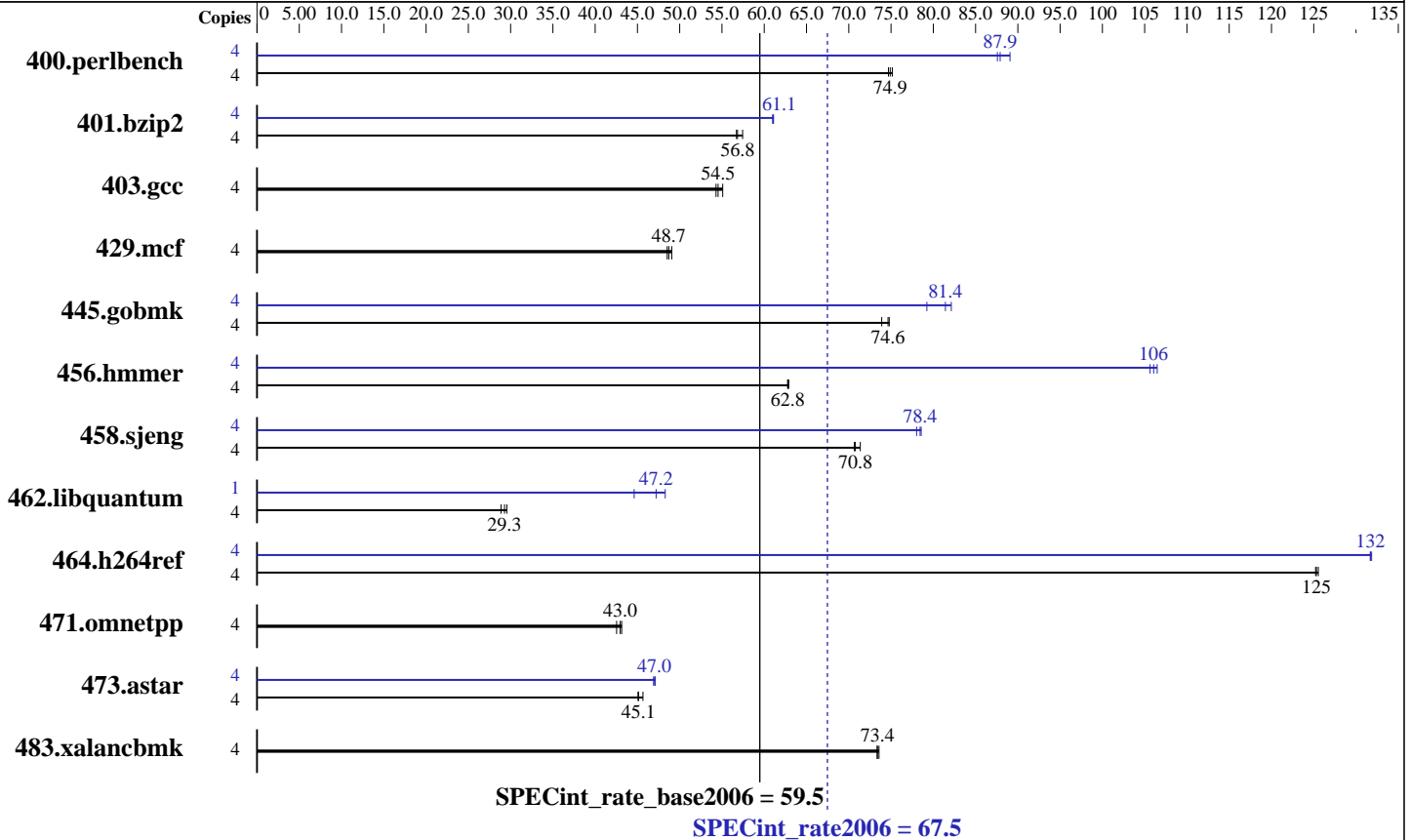
Test sponsor: Itautec

Tested by: Itautec

Test date: May-2008

Hardware Availability: Dec-2007

Software Availability: Jan-2008



#### Hardware

CPU Name: Intel Xeon X5450  
 CPU Characteristics:  
 CPU MHz: 3000  
 FPU: Integrated  
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores  
 L3 Cache: None  
 Other Cache: None  
 Memory: 12 GB (6 \* 2 GB PC2-5300 FBDIMM, CL-5-5-5, ECC)  
 Disk Subsystem: 1 x SCSI, 73GB, 10000 RPM  
 Other Hardware: None

#### Software

Operating System: SUSE Linux Enterprise Server 10 (x86\_64) SP1, Kernel 2.6.16.46-0.12-smp  
 Compiler: Intel C++ Compiler for Linux version 10.1 Build 20080112 Package ID: l\_cc\_p\_10.1.012  
 Auto Parallel: Yes  
 File System: ReiserFS  
 System State: Run Level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Binutils 2.17.10.50  
 MicroQuill SmartHeap V8.1



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Itaotec

SPECint\_rate2006 = 67.5

Servidor Itaotec LX211 (Intel Xeon X5450)

SPECint\_rate\_base2006 = 59.5

CPU2006 license: 9001  
Test sponsor: Itaotec  
Tested by: Itaotec

Test date: May-2008  
Hardware Availability: Dec-2007  
Software Availability: Jan-2008

## Results Table

Benchmark	Base						Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	<u>522</u>	<u>74.9</u>	523	74.7	520	75.1	4	439	89.1	<u>445</u>	<u>87.9</u>	446	87.6
401.bzip2	4	681	56.7	672	57.4	<u>679</u>	<u>56.8</u>	4	632	61.1	633	61.0	<u>632</u>	<u>61.1</u>
403.gcc	4	<u>590</u>	<u>54.5</u>	585	55.1	593	54.3	4	<u>590</u>	<u>54.5</u>	585	55.1	593	54.3
429.mcf	4	744	49.1	<u>749</u>	<u>48.7</u>	752	48.5	4	744	49.1	<u>749</u>	<u>48.7</u>	752	48.5
445.gobmk	4	<u>562</u>	<u>74.6</u>	568	73.9	561	74.8	4	511	82.1	530	79.2	<u>515</u>	<u>81.4</u>
456.hammer	4	<u>594</u>	<u>62.8</u>	593	62.9	595	62.8	4	<u>352</u>	<u>106</u>	350	106	353	106
458.sjeng	4	<u>684</u>	<u>70.8</u>	685	70.7	678	71.4	4	<u>617</u>	<u>78.4</u>	616	78.6	620	78.0
462.libquantum	4	2804	29.6	<u>2831</u>	<u>29.3</u>	2871	28.9	1	429	48.3	<u>439</u>	<u>47.2</u>	464	44.6
464.h264ref	4	705	126	<u>706</u>	<u>125</u>	707	125	4	671	132	<u>672</u>	<u>132</u>	672	132
471.omnetpp	4	580	43.1	<u>582</u>	<u>43.0</u>	588	42.5	4	580	43.1	<u>582</u>	<u>43.0</u>	588	42.5
473.astar	4	615	45.7	<u>622</u>	<u>45.1</u>	623	45.1	4	596	47.1	<u>597</u>	<u>47.0</u>	599	46.9
483.xalancbmk	4	376	73.4	<u>376</u>	<u>73.4</u>	375	73.6	4	376	73.4	<u>376</u>	<u>73.4</u>	375	73.6

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Compiler Invocation Notes

OMP\_NUM\_THREADS set to number of cores  
KMP\_STACK\_SIZE set to 64M  
KMP\_AFFINITY set to physical,0

## Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run.  
'/usr/bin/taskset' used to bind benchmark copies to processor, except for 462.libquantum at peak.

## Platform Notes

BIOS configuration:  
Hardware Prefetch Disabled

## Base Compiler Invocation

C benchmarks:  
icc

C++ benchmarks:  
icpc



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Itautec

SPECint\_rate2006 = 67.5

Servidor Itautec LX211 (Intel Xeon X5450)

SPECint\_rate\_base2006 = 59.5

CPU2006 license: 9001  
Test sponsor: Itautec  
Tested by: Itautec

Test date: May-2008  
Hardware Availability: Dec-2007  
Software Availability: Jan-2008

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:  
-fast -inline-calloc -opt-malloc-options=3  
  
C++ benchmarks:  
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs  
-L/opt/sh/SmartHeap\_8.1/lib -lsmartheap

## Base Other Flags

C benchmarks:  
403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):  
icc  
  
401.bzip2: /opt/intel/cce/10.1.012/bin/icc  
-L/opt/intel/cce/10.1.012/lib  
-I/opt/intel/cce/10.1.012/include  
  
456.hmmer: /opt/intel/cce/10.1.012/bin/icc  
-L/opt/intel/cce/10.1.012/lib  
-I/opt/intel/cce/10.1.012/include  
  
C++ benchmarks:  
icpc

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
401.bzip2: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LINUX

Continued on next page



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Itaotec

SPECint\_rate2006 = 67.5

Servidor Itaotec LX211 (Intel Xeon X5450)

SPECint\_rate\_base2006 = 59.5

CPU2006 license: 9001  
Test sponsor: Itaotec  
Tested by: Itaotec

Test date: May-2008  
Hardware Availability: Dec-2007  
Software Availability: Jan-2008

## Peak Portability Flags (Continued)

483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias  
-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch

403.gcc: basepeak = yes

429.mcf: basepeak = yes

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo  
-no-prec-div -ansi-alias

456.hmmer: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll4 -Ob0 -prefetch  
-opt-streaming-stores always -vec-guard-write  
-opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2  
-ansi-alias

C++ benchmarks:

471.omnetpp: basepeak = yes

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo  
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine  
-Wl,-z,muldefs -L/opt/sh/SmartHeap\_8.1/lib -lsmarheap

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Itautec

SPECint\_rate2006 = 67.5

Servidor Itautec LX211 (Intel Xeon X5450)

SPECint\_rate\_base2006 = 59.5

CPU2006 license: 9001

Test sponsor: Itautec

Tested by: Itautec

Test date: May-2008

Hardware Availability: Dec-2007

Software Availability: Jan-2008

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Itautec-ic10.1-INT-intel64-linux-flags.20090714.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Itautec-ic10.1-INT-intel64-linux-flags.20090714.xml>

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.0.1.  
Report generated on Tue Jul 22 16:57:43 2014 by SPEC CPU2006 PS/PDF formatter v6932.  
Originally published on 27 May 2008.