SPEC® CINT2006 Result
Copyright 2006-2014 Standard Performance Evaluation Corporation

Supermicro
Motherboard X7DWT

$SPECint^{®}\_rate2006 =$ 130
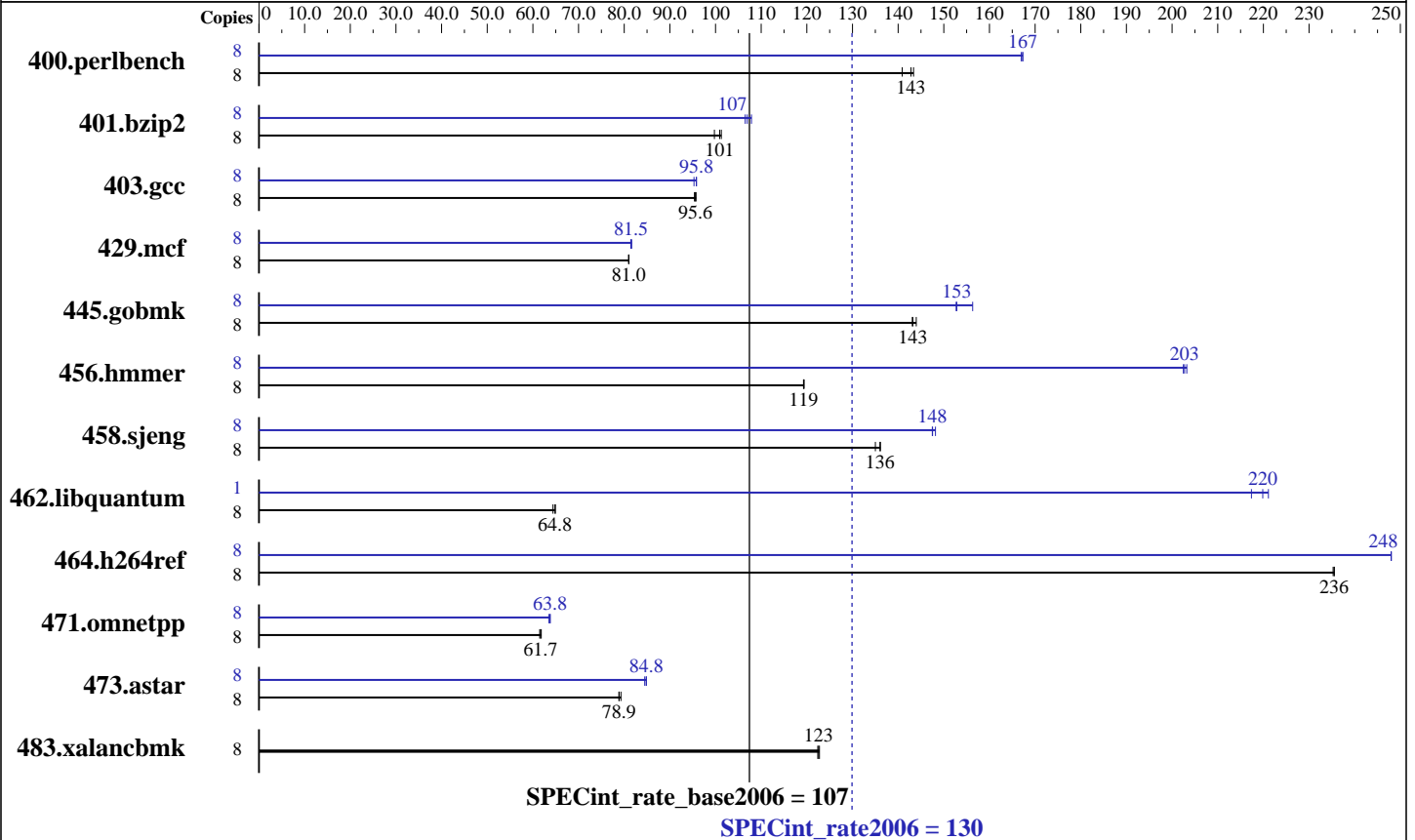
$SPECint\_rate\_base2006 =$ 107

CPU2006 license: 001176
Test sponsor: Supermicro
Tested by: Supermicro

Test date: Mar-2008
Hardware Availability: Nov-2007
Software Availability: Nov-2007

Copies scale: 0 10.0 20.0 30.0 40.0 50.0 60.0 70.0 80.0 90.0 100 110 120 130 140 150 160 170 180 190 200 210 220 230 250

Benchmark	Copies	Peak	Base
400.perlbench	8	167	143
401.bzip2	8	107	101
403.gcc	8	95.8	95.6
429.mcf	8	81.5	81.0
445.gobmk	8	153	143
456.hmmer	8	203	119
458.sjeng	8	148	136
462.libquantum	1 / 8	220	64.8
464.h264ref	8	248	236
471.omnetpp	8	63.8	61.7
473.astar	8	84.8	78.9
483.xalancbmk	8		123

SPECint_rate_base2006 = 107
SPECint_rate2006 = 130

Hardware

CPU Name:	Intel Xeon E5440
CPU Characteristics:	Quad Core, 2.83 GHz, 1333 MHz System Bus
CPU MHz:	2833
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	12 MB I+D on chip per chip, 6 MB shared / 2 cores
L3 Cache:	None
Other Cache:	None
Memory:	32 GB (8 X 4 GB ECC PC2-5300, CL5, FBDIMM)
Disk Subsystem:	SAMSUNG HD501LJ 500 GB SATA II, 7200 RPM
Other Hardware:	None

Software

Operating System:	64-Bit SUSE Linux Enterprise Server 10 SP1
Compiler:	Intel C++ and Fortran Compiler for Linux32 and Linux64 version 10.1 Build 20070725
Auto Parallel:	Yes
File System:	ReiserFS
System State:	Multi-user, run level 3
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	MicroQuill SmartHeap Library 8.1 Binutils 2.17.50.0.15

SPEC CINT2006 Result
Copyright 2006-2014 Standard Performance Evaluation Corporation

Supermicro Motherboard X7DWT

$SPECint\_rate2006 =$ 130

$SPECint\_rate\_base2006 =$ 107

CPU2006 license: 001176
Test sponsor: Supermicro
Tested by: Supermicro

Test date: Mar-2008
Hardware Availability: Nov-2007
Software Availability: Nov-2007

Results Table

Benchmark	\[Base\] Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	\[Peak\] Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	545	143	555	141	**547**	**143**	8	467	167	**467**	**167**	468	167
401.bzip2	8	774	99.8	**765**	**101**	762	101	8	725	106	716	108	**722**	**107**
403.gcc	8	**674**	**95.6**	675	95.4	673	95.7	8	672	95.8	676	95.3	**672**	**95.8**
429.mcf	8	901	81.0	900	81.0	**901**	**81.0**	8	**895**	**81.5**	894	81.6	896	81.4
445.gobmk	8	583	144	**586**	**143**	586	143	8	537	156	550	153	**549**	**153**
456.hmmer	8	626	119	625	119	**626**	**119**	8	369	202	**368**	**203**	367	203
458.sjeng	8	711	136	**712**	**136**	717	135	8	656	147	**656**	**148**	653	148
462.libquantum	8	2574	64.4	**2557**	**64.8**	2553	64.9	1	93.7	221	**94.2**	**220**	95.3	217
464.h264ref	8	752	235	**752**	**236**	752	236	8	714	248	714	248	**714**	**248**
471.omnetpp	8	**811**	**61.7**	809	61.8	813	61.5	8	788	63.5	784	63.8	**784**	**63.8**
473.astar	8	712	78.8	708	79.3	**712**	**78.9**	8	**662**	**84.8**	662	84.9	665	84.5
483.xalancbmk	8	451	122	**450**	**123**	450	123	8	451	122	**450**	**123**	450	123

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

    All benchmarks compiled in 32-bit mode except 401.bzip2 and 456.hmmer,
    for peak, which are compiled in 64-bit mode
    OMP_NUM_THREADS set to number of processors
    KMP_AFFINITY set to "physical,0"

Base Compiler Invocation

C benchmarks:
    icc

C++ benchmarks:
    icpc

Base Portability Flags

    400.perlbench: -DSPEC_CPU_LINUX_IA32
    462.libquantum: -DSPEC_CPU_LINUX
    483.xalancbmk: -DSPEC_CPU_LINUX

SPEC CINT2006 Result
Copyright 2006-2014 Standard Performance Evaluation Corporation

Supermicro Motherboard X7DWT

SPECint_rate2006 =   130

SPECint_rate_base2006 = 107

CPU2006 license: 001176
Test sponsor: Supermicro
Tested by: Supermicro

Test date: Mar-2008
Hardware Availability: Nov-2007
Software Availability: Nov-2007

Base Optimization Flags

C benchmarks:
```
-fast -inline-calloc -opt-malloc-options=3
```

C++ benchmarks:
```
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap
```

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks (except as noted below):
```
icc
```

```
401.bzip2: /home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/bin/icc
           -L/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/lib
           -I/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/include
```

```
456.hmmer: /home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/bin/icc
           -L/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/lib
           -I/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/include
```

C++ benchmarks:
```
icpc
```

Peak Portability Flags

```
400.perlbench: -DSPEC_CPU_LINUX_IA32
    401.bzip2: -DSPEC_CPU_LP64
    456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX
```

Peak Optimization Flags

C benchmarks:

Supermicro
Motherboard X7DWT

SPECint_rate2006 =   130

SPECint_rate_base2006 = 107

CPU2006 license: 001176

Test sponsor: Supermicro

Tested by: Supermicro

Test date: Mar-2008

Hardware Availability: Nov-2007

Software Availability: Nov-2007

Peak Optimization Flags (Continued)

400.perlbench: `-prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias`
`-prefetch`

401.bzip2: `-prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch`

403.gcc: `-fast -inline-calloc -opt-malloc-options=3`

429.mcf: `-fast -prefetch`

445.gobmk: `-prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo`
`-no-prec-div -ansi-alias`

456.hmmer: `-fast -unroll2 -ansi-alias -opt-multi-version-aggressive`

458.sjeng: `-prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4`

462.libquantum: `-fast -unroll4 -Ob0 -prefetch`
`-opt-streaming-stores always -vec-guard-write`
`-opt-malloc-options=3 -parallel -par-runtime-control`

464.h264ref: `-prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2`
`-ansi-alias`

C++ benchmarks:

471.omnetpp: `-prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo`
`-no-prec-div -ansi-alias -opt-ra-region-strategy=block`
`-Wl,-z,muldefs`
`-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap`

473.astar: `-prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo`
`-no-prec-div -ansi-alias -opt-ra-region-strategy=routine`
`-Wl,-z,muldefs`
`-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap`

483.xalancbmk: `basepeak = yes`

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic10.1-INT-ia32-linux-flags.20090713.00.html

SPEC CINT2006 Result
Copyright 2006-2014 Standard Performance Evaluation Corporation

Supermicro Motherboard X7DWT

SPECint_rate2006 = 130

SPECint_rate_base2006 = 107

You can also download the XML flags source by saving the following link:

http://www.spec.org/cpu2006/flags/Intel-ic10.1-INT-ia32-linux-flags.20090713.00.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.1.
Report generated on Tue Jul 22 16:57:04 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 27 May 2008.