



# SPEC® CINT2006 Result

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## Itautec

### SPECint®\_rate2006 = 92.8

### Servidor Itautec LX201 (Intel Xeon E5410)

### SPECint\_rate\_base2006 = 85.0

CPU2006 license: 9001

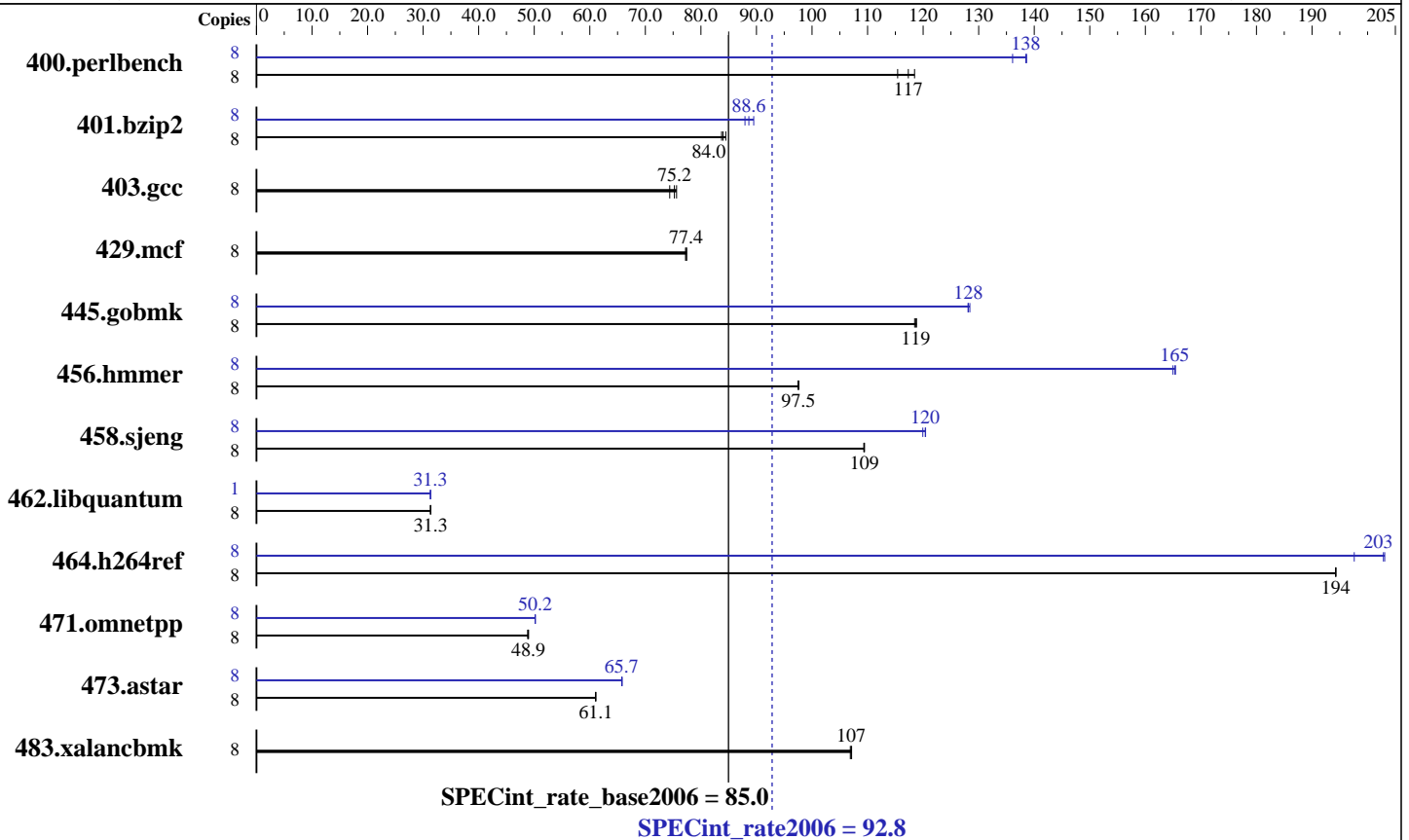
Test sponsor: Itautec

Tested by: Itautec

Test date: Apr-2008

Hardware Availability: Dec-2007

Software Availability: Jan-2008



### Hardware

CPU Name: Intel Xeon E5410  
 CPU Characteristics:  
 CPU MHz: 2330  
 FPU: Integrated  
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores  
 L3 Cache: None  
 Other Cache: None  
 Memory: 8 GB (8 \* 1 GB PC2-5300 FBDIMM, CL-5-5-5, ECC)  
 Disk Subsystem: 1 x SATA-2, 160GB, 7200 RPM  
 Other Hardware: None

### Software

Operating System: SUSE Linux Enterprise Server 10 (x86\_64) SP1, Kernel 2.6.16.46-0.12-smp  
 Compiler: Intel C++ Compiler for Linux version 10.1 Build 20080112 Package ID: l\_cc\_p\_10.1.012  
 Auto Parallel: Yes  
 File System: ReiserFS  
 System State: Run Level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Binutils 2.17.10.50  
 MicroQuill SmartHeap V8.1



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## Results Table

Benchmark	Base						Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	660	118	677	115	<b>666</b>	<b>117</b>	8	564	139	<b>564</b>	<b>138</b>	574	136
401.bzip2	8	<b>919</b>	<b>84.0</b>	914	84.5	922	83.7	8	863	89.5	<b>871</b>	<b>88.6</b>	878	87.9
403.gcc	8	852	75.6	<b>856</b>	<b>75.2</b>	866	74.4	8	852	75.6	<b>856</b>	<b>75.2</b>	866	74.4
429.mcf	8	942	77.4	<b>943</b>	<b>77.4</b>	945	77.2	8	942	77.4	<b>943</b>	<b>77.4</b>	945	77.2
445.gobmk	8	708	118	<b>707</b>	<b>119</b>	706	119	8	655	128	<b>655</b>	<b>128</b>	654	128
456.hammer	8	<b>765</b>	<b>97.5</b>	765	97.6	766	97.5	8	451	165	452	165	<b>451</b>	<b>165</b>
458.sjeng	8	886	109	<b>885</b>	<b>109</b>	885	109	8	<b>804</b>	<b>120</b>	804	120	807	120
462.libquantum	8	5288	31.3	<b>5289</b>	<b>31.3</b>	5290	31.3	1	662	31.3	<b>661</b>	<b>31.3</b>	661	31.3
464.h264ref	8	911	194	912	194	<b>911</b>	<b>194</b>	8	<b>873</b>	<b>203</b>	896	198	872	203
471.omnetpp	8	1024	48.8	1021	49.0	<b>1022</b>	<b>48.9</b>	8	996	50.2	<b>997</b>	<b>50.2</b>	997	50.2
473.astar	8	<b>920</b>	<b>61.1</b>	920	61.0	920	61.1	8	853	65.8	<b>854</b>	<b>65.7</b>	854	65.7
483.xalancbmk	8	515	107	516	107	<b>516</b>	<b>107</b>	8	515	107	516	107	<b>516</b>	<b>107</b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Compiler Invocation Notes

OMP\_NUM\_THREADS set to number of cores  
KMP\_STACK\_SIZE set to 64M  
KMP\_AFFINITY set to physical,0

## Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run.  
'/usr/bin/taskset' used to bind benchmark copies to processors.

## Platform Notes

BIOS configuration:  
Hardware Prefetch Enabled

## General Notes

## Base Compiler Invocation

C benchmarks:  
icc

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## Base Compiler Invocation (Continued)

C++ benchmarks:  
icpc

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:  
-fast -inline-calloc -opt-malloc-options=3  
C++ benchmarks:  
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs  
-L/opt/sh/SmartHeap\_8.1/lib -lsmartheap

## Base Other Flags

C benchmarks:  
403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):  
icc  
401.bzip2: /opt/intel/cce/10.1.012/bin/icc  
-L/opt/intel/cce/10.1.012/lib  
-I/opt/intel/cce/10.1.012/include  
456.hmmer: /opt/intel/cce/10.1.012/bin/icc  
-L/opt/intel/cce/10.1.012/lib  
-I/opt/intel/cce/10.1.012/include  
C++ benchmarks:  
icpc



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## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
401.bzip2: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias  
-prefetch  
401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch  
403.gcc: basepeak = yes  
429.mcf: basepeak = yes  
445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo  
-no-prec-div -ansi-alias  
456.hmmer: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive  
458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4  
462.libquantum: -fast -unroll4 -Ob0 -prefetch  
-opt-streaming-stores always -vec-guard-write  
-opt-malloc-options=3 -parallel -par-runtime-control  
464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2  
-ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo  
-no-prec-div -ansi-alias -opt-ra-region-strategy=block  
-Wl,-z,muldefs -L/opt/sh/SmartHeap\_8.1/lib -lsmarheap  
473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo  
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine  
-Wl,-z,muldefs -L/opt/sh/SmartHeap\_8.1/lib -lsmarheap  
483.xalancbmk: basepeak = yes



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## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Itautec-ic10.1-INT-intel64-linux-flags.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Itautec-ic10.1-INT-intel64-linux-flags.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.0.1.  
Report generated on Tue Jul 22 16:50:06 2014 by SPEC CPU2006 PS/PDF formatter v6932.  
Originally published on 29 April 2008.