



# SPEC® CINT2006 Result

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Itaotec

SPECint®\_rate2006 = 95.7

Servidor Itaotec LX201 (Intel Xeon E5410)

SPECint\_rate\_base2006 = 87.0

CPU2006 license: 9001

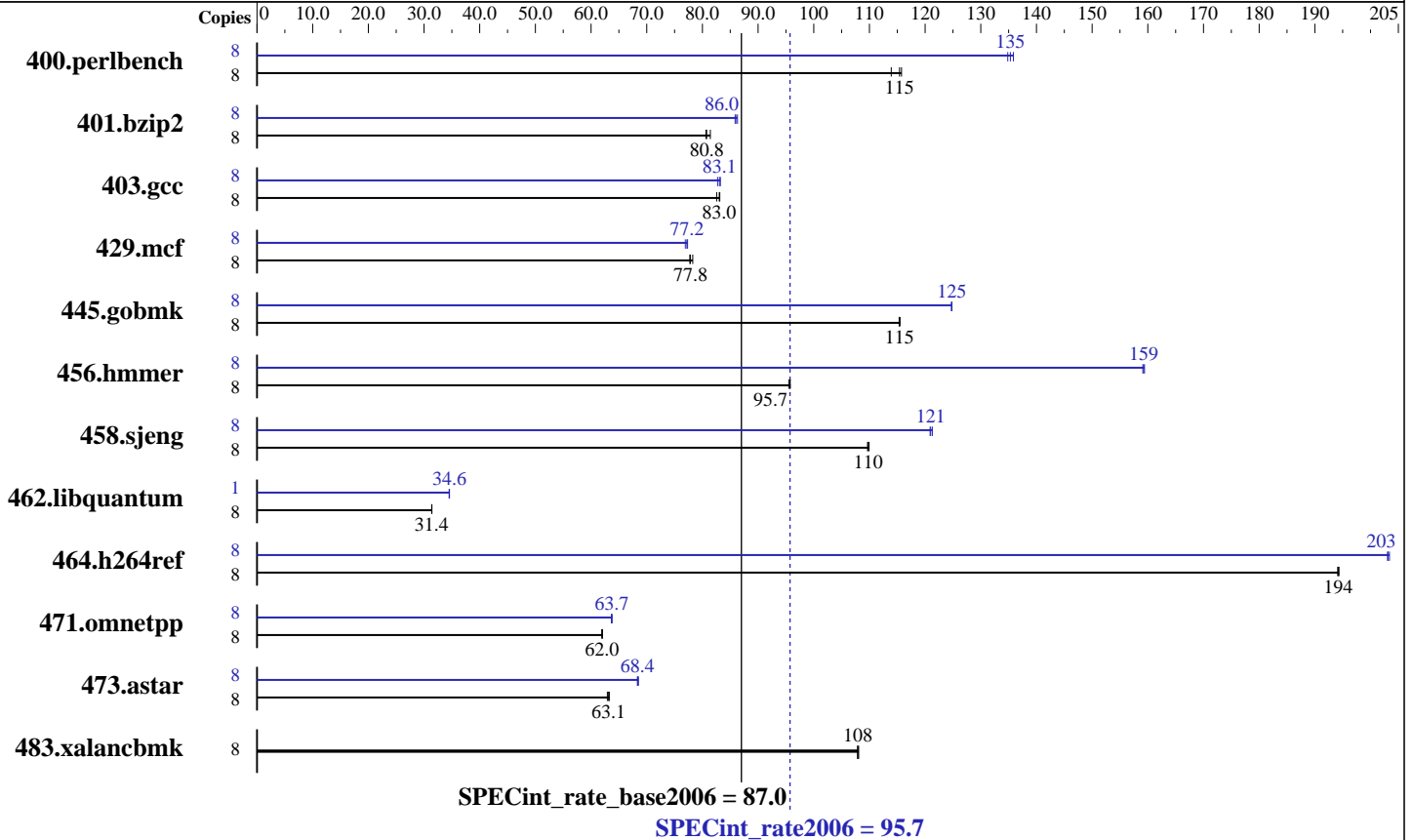
Test sponsor: Itaotec

Tested by: Itaotec

Test date: Apr-2008

Hardware Availability: Dec-2007

Software Availability: Jan-2008



## Hardware

CPU Name: Intel Xeon E5410  
 CPU Characteristics:  
 CPU MHz: 2330  
 FPU: Integrated  
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores  
 L3 Cache: None  
 Other Cache: None  
 Memory: 8 GB (8 \* 1 GB PC2-5300 FBDIMM, CL-5-5-5, ECC)  
 Disk Subsystem: 1 x SATA-2, 160GB, 7200 RPM  
 Other Hardware: None

## Software

Operating System: SUSE Linux Enterprise Server 10 (x86\_64) SP1, Kernel 2.6.16.46-0.12-smp  
 Compiler: Intel C++ Compiler for Linux version 10.1 Build 20080112 Package ID: l\_cc\_p\_10.1.012  
 Auto Parallel: Yes  
 File System: ReiserFS  
 System State: Run Level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Binutils 2.17.10.50  
 MicroQuill SmartHeap V8.1



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## Results Table

Benchmark	Base						Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	675	116	<b>677</b>	<b>115</b>	686	114	8	<b>578</b>	<b>135</b>	580	135	575	136
401.bzip2	8	948	81.4	957	80.6	<b>956</b>	<b>80.8</b>	8	895	86.3	<b>898</b>	<b>86.0</b>	899	85.9
403.gcc	8	775	83.1	<b>776</b>	<b>83.0</b>	780	82.6	8	<b>775</b>	<b>83.1</b>	778	82.7	774	83.2
429.mcf	8	<b>937</b>	<b>77.8</b>	939	77.7	932	78.3	8	943	77.3	<b>945</b>	<b>77.2</b>	948	76.9
445.gobmk	8	728	115	<b>727</b>	<b>115</b>	727	115	8	672	125	673	125	<b>672</b>	<b>125</b>
456.hammer	8	781	95.5	<b>780</b>	<b>95.7</b>	779	95.8	8	468	159	<b>469</b>	<b>159</b>	469	159
458.sjeng	8	<b>882</b>	<b>110</b>	883	110	880	110	8	798	121	801	121	<b>800</b>	<b>121</b>
462.libquantum	8	<b>5281</b>	<b>31.4</b>	5276	31.4	5282	31.4	1	<b>600</b>	<b>34.6</b>	601	34.5	599	34.6
464.h264ref	8	912	194	911	194	<b>912</b>	<b>194</b>	8	870	203	872	203	<b>872</b>	<b>203</b>
471.omnetpp	8	<b>807</b>	<b>62.0</b>	806	62.0	807	61.9	8	<b>785</b>	<b>63.7</b>	786	63.6	784	63.8
473.astar	8	888	63.3	892	62.9	<b>889</b>	<b>63.1</b>	8	820	68.5	<b>821</b>	<b>68.4</b>	822	68.3
483.xalancbmk	8	512	108	511	108	<b>511</b>	<b>108</b>	8	512	108	511	108	<b>511</b>	<b>108</b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Compiler Invocation Notes

OMP\_NUM\_THREADS set to number of cores  
KMP\_STACK\_SIZE set to 64M  
KMP\_AFFINITY set to physical,0

## Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run.  
'/usr/bin/taskset' used to bind benchmark copies to processors.

## Platform Notes

BIOS configuration:  
Hardware Prefetch Disabled

## General Notes

## Base Compiler Invocation

C benchmarks:  
icc

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## Base Compiler Invocation (Continued)

C++ benchmarks:  
icpc

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:  
-fast -inline-calloc -opt-malloc-options=3  
C++ benchmarks:  
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs  
-L/opt/sh/SmartHeap\_8.1/lib -lsmartheap

## Base Other Flags

C benchmarks:  
403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):  
icc  
401.bzip2: /opt/intel/cce/10.1.012/bin/icc  
-L/opt/intel/cce/10.1.012/lib  
-I/opt/intel/cce/10.1.012/include  
456.hmmer: /opt/intel/cce/10.1.012/bin/icc  
-L/opt/intel/cce/10.1.012/lib  
-I/opt/intel/cce/10.1.012/include  
C++ benchmarks:  
icpc



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## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
401.bzip2: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias  
-prefetch  
401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch  
403.gcc: -fast -inline-calloc -opt-malloc-options=3  
429.mcf: -fast -prefetch  
445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo  
-no-prec-div -ansi-alias  
456.hmmer: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive  
458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4  
462.libquantum: -fast -unroll4 -Ob0 -prefetch  
-opt-streaming-stores always -vec-guard-write  
-opt-malloc-options=3 -parallel -par-runtime-control  
464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2  
-ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo  
-no-prec-div -ansi-alias -opt-ra-region-strategy=block  
-Wl,-z,muldefs -L/opt/sh/SmartHeap\_8.1/lib -lsmarheap  
473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo  
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine  
-Wl,-z,muldefs -L/opt/sh/SmartHeap\_8.1/lib -lsmarheap  
483.xalancbmk: basepeak = yes



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## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Itautec-ic10.1-INT-intel64-linux-flags.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Itautec-ic10.1-INT-intel64-linux-flags.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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